

# **CHAKSHU GROVER**



#### ACADEMIC DETAILS

Year	Degree / Exam	Institute	GPA/Marks(%)
2014	B.Tech in Electrical Engineering	Indian Institute of Technology Delhi	7.747
2010	CBSE	Cambridge Court High School, Jaipur	93.4
2008	CBSE	Maria Assumpta Convent School, Kashipur	93.8

#### COURSES DONE

Data Structures , Artificial Intelligence , Computer Architecture , Operating Systems , Intro. To Analysis & Differential Equations , Probability & Stochastic Processes , Analysis & Design Of Algorithms .

# IIT DELHI THESIS

Title - Classification of Ethnicity, Age and Gender using Image Processing

Supervisor - Dr. Brejesh Lall

- Description → Detection and Extraction of faces from Image or Video and comparing to large database containing image of subjects of various races and both gender
  - Computation and Combination of Nearest neighbours from these feature sets to yeild hybrid classification and then employment of techniques like comparison of Color Histograms, Gabor Filter, Local Binary Pattern and Multiple Kernel Learning

### SCHOLASTIC ACHIEVEMENTS

- Summer Undergraduate Research Award: Received Summer Undergraduate Research Award (SURA)-2012
  under the aegis of Undergraduate Research Opportunity program of IITD
- ACADEMIC RECOGNITION: :
  - Secured AIR 230 in IIT JEE 2010 and AIR 271 in AIEEE 2010
  - Awarded CBSE Merit Certificate for being amongst top 0.1% candidates nationally in Mathematics Exam (scored 100/100 marks) ('07-08)
  - Awarded Certficate for being amongst top 1 % of candidates in National Standard Physics Olympiad ('10)
  - Secured All India Top 200 ranks in 12th National Science Olympiad and 3rd International Mathematics Olympiad ('10)

#### **INTERNSHIPS**

- Development and Implementation of Operating System for Networking Gear, Kenstel Communications (May, 2013 - July, 2013):
  - o Front End Development for network cameras using HTML, CSS, Javascript
  - o Boot Loader, Kernel Porting and Front End Development for Networking Gear using Lua

# **PROJECTS**

- Ciosts Web Designers (Dec, 2012 Jan, 2012): Collaborated and Contributed as a Web Designer for
  Ciosts leading a team of 5 and helping in development of website of AIET Jodhpur by primarily working on
  JavaScript end of website
- Auto car-parking system (Feb, 2012 May, 2012): Designed and Programmed an Automatic Car-Parking System on a FPGA board in Verilog using Xilinx
- Stock Market simulation (Aug, 2012 Aug, 2012): Developed a Simulated Stock market using highly efficient
  Java program with Parallel Processing to manage simulataneous transactions
- Gesture controlled Robot (July, 2013 Present): Developed a device to remotely control movement of a robot using hand gestures using Inertial Measurement Unit
- Artificial Intelligence (Jan, 2012 May, 2012): Implemented a modified connect 4 playing program which
  competes with other such programs using Intelligent Gaming Algorithms like modified MINIMAX and Alpha
  Beta Pruning algorithms

## TECHNICAL SKILLS

 Java, C++, Ardiono, 8086 assembly language, VHDL, Verilog, Python, HTML, CSS, Javascript, Lua, OpenGL, OpenCV, Microsoft Office and basic Android Development

#### OTHER INTERESTS

Reading Non-fiction, Avid interest in Adventure Sports, Football, Poker and card games