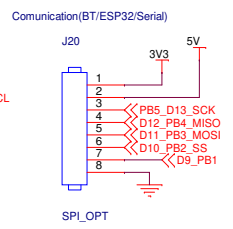
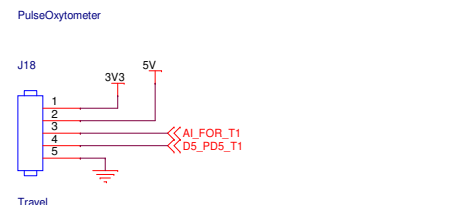
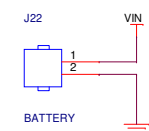


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The diagram shows two pin headers, J7 and J8, connected to a breadboard. J7 is a 15-pin header with the following connections:

- Pin 1: 3V3
- Pin 2: PB5_D13_SCK
- Pin 3: AREF
- Pin 4: A0_PC0_D14
- Pin 5: A1_PC1_D15
- Pin 6: A2_PC2_D16
- Pin 7: A3_PC3_D17
- Pin 8: A4_PC4_D18_SDA
- Pin 9: A5_PC5_D19_SCL
- Pin 10: A6_D20
- Pin 11: A7_D21
- Pin 12: 5V
- Pin 13: PC6_RESET
- Pin 14: VIN
- Pin 15: Ground

J8 is a 15-pin header with the following connections:

- Pin 1: D12_PB4_MISO
- Pin 2: D11_PB3_MOSI
- Pin 3: D10_PB2_SS
- Pin 4: D9_PB1
- Pin 5: D8_PB0
- Pin 6: D7_PD7
- Pin 7: D6_PD6
- Pin 8: D5_PD5_T1
- Pin 9: D4_PD4_T0
- Pin 10: D3_PD3
- Pin 11: D2_PD2
- Pin 12: Ground
- Pin 13: RESET_PC6
- Pin 14: RX_PD1
- Pin 15: TX_PD0

J10

1 IOREF
2 X
3 << PC6_RESET
4 3V3
5 5V
6
7 VIN
8

HEADER 8

J9

1 D18_PC5_SCL
2 D18_PC4_SDA
3
4
5 PB5_D13_SCK
6 D12_PB4_MISO
7 D11_PB3_MOSI
8 D10_PB2_SS
9 D9_PB1
10 D8_PB0

HEADER 10

J12

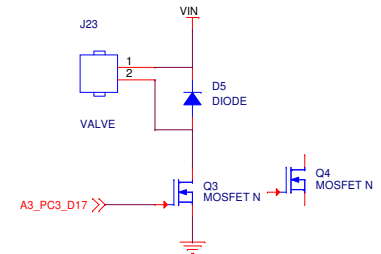
1 A0_PC0_D14
2 A1_PC1_D15
3 A2_PC2_D16
4 A3_PC3_D17
5 A4_PC4_D18_SDA
6 A5_PC5_D19_SCL

HEADER 6

J11

1 D7_PD7
2 D6_PD6
3 D5_PD5_T1
4 D4_PD4_T0
5 D3_PD3
6 D2_PD2
7 RX_PD1
8 TX_PD0

HEADER 8



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| Title <Title> | | | |
| Size B | Document Number <Doc> | | Rev <Rev/Code> |
| Date: Wednesday, March 25, 2020 Sheet 1 of 1 | | | |

