Applied Parallel Computing With Python

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PyCON 2013 Santa Clara, CA March 14, 2013

Bio (Minesh)

```
'97-'05 Developed Commercial Serial Parallel Enterprise Software still used by Hardware Engineers

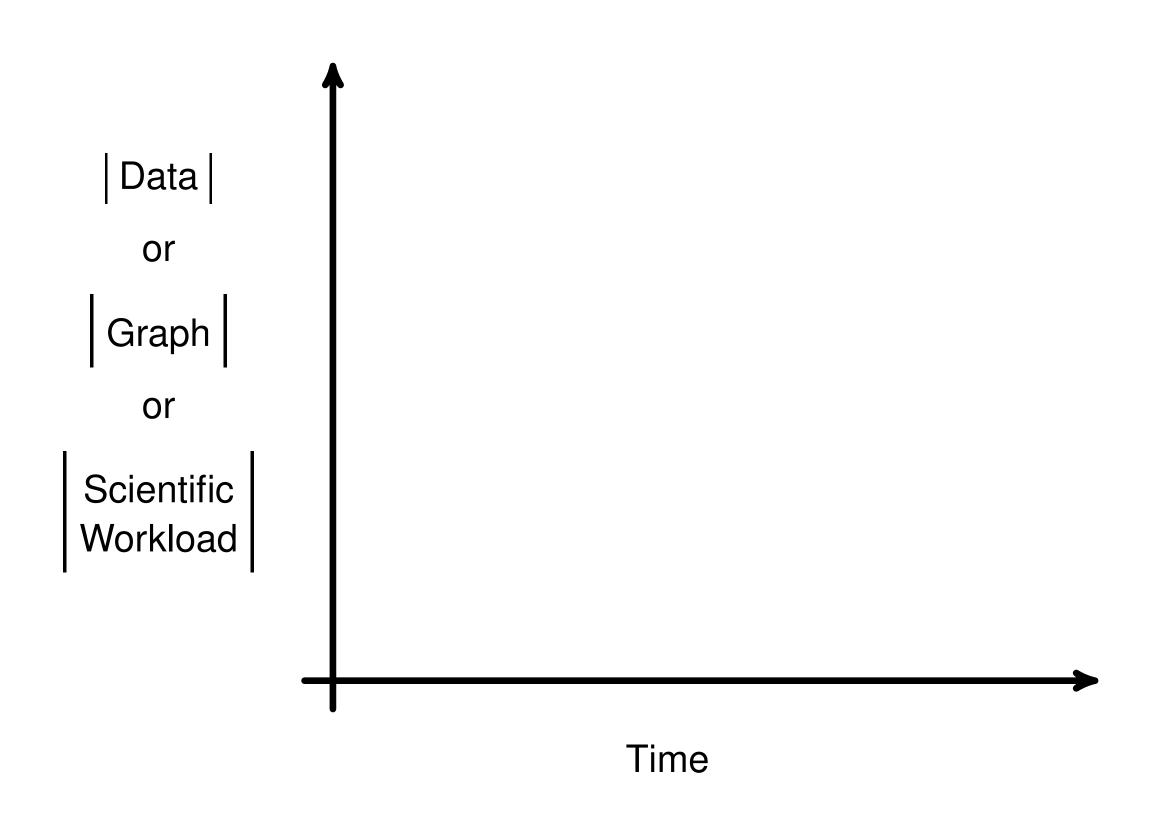
'06- MBA Sciences, Inc
Self-funded Engineer/CTO/Founder → SPM.Python + Consulting Services

Disruptive Technology
Supercomputing Conference 2010
GTC 2012
```

Footnote on the term ...

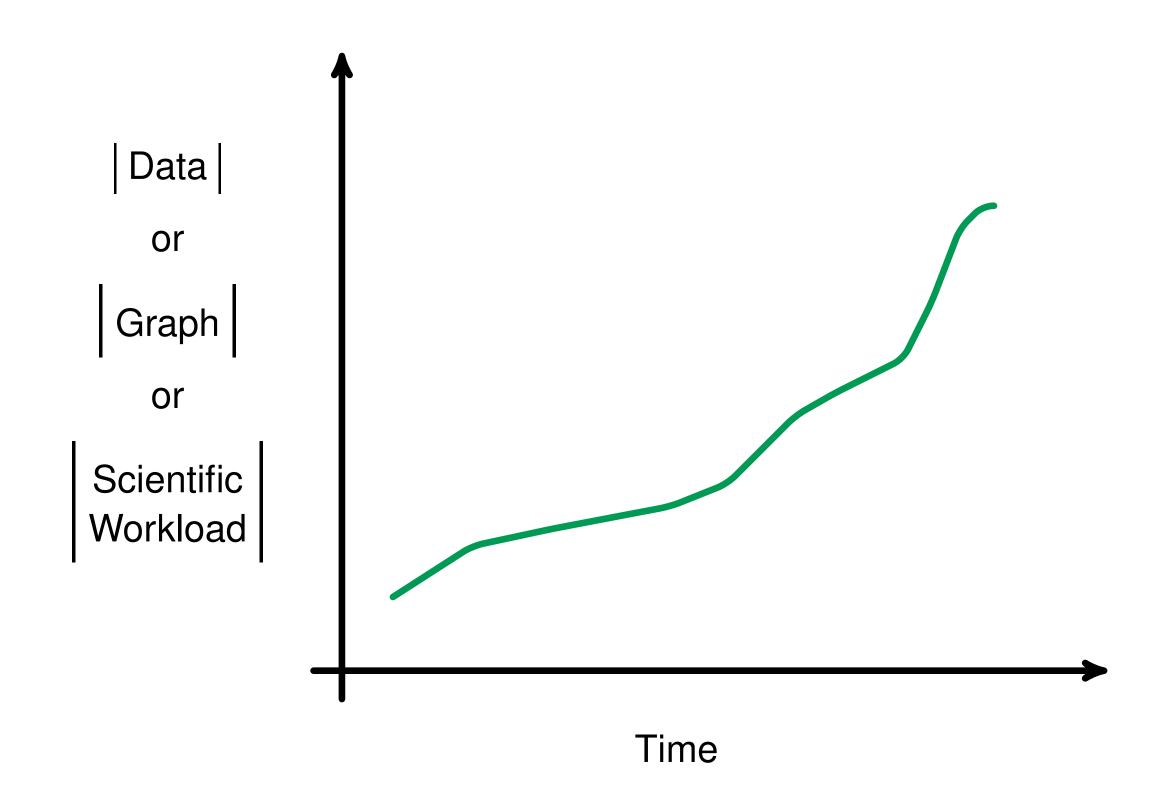
"Exploiting Parallelism"

Robust Parallelism: Why?



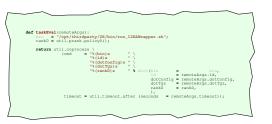
Robust Fault tolerant

Parallelism: Why?

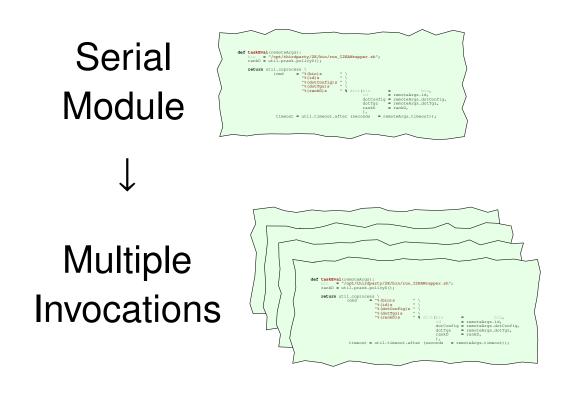


Robust Fault tolerant Parallelism: How (Take I)?

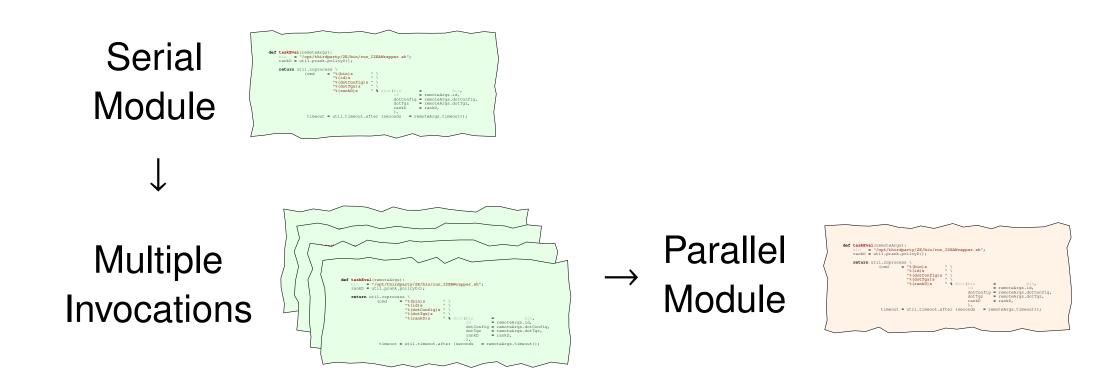
Serial Module



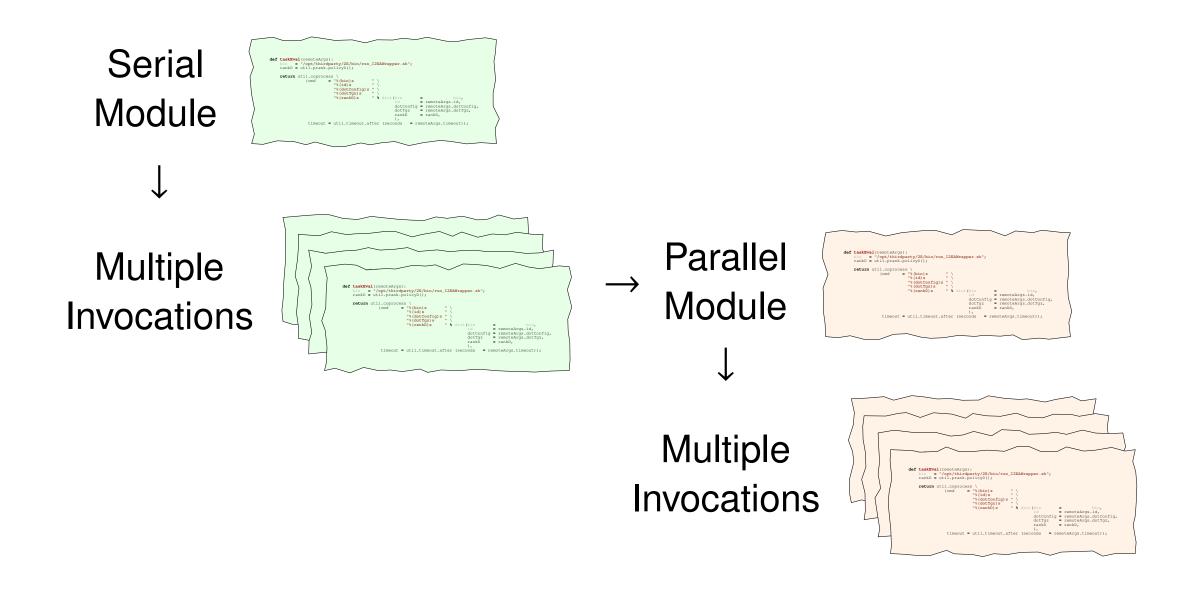
Robust Fault tolerant



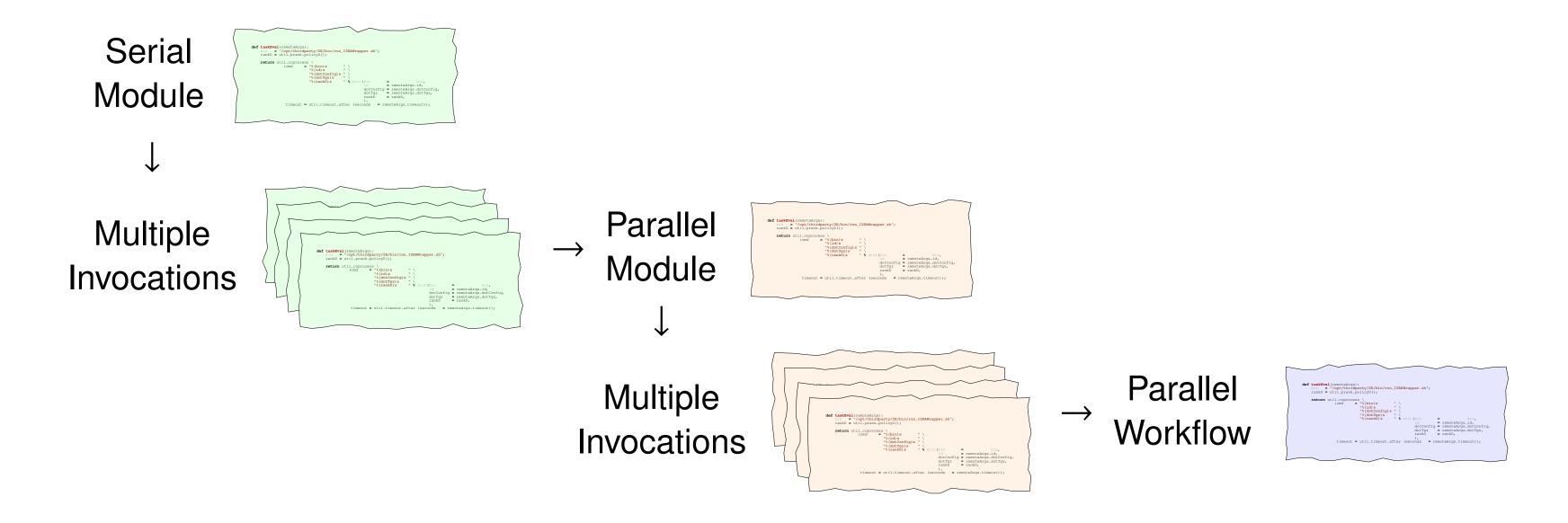
Robust Fault tolerant



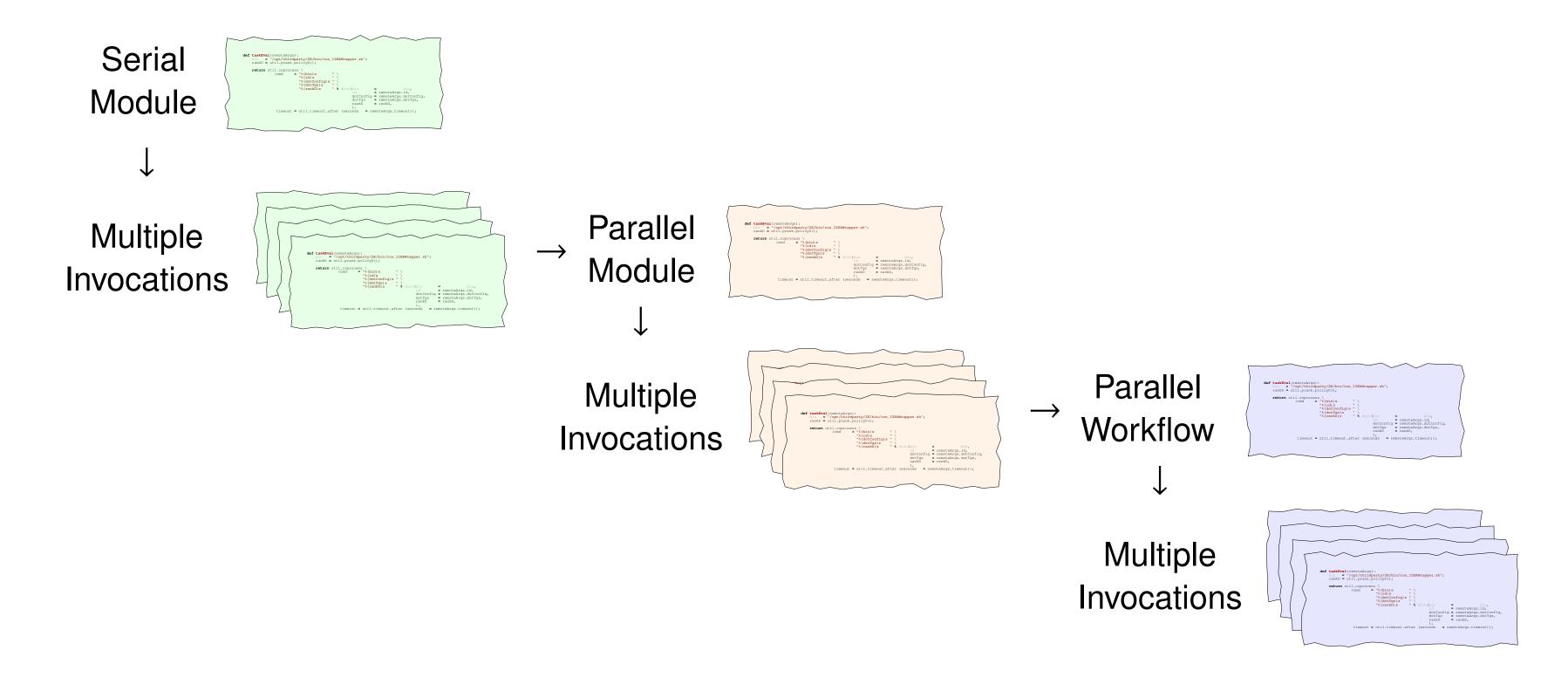
Robust Fault tolerant



Robust Fault tolerant



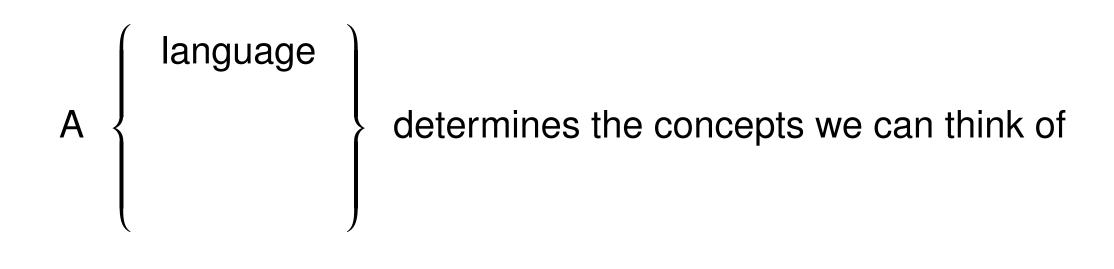
Robust Fault tolerant



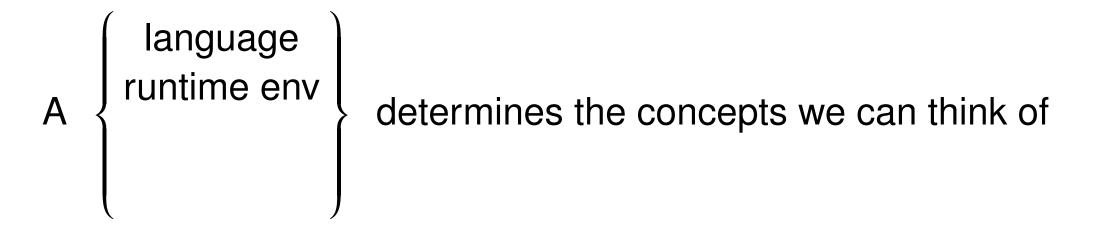
Robust Fault tolerant Parallelism: How (Take II)?

A language determines the concepts we can think of - Benjamin Worf

Robust Parallelism: How (Take II)?



Robust Parallelism: How (Take II)?



Robust Fault tolerant Parallelism: How (Take II)?

language runtime env framework

determines the concepts we can think of

Robust Parallelism: How (Take II)?

language runtime env framework library

determines the concepts we can think of

Preamble: "The Big Picture"

Question: Is exploiting parallelism $\left\{\begin{array}{l} \text{easy} \\ \text{hard} \end{array}\right\}$?

Preamble: "The Big Picture"

What makes

Question: \downarrow s exploiting parallelism $\left\{\begin{array}{l} easy \\ hard \end{array}\right\}$?



Copyright 1994, The UNIX-HATERS Handbook

Preamble: "The Big Picture"

What Makes

Question: Is exploiting parallelism {



Copyright 1994, The UNIX-HATERS Handbook

Supposition: The gap between developer's intent and API of PET (parallel enabling technologies) ...

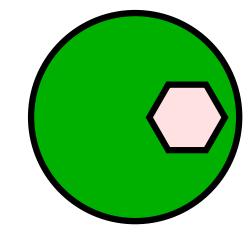
Preamble: "Parallel Enabling Technologies"

Means to the end

Bottom-up

OpenMPI OpenMP CUDA OpenGL

- Maximum flexibility
- Maximum headaches
- Must implement fault tolerance



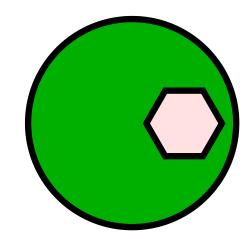
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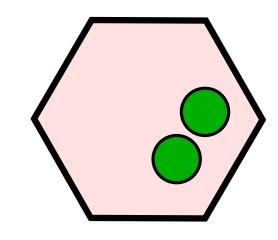
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Top-down

Hadoop Goldenorb GraphLab DISCO

- Limited flexibility
- Fewer headaches
- Fault tolerance is inherited



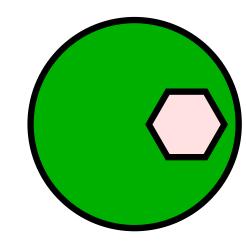
Preamble: "Parallel Enabling Technologies"

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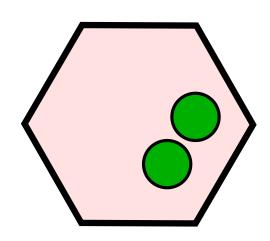
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Top-down

Hadoop Goldenorb GraphLab DISCO

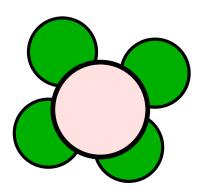
- Limited flexibility
- Fewer headaches
- Fault tolerance is inherited



Self-contained environment

SPM.Python

- Maximum flexibility
- Fewest headaches
- Fault tolerance is inherited



Preamble: "Exploiting Parallelism"

Parallelism: The management of a collection of serial tasks

Management: The policies by which:

- tasks are scheduled,
- premature terminations are handled,
- preemptive support is provided,
- communication primitives are enabled/disabled, and
- the manner in which resources are obtained and released

Serial Tasks: Are classified in terms of either:

- Coarse Grain ... where tasks may not communicate prior to conclusion, or
- Fine Grain ... where tasks may communicate prior to conclusion.

Preamble: "Exploiting Parallelism"

Parallelism: The management of a collection of serial tasks

Management: The policies by which:

- tasks are scheduled,
- premature terminations are handled,
- preemptive support is provided.

Management policies codify how serial tasks are to be managed ... independent of what they may be and released

Serial Tasks: Are classified in terms of either:

- Coarse Grain ... where tasks may not communicate prior to conclusion, or
- Fine Grain ... where tasks may communicate prior to conclusion.

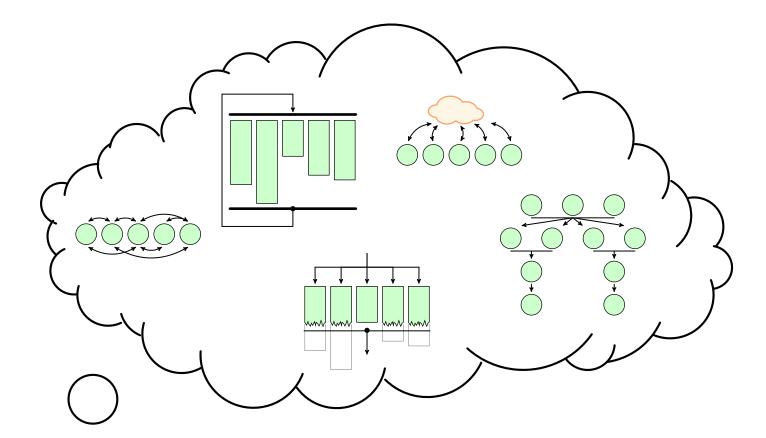
management:

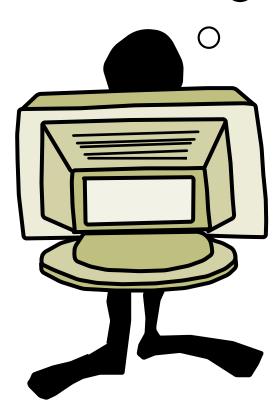
... a more challenging facet of [parallel] software engineering ...

- The Future of Computing Performance: Game Over or Next Level?

National Academy of Sciences, 2011







```
def taskEval(remoteArgs):
    return util.coprocess \
                       = "%(bin)s " \
              (cmd
                          "%(v)s "\
                          "-c %(c)s " \
                          "-d %(d)s " \
                          "-s %(s)s " \
                          "-a %(a)s " \
                          "-o %(o)s " % dict(bin =
                                                           remoteArgs.bin,
                                                 = { True : '-v',
                                                     False: '',
None: '',
                                                          [ remoteArgs.v ],
                                                          remoteArgs.c,
                                             С
                                                           remoteArgs.d,
                                                           remoteArgs.s,
                                                            remoteArgs.a,
                                                            remoteArgs.o,
               timeout = util.timeout.after(seconds = remoteArgs.timeout));
```

Module (Parallel)

Parallel Workflow

```
def taskEval(remoteArgs):
          = "/opt/thirdparty/2E/bin/run_I2EAWrapper.sh";
    rankD = util.prank.policyD();
    return util.coprocess \
                       = "% (bin)s
              (cmd
                         "%(id)s
                         "% (dotConfig)s
                         "% (dotTgz)s
                         "% (rankD)s
                                         " % dict (bin
                                                                          bin,
                                                            = remoteArgs.id,
                                                  id
                                                  dotConfig = remoteArgs.dotConfig,
                                                  dotTgz
                                                            = remoteArgs.dotTgz,
                                                            = rankD,
                                                  rankD
               timeout = util.timeout.after (seconds
                                                        = remoteArgs.timeout));
```

Module (Serial)

Module (Parallel)

```
def main(pool, env):
    taskSubmit (env
                                    = env) \
   .managerEval(pool
                                    = pool,
               timeoutWaitForSpokes = 2,  # Secs
               timeoutExecution
                                    = 300, # Secs
    if (terminateEarly):
       raise Exception("phaseA failed");
@grainCoarseSingleton.pclosure
def taskSubmit():
    return stdlib.cache(# Core options
                        bin = '...',
                           = True,
                           = "/nfs/expt100000/dotCConfig.json",
                           = "/nfs/expt100000/dotD",
                           = "/nfs/expt100000/dotSConfig.json",
                           = "/nfs/expt100000/dotAConfig.json",
                           = "/nfs/expt100000/output",
                        # Meta options
                        timeout = 300, # Secs
                        label = "phaseA (exec)",
                               = env);
                        env
```

Parallel Workflow

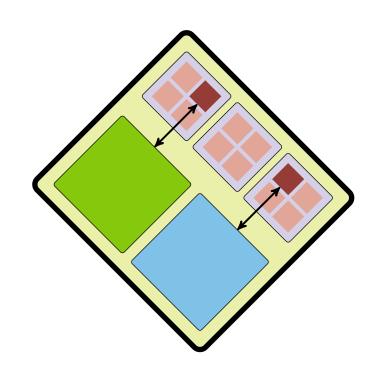
Module (Parallel)

Parallel Workflow

```
def main(pool, env, tasks):
    tasksSubmit(env
                                     = env,
                                    = tasks) \
               tasks
                                    = pool,
   .managerEval (pool
               timeoutWaitForSpokes = 2,  # Secs
               timeoutExecution
                                    = 300, # Secs
    if (terminateEarly):
       raise Exception("imageToEdge failed");
@grainCoarseList.pclosure
def tasksSubmit(env, tasks):
    rval = [];
    for cmd in filter(len,
                                             # Skip any empty line ...
                      map((lambda x:
                             x.strip()), # Skip any prefix/suffix spaces ...
                         tasks)):
       rval += [ stdlib.cache(cmd
                                      = cmd,
                              timeout = 2, # Secs
                                      = "imageToEdge (exec - %(ct)d)" \% dict(ct = len(rval),),
                              label
                               ),
    return rval;
```

```
def main():
    import __hidden__.pool as pool;
    import __hidden__.env as env;
    import util.phaseA.par as phaseA;
    import util.phaseB.par as phaseB;
    import util.phaseC.par as phaseC;
    import util.phaseD.par as phaseC;
   try:
        pool = pool.interAll();
        env = env .main ();
                              .main(pool = pool, env = env);
        phaseA
        phaseB
                              .main(pool = pool, env = env);
        tasks2ndRound = phaseC.main(pool = pool, env = env);
                              .main(pool = pool, env = env, tasks = tasks2ndRound);
        phaseD
    except Exception e:
    return;
```

Module (Parallel): Intra-node

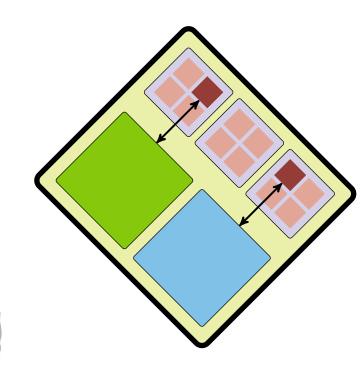


Device-specific Component (in Emerald, C, C++ and Fortran)

Module (Parallel): Intra-node

Heterogeneous Component (in Emerald)

```
def::api main::void (diml::int, dim2::int):
 var a::matrixA[dim1,dim2] = rand;
 var b::matrixB[dim1,dim2] = rand;
 try::concurrent:
   from ( demo :: explict ) import pow2;
   pow2 (a);
   b = 2.0;
 except:
   raise;
 assert(a::(Cuda == Serial == X86Cores)); round implementation in C
 assert(b::(Cuda == Serial));
 from global import result;
 result = a::Cuda;
 return;
```



```
ice-specific Component
rald, C, C++ and Fortran)
::void <Target = Cuda>
       (var a::matrixA&):
round implementation in C++
::void <Target = X98Cores> \
       (var a::matrixA&):
::void <Target = Serial>
       (var a::matrixA&):
```

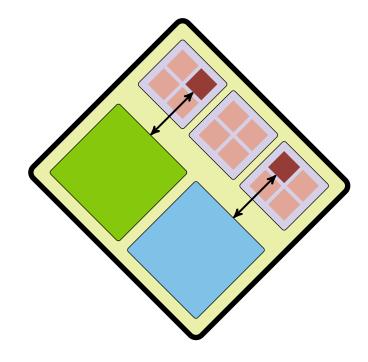
Module (Parallel): Intra-node

Heterogeneous Data Structure (in Emerald)

def::struct myMatrix (nDim1 :: int,

```
nDim2 :: int):
- @ -::Array
                                           bus Component
 Domain = (nDim1, nDim2);
 Format = Row;
                                           merald)
 Target = (::CUDA, ::Serial, ::X86Cores); a (diml::int, dim2::int):
                                           im1,dim2] = rand;
                                           im1,dim2] = rand;
  float _;
};
                            from
                                  ( demo :: explict ) import pow2;
                            pow2 (a);
                            b = 2.0;
                          except:
                            raise;
                          assert(a::(Cuda == Serial == X86Cores)); round implementation in C
                          assert(b::(Cuda == Serial));
                          from global import result;
                          result = a::Cuda;
```

return;



ice-specific Component rald, C, C++ and Fortran)

```
::void <Target = Cuda>
       (var a::matrixA&):
round implementation in C++
::void <Target = X98Cores> \
       (var a::matrixA&):
::void <Target = Serial>
       (var a::matrixA&):
```

Suppositions

Most embarrassingly parallel solutions perform a lot of redundant work ...

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Only fix ... share knowledge



Many problems in HPC and Analytics are memory bounded ...

Suppositions

Many problems in HPC and Analytics are memory bounded ...



- Cannot depend on virtualization
- Must throw everything at the problem

Suppositions: Constraints When Exploiting Parallelism

Prototypes vs Runtime Env

Suppositions: Constraints When Exploiting Parallelism

Prototypes → Construct-by-correction vs
Runtime Env

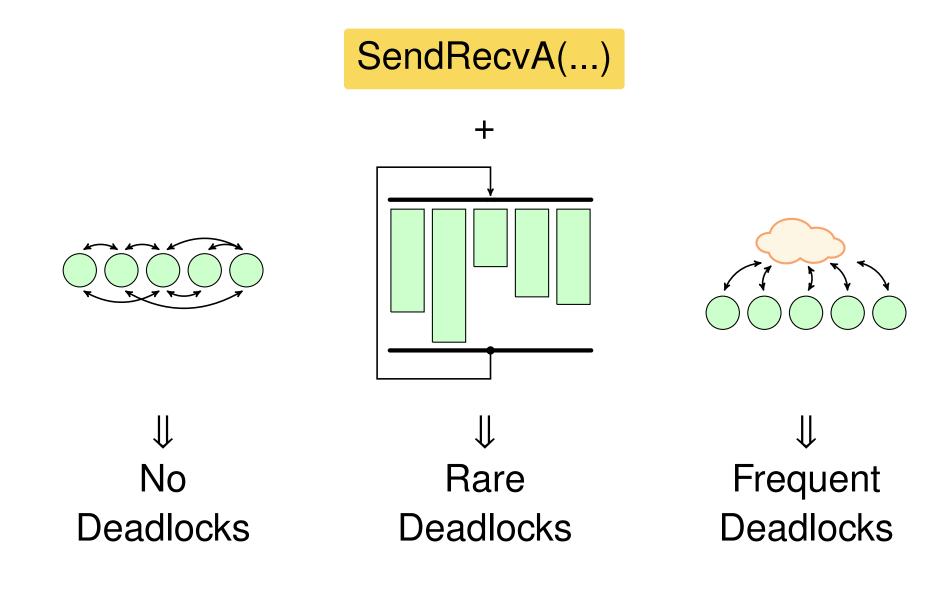
Suppositions: Constraints When Exploiting Parallelism

Prototypes → Construct-by-correction

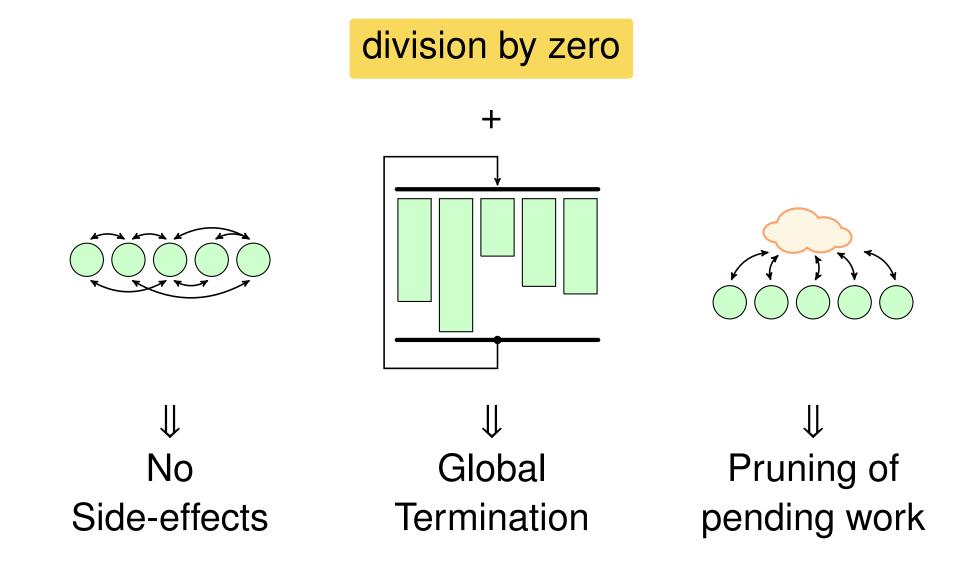
VS

Runtime Env → Correct-by-construction

Suppositions: Enabling / Disabling Communication Primitives



Suppositions: Parallel Semantics



Conclusion: Rest of the tutorial

For each form of parallelism to be reviewed:

- What is the management policy?
- Describe a compatible communication primitive
- Describe a toxic communication primitive