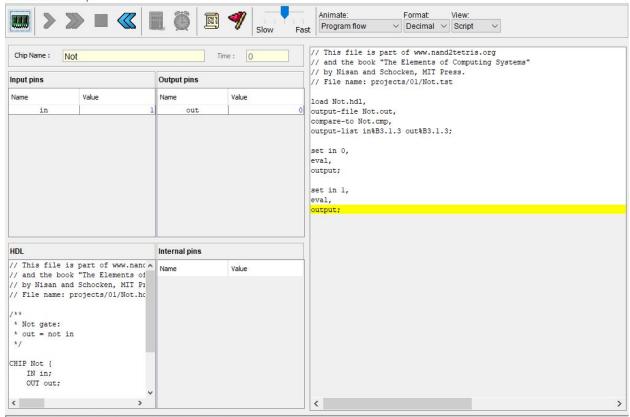
## **Not Gate**

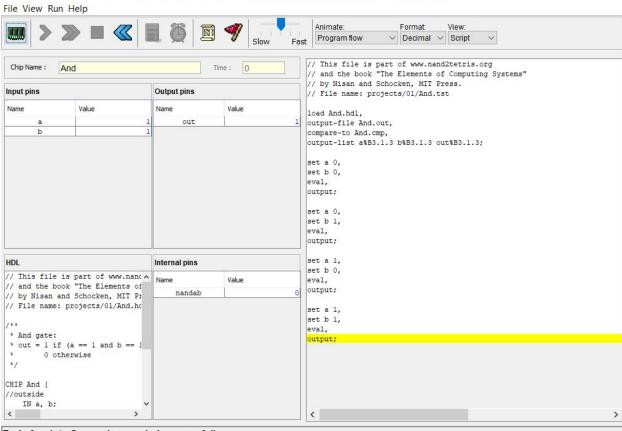
Hardware Simulator (2.5) - C:\Users\april\Downloads\CS220\nand2tetris\nand2tetris\projects\01\Not.hdl File View Run Help



End of script - Comparison ended successfully

## **And Gate**

Hardware Simulator (2.5) - C:\Users\april\Downloads\CS220\nand2tetris\nand2tetris\projects\01\And.hdl



End of script - Comparison ended successfully

## Or Gate

lardware Simulator (2.5) - C:\Users\april\Downloads\CS220\nand2tetris\nand2tetris\projects\01\Or.hdl

File View Run Help View: ∨ Decimal ∨ Script Program flow Slow Fast // This file is part of www.nand2tetris.org
// and the book "The Elements of Computing Systems" Chip Name : Or Time: 0 // by Nisan and Schocken, MIT Press. **Output pins** Input pins // File name: projects/01/Or.tst Name Name Value Value load Or.hdl, output-file Or.out, out compare-to Or.cmp, output-list a%B3.1.3 b%B3.1.3 out%B3.1.3; set a 0, set b 0, eval, output; set a 0, set b 1, eval. output; set a 1, HDL Internal pins set b 0, // This file is part of www.nanc A // and the book "The Elements of // by Nisan and Schocken, MIT P: Value Name eval, output; nandaa nandbb // File name: projects/01/Or.hdl set a 1, set b 1, /\*\*
\* Or gate: eval. utput; \* out = 1 if (a == 1 or b == 1) 0 otherwise CHIP Or { IN a, b; OUT out;

End of script - Comparison ended successfully

## **Xor Gate**

lardware Simulator (2.5) - C:\Users\april\Downloads\CS220\nand2tetris\nand2tetris\projects\01\Xor.hdl

File View Run Help View: ∨ Decimal ∨ Script Program flow Slow Fast // This file is part of www.nand2tetris.org
// and the book "The Elements of Computing Systems" Chip Name : Xor Time: 0 // by Nisan and Schocken, MIT Press. **Output pins** Input pins // File name: projects/01/Xor.tst Name Name Value Value load Xor.hdl, output-file Xor.out, compare-to Xor.cmp, output-list a%B3.1.3 b%B3.1.3 out%B3.1.3; out set a 0, set b 0, eval, output; set a 0, set b 1, eval, output; set a 1, HDL Internal pins // This file is part of www.nanc A
// and the book "The Elements of
// by Nisan and Schocken, MIT P:
// File name: projects/01/Xor.hc set b 0, Value Name eval, output; notb nota set a 1, outandl set b 1, outand2 eval. \* Exclusive-or gate: output; \* out = not (a == b) \*/ CHIP Xor {
 IN a, b;
 OUT out;

End of script - Comparison ended successfully