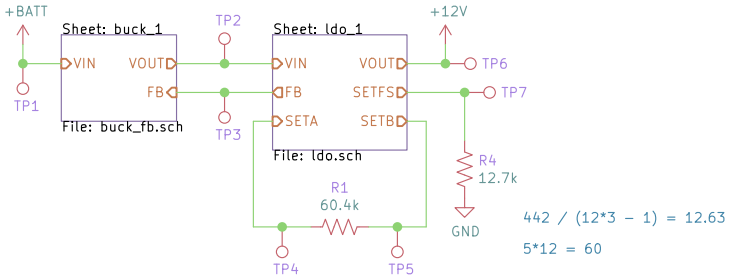

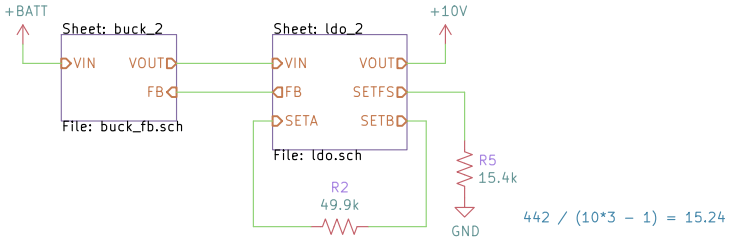

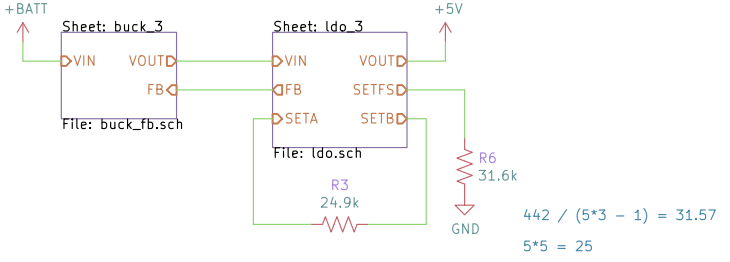

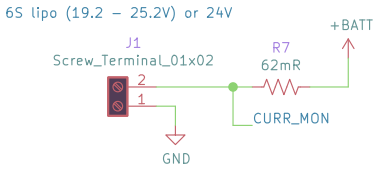

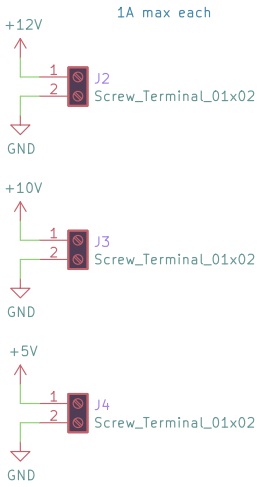

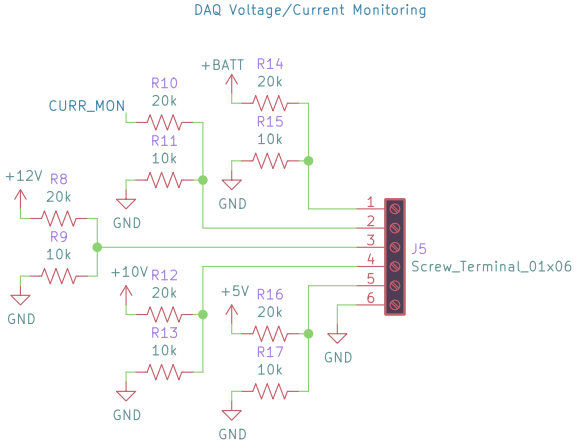

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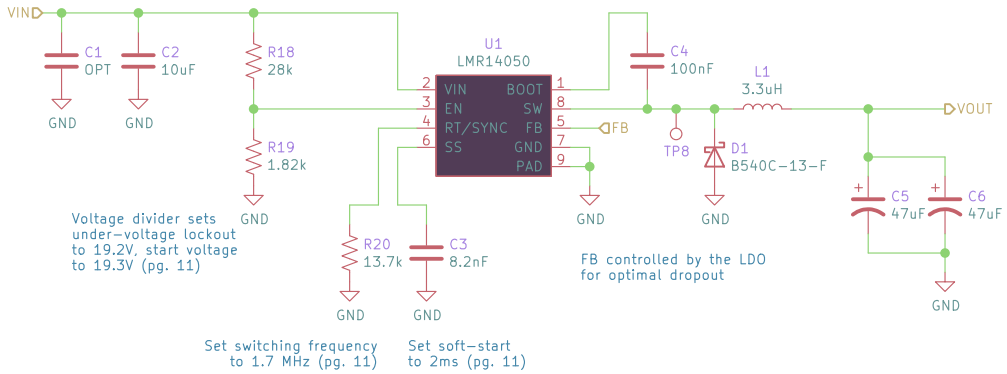
Variable buck regulator. Most values taken from webench.ti.com for 17V out.
https://www.ti.com/lit/ds/symlink/lmr14050.pdf



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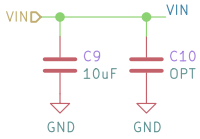
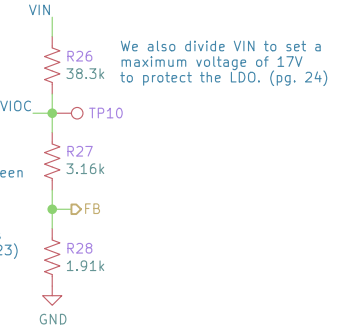
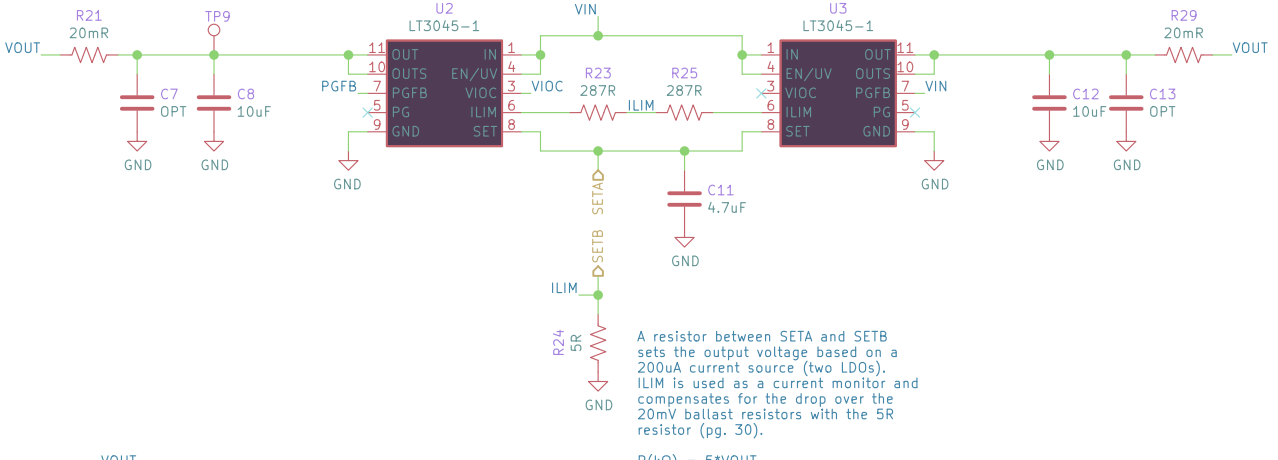




VIOC is driven to the difference between the set voltage and VOUT. We want VIOC=2V so we divide such that 2V->0.75V, the switching threshold for the buck converter, and use it as the buck converter's feedback. (pg. 23)

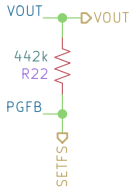
Parallel two LDOs for 1A output.

https://www.analog.com/media/en/technical-documentation/data-sheets/30451fa.pdf



A resistor between SETA and SETB sets the output voltage based on a 200uA current source (two LDOs). ILIM is used as a current monitor and compensates for the drop over the 20mV ballast resistors with the 5R resistor (pg. 30).

R(kΩ) = 5*VOUT



When PGFB is below 300mV fast startup is enabled. SETFS is a resistor to ground which should divide VOUT such that PGFB reaches 300mV at 90% of the desired VOUT. (pg. 21)

R(kΩ) = 442 / (3*VOUT - 1)

Sheet: /ldo_1/		
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