

# Abrar Rahman Protyasha

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CONTACT INFORMATION	Phone: +1 (917) 862-1504 Email: <a href="mailto:aprotyas@u.rochester.edu">aprotyas@u.rochester.edu</a>	Website: <a href="https://aprotyas.github.io">https://aprotyas.github.io</a>
EDUCATION	<b>University of Rochester</b> <i>B.S., Electrical and Computer Engineering</i> • GPA: 3.80 out of 4.00; Research and Innovation Grant (RIG) recipient.	Rochester, NY <b>Aug 2017 – May 2021</b>
RELEVANT COURSEWORK, PROJECTS	<i>Coursework:</i> Machine learning, Autonomous mobile robots, Digital image processing, Random processes, Embedded systems programming, Computer architecture, Integrated circuit design. <i>Notable projects:</i> <ul style="list-style-type: none"><li>• <b>Autonomous mobile robot software architecture:</b> Developed ROS packages (C++) for perception, occupancy grid mapping, path planning, localization, and path following controls to explore a partially known world using a TurtleBot2, in addition to a simulator and OpenGL GUI for testing purposes (<a href="https://aprotyas.github.io/projects/amr.html">https://aprotyas.github.io/projects/amr.html</a>)</li><li>• <b>Remotely operated vehicle:</b> Designed a wirelessly controlled vehicle with an on-board Raspberry Pi and PIC32 MCU, driving two DC gear-motors through a dual motor driver carrier using a PID controller. (<a href="https://aprotyas.github.io/projects/remote-vehicle.html">https://aprotyas.github.io/projects/remote-vehicle.html</a>)</li><li>• <b>Feedforward neural network:</b> Implemented a neural network for binary classification on the HIGGS dataset using NumPy. (<a href="https://aprotyas.github.io/projects/nm.html">https://aprotyas.github.io/projects/nm.html</a>)</li></ul>	
PROFESSIONAL EXPERIENCE	<b>Silicon Labs</b> <i>Applications Engineering Intern</i> Product validation, solutions bring-up, and design collateral generation for IEEE 1588 modules. <ul style="list-style-type: none"><li>• Developed a 1 PPS/ToD stream alignment tool on an ARM Cortex-M4 based EFM32 MCU.</li><li>• Established procedure to demonstrate PTP synchronization of IEEE 1588 modules using the W32Time networking module in MS Windows. Reduced demo bring-up cost by <math>\gg 100\%</math>.</li><li>• Traced IEEE 1588 modules' electrical/performance specifications to underlying components to conform with internal documentation standards.</li></ul> <b>Wireless Communication and Networking Group (WCNG)</b> <i>Xerox Engineering Research Fellow</i> Researched on mobile ad-hoc network creation and management. <ul style="list-style-type: none"><li>• Developed channel selection, IPV6 support, and improved debugging infrastructure in a wireless network emulating testbed used to evaluate mobile ad-hoc network protocols.</li><li>• Automated network data acquisition using Python test scripts and socket programs in C.</li><li>• Compiled use-case data, generated weekly reports, and led weekly lab meetings on this project.</li></ul> <b>University of Rochester</b> <i>Lead Teaching Assistant</i> Lead TA for ECE114 (Intro to C/C++ programming). Responsibilities included creating assignments, delivering lecture sessions, and establishing general course direction with Prof. Stephen Kastner.	Nashua, NH <b>May 2020 – Aug 2020</b>  Rochester, NY <b>May 2019 – May 2020</b>  Rochester, NY <b>Aug 2018 – May 2020</b>
	<b>Busza Lab, University of Rochester Medical Center</b> <i>Research Assistant</i> Investigated the relationship between muscle exercise and early stroke mobility recovery. <ul style="list-style-type: none"><li>• Statistically analyzed and processed arm sEMG signals on MATLAB to predict arm motion.</li><li>• Created an angle measurement tool using an Arduino Uno feeding into MATLAB.</li></ul>	Rochester, NY <b>Feb 2019 – June 2019</b>
TECHNICAL SKILLS	<i>Languages:</i> C++, Python, C, MATLAB, Bash, L <sup>A</sup> T <sub>E</sub> X, Verilog. <i>Technologies:</i> Git, ROS, OpenGL, NumPy, SciPy, UNIX, Network utilities (ifconfig, Mininet).	