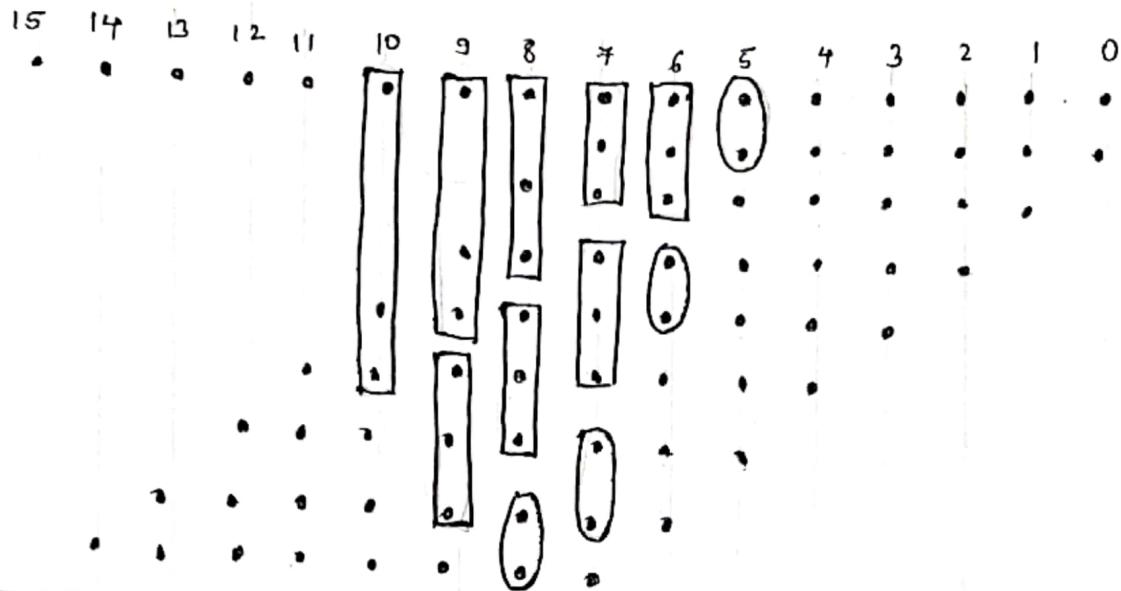
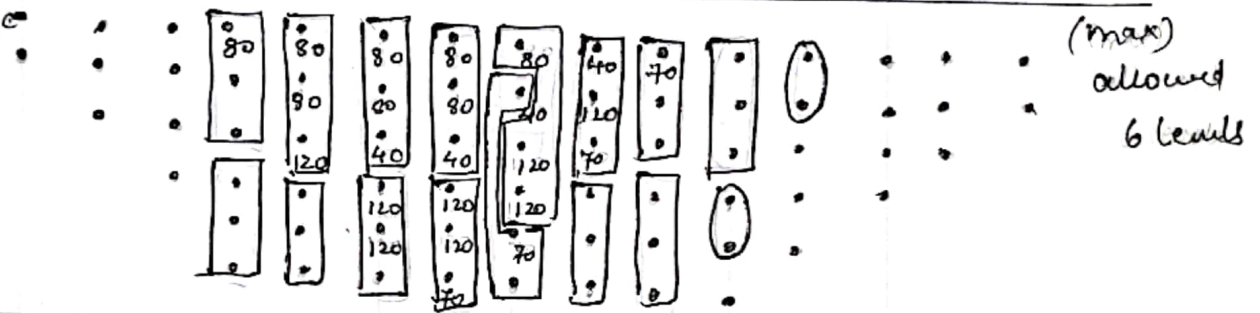


# Reduction Stages for Dadda Multiplier.

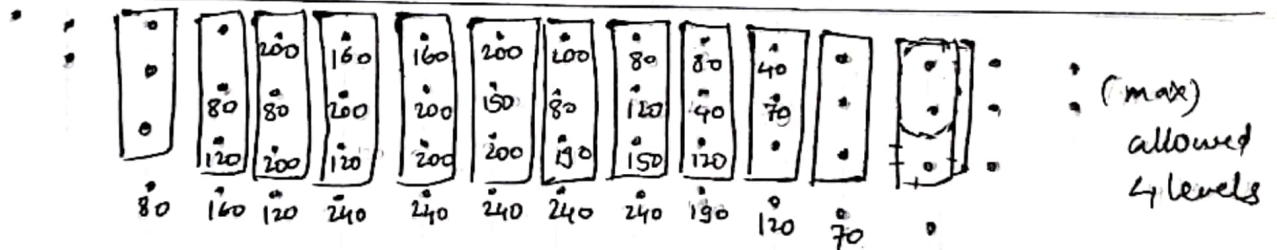


Half  
adder  
S → 70125  
C → 40125  
P-A  
S → 12015  
C → 80125

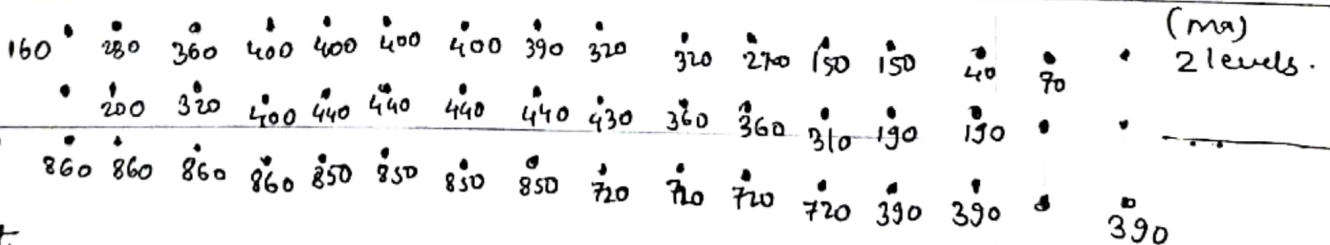
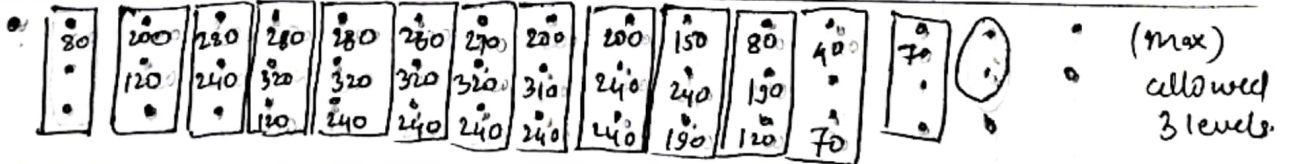
First stage



Second stage



Third stage



Output