Programmer's Manual"

### **NAME**

```
__ppc_set_ppr_med, __ppc_set_ppr_very_low, __ppc_set_ppr_low, __ppc_set_ppr_med_low, __ppc_set_ppr_med_high - Set the Program Priority Register
```

#### **LIBRARY**

Standard C library (libc, -lc)

## **SYNOPSIS**

```
#include <sys/platform/ppc.h>
void __ppc_set_ppr_med(void);
void __ppc_set_ppr_very_low(void);
void __ppc_set_ppr_low(void);
void __ppc_set_ppr_med_low(void);
void __ppc_set_ppr_med_high(void);
```

### DESCRIPTION

These functions provide access to the Program Priority Register (PPR) on the Power architecture.

The PPR is a 64-bit register that controls the program's priority. By adjusting the PPR value the programmer may improve system throughput by causing system resources to be used more efficiently, especially in contention situations. The available unprivileged states are covered by the following functions:

The privileged state *medium high* may also be set during certain time intervals by problem-state (unprivileged) programs, with the following function:

```
__ppc_set_ppr_med_high()
sets the Program Priority to medium high.
```

If the program priority is medium high when the time interval expires or if an attempt is made to set the priority to medium high when it is not allowed, the priority is set to medium.

## **VERSIONS**

The functions \_\_ppc\_set\_ppr\_med(), \_\_ppc\_set\_ppr\_low(), and \_\_ppc\_set\_ppr\_med\_low() are provided since glibc 2.18. The functions \_\_ppc\_set\_ppr\_very\_low() and \_\_ppc\_set\_ppr\_med\_high() first appeared in glibc 2.23.

### **ATTRIBUTES**

For an explanation of the terms used in this section, see attributes(7).

Interface	Attribute	Value
ppc_set_ppr_med(),ppc_set_ppr_very_low(),	Thread safety	MT-Safe
ppc_set_ppr_low(),ppc_set_ppr_med_low(),		
ppc_set_ppr_med_high()		

### **STANDARDS**

These functions are nonstandard GNU extensions.

# **NOTES**

The functions \_\_ppc\_set\_ppr\_very\_low() and \_\_ppc\_set\_ppr\_med\_high() will be defined by<sys/plat-form/ppc.h> if \_ARCH\_PWR8 is defined. Availability of these functions can be tested using #ifdef \_ARCH\_PWR8.

# **SEE ALSO**

**\_\_ppc\_yield**(3)

Power ISA, Book II - Section 3.1 (Program Priority Registers)