REV DESCRIPTION DATE APPROVED

A Initial schematic for layout 27/09/2017 Long.Han

JN_IPI_GLOBAL_V02 SCHEMATIC SHEETS (REV V11)

SCHEMATIC CONTENTS

sh01 - TITLE sh02 - GPMC & HDMI

Shuz - GPMC & HDMI

sh03 - MMC & ETH1 & CSI & JTAG

sh04 - PCIE&USBÐ0 &MCASP

sh05 - MLBP & UART & SPI

sh06 - DDR3

sh07 - ANA PWR sh08 - DIG PWR

sh09 - eMMC & SYSBOOT

sh10 - 5708 BANK

sh11 - DDR3L BANK CAPS & TERM

sh12 - POWER IN & RST & hole

sh13 - SECONDARY LDO'S/SWITCHES

sh14 - PMIC

sh15 - HDMI & TF

sh16 - USB

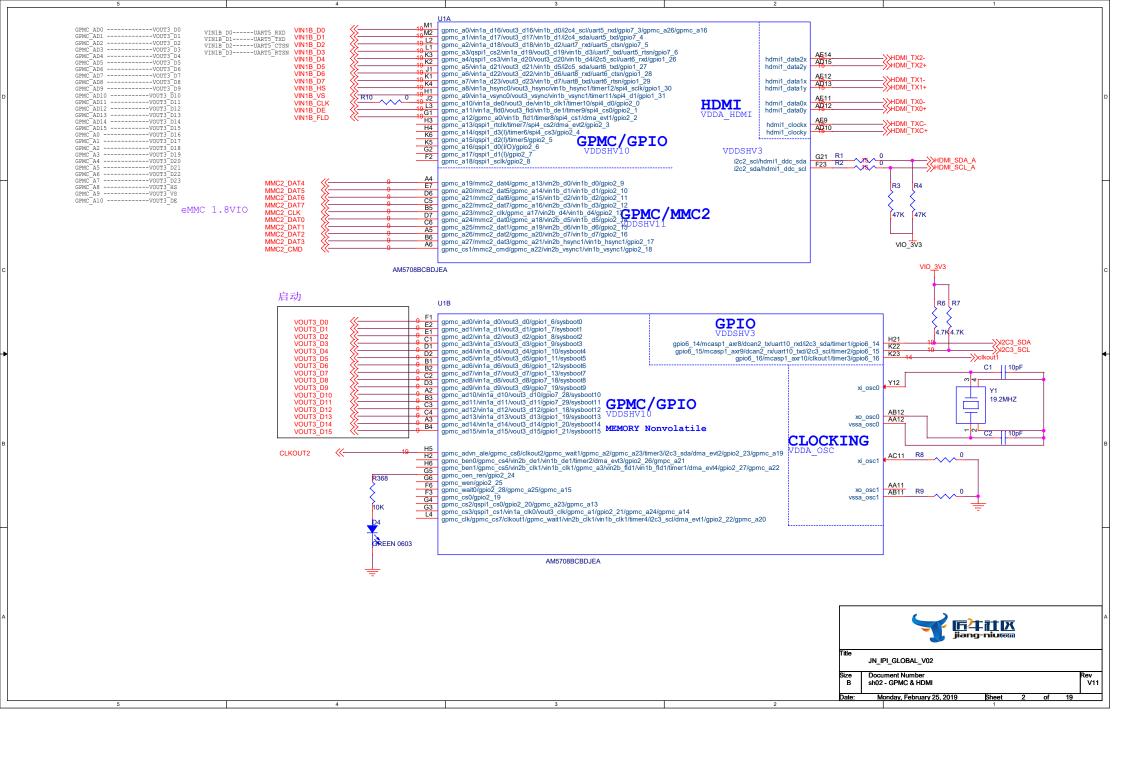
sh17 - ETHERNETO

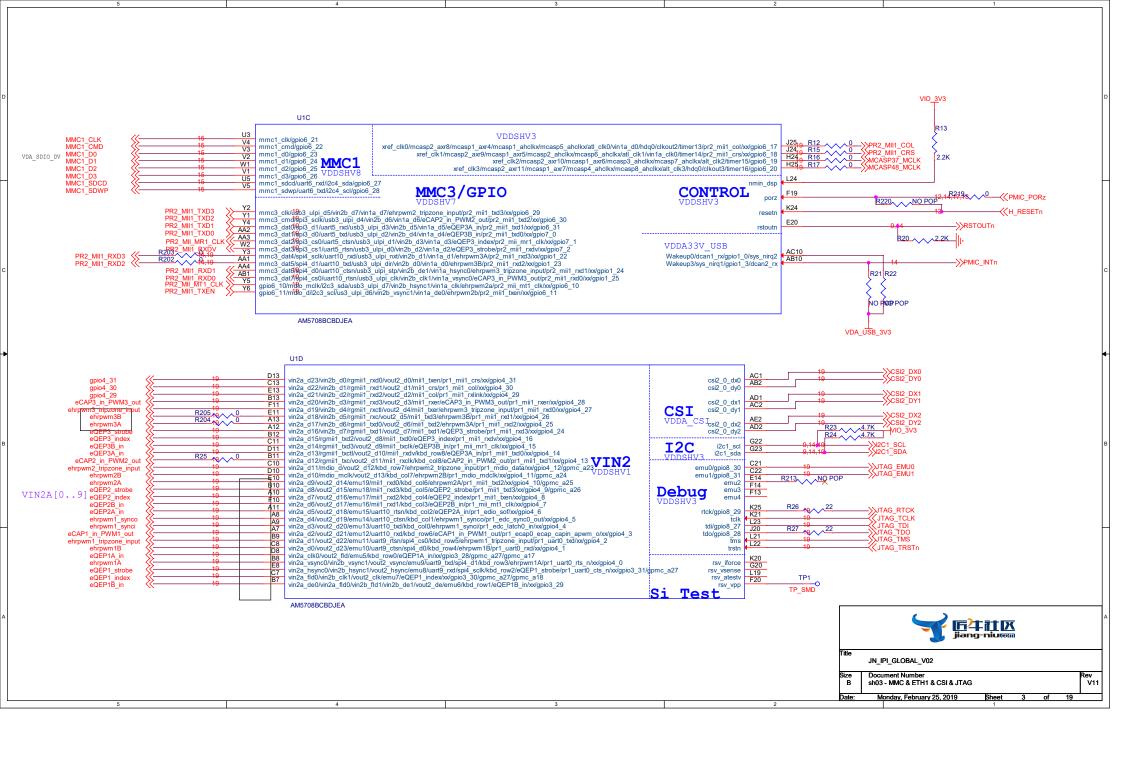
sh18 - ETHERNET1

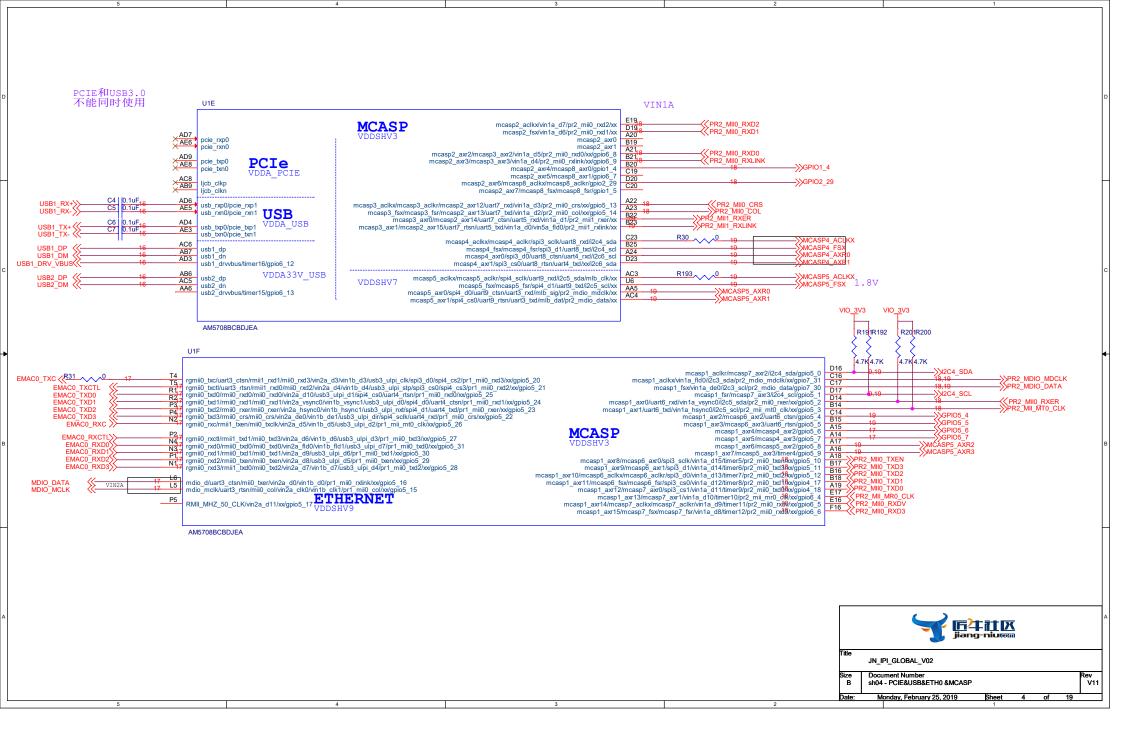
sh19 - INTERFACE

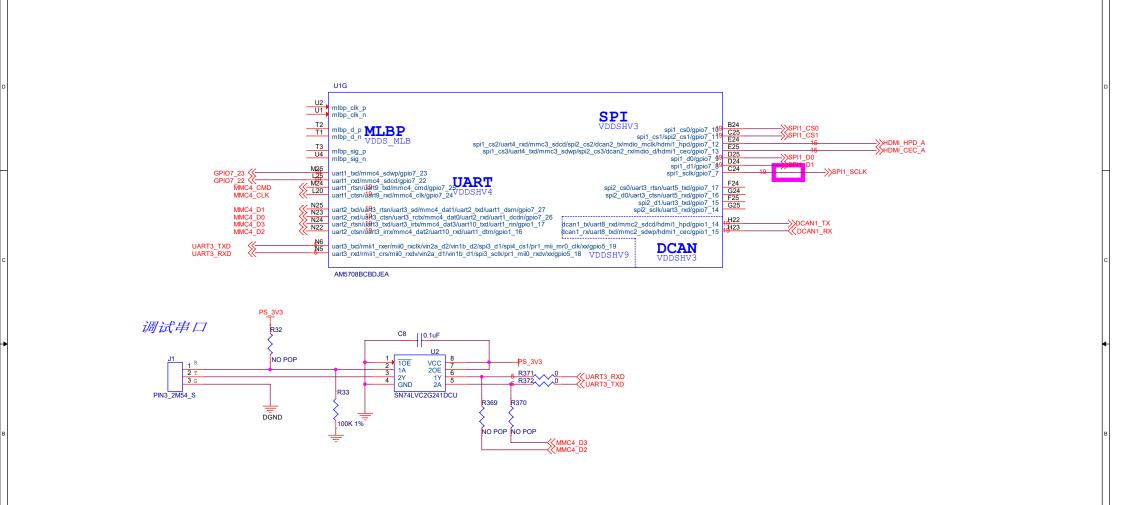
				REVISION STATUS OF SHEETS							
	REV										
	SH										
	REV										
L	SH										
_	REV										
L	SH										
	REV										
Ī	SH	21	22	23	24	25	26	27	28		
Γ	REV	A	А	А	A	A	A	A	A		
ſ	SH	11	12	13	14	15	16	17	18	19	20
	REV	A	А	А	A	А	A	A	A	А	A
Γ	SH	1	2	3	4	5	6	7	8	9	10



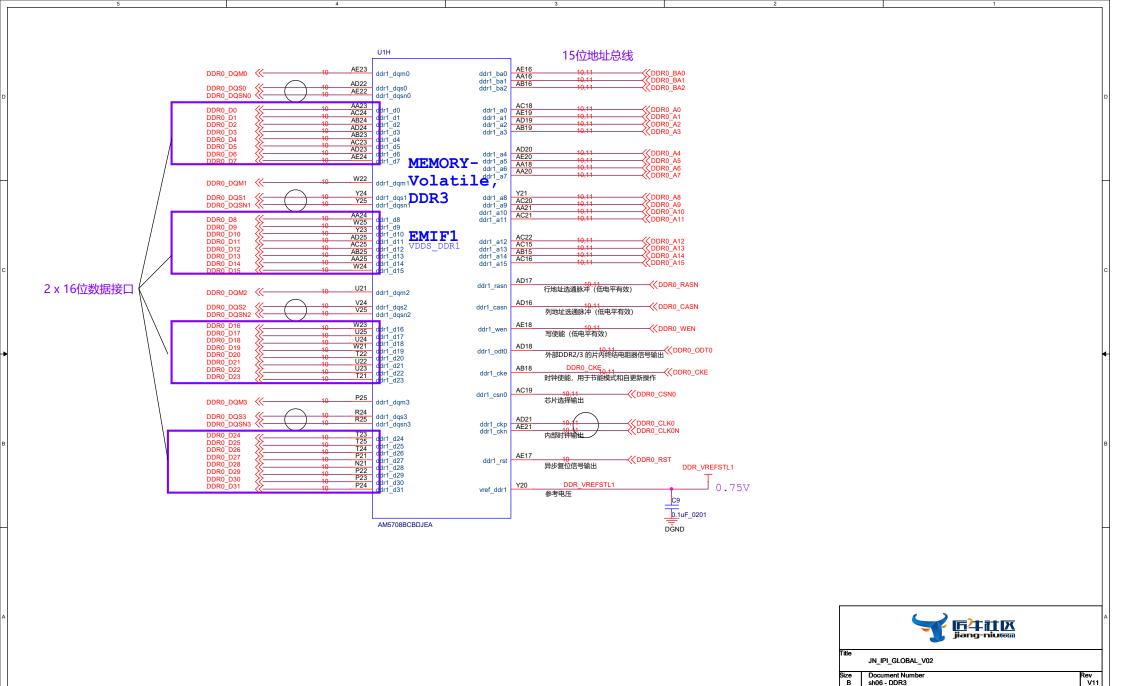












Monday, February 25, 2019

Sheet

