EAST WEST UNIVERSITY

Course Name : Electronic Circuits

Course Code : CSE251

Section No :06

Group No :

Experiment No : 04

Name of the Experiment : Adder and Amplifier Circuits Using 741 Op Amp

Date of allocation :

Date of submission: 6-1-2020

Submitted To : Surajit Das Barman
Senior Lecturer

Student's Name : Apurba Roy
Student's ID :2018-3-60-063

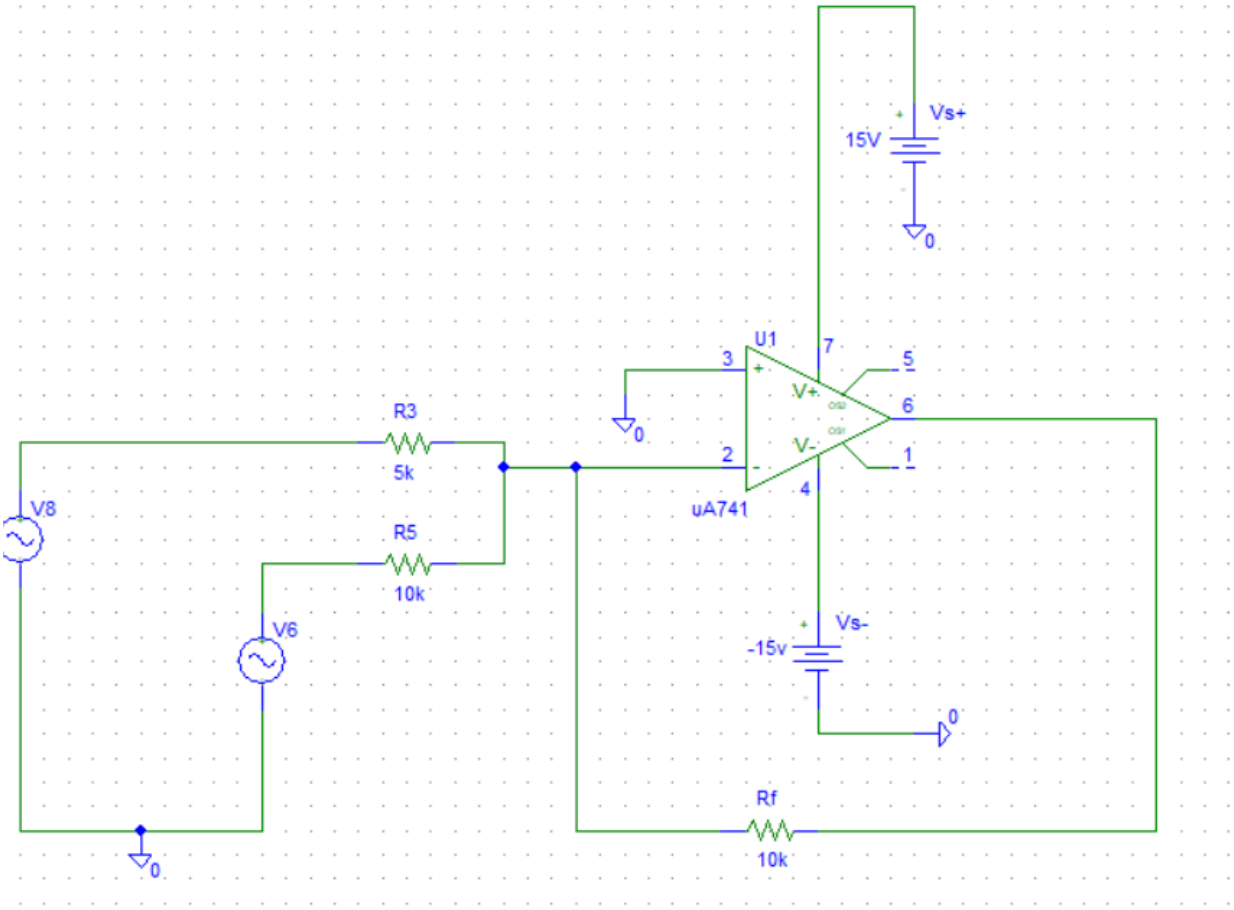
Student's Name : Monjurul Alam
Student's ID :2018-3-60-035

Objectives: 1. To familiarize with the 741 Op Amp Integrated Circuit (IC). 2. To design and construct an adder using 741 Op Amp. 3. To design and construct an amplifier using 741 Op Amp.

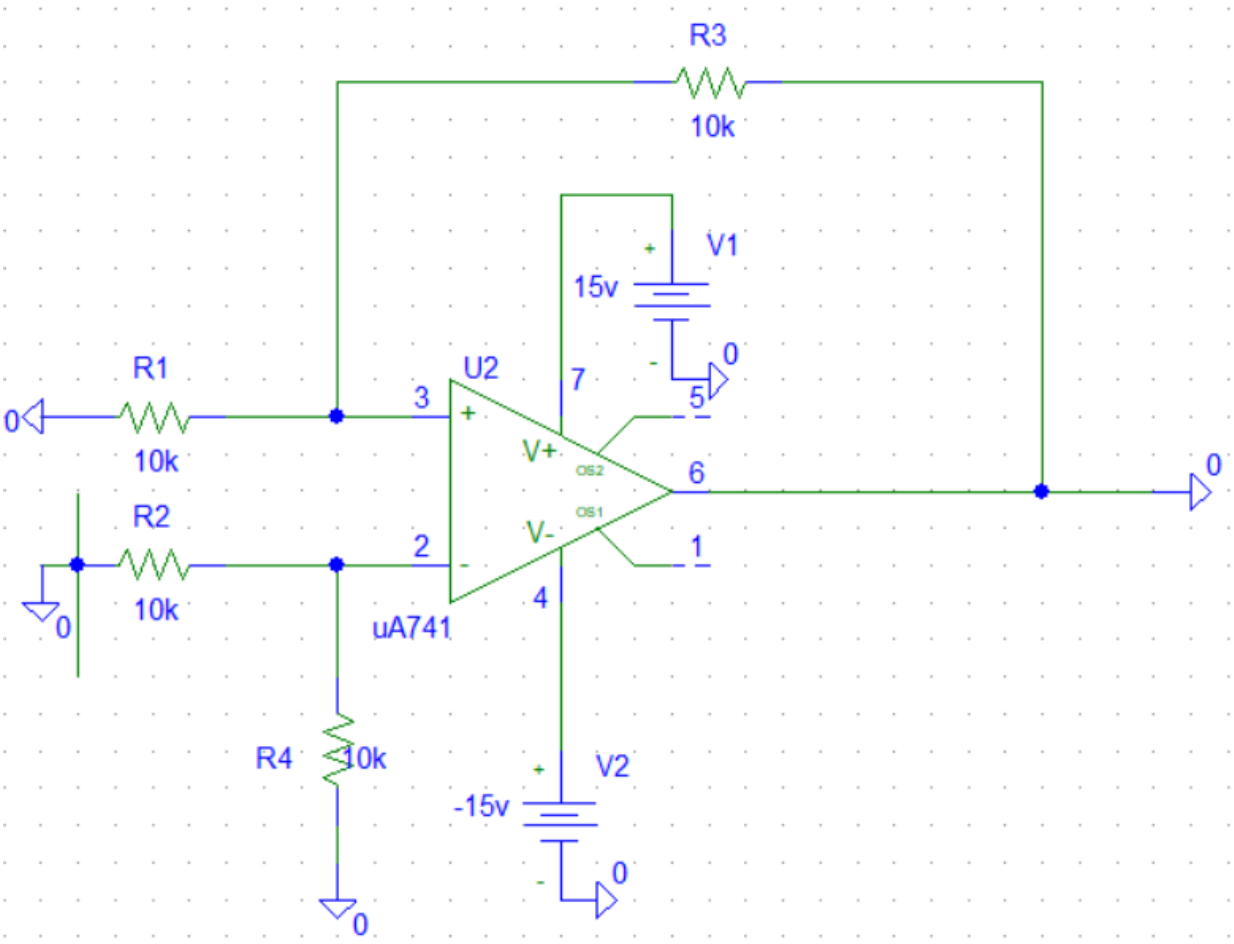
Introduction: Operational Amplifier (Op Amp) is a differential amplifier and can perform mathematical operations such as addition, subtraction, etc. This is an integrated circuit (IC).

Circuit diagram:

Adder circuit :



Amplifier Circuits:

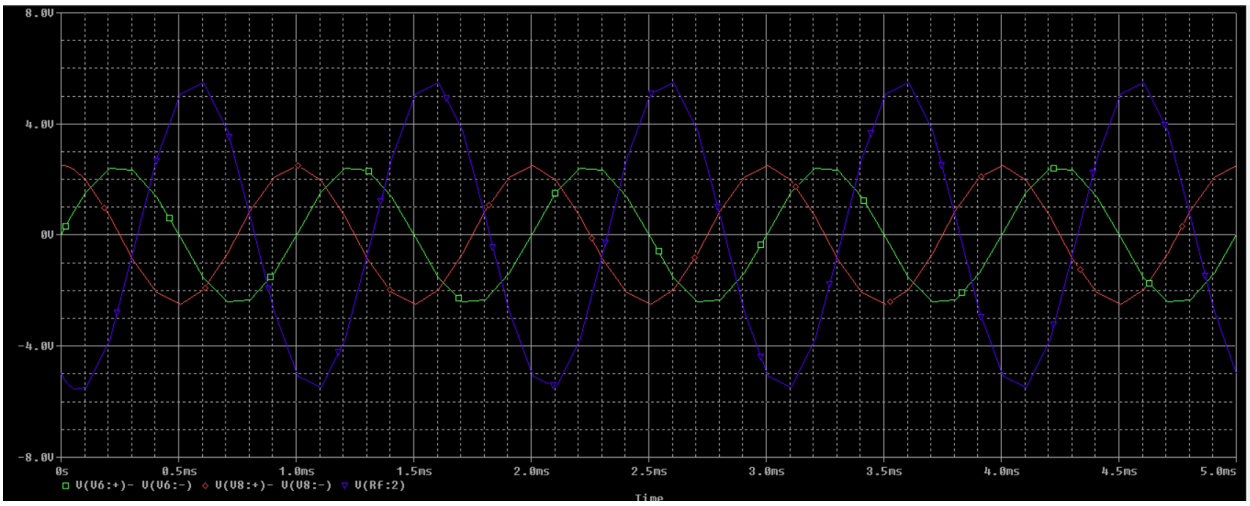


Post-Lab Report Questions:

ADDER CIRCUIT:

3. Simulate the circuit shown in Figure 2 in Pspice. Use V1 a 5V peak to peak, 1KHZ, 00 phase sine wave and V2 a 5V peak to peak, 1KHZ, 900 phase sine wave. Perform simulation for 4 cycles (transient analysis for 4 ms) and attach the printed output with your report.

Answer:



Conclusion :

We connect this circuit using Pspice software. I think if we could do this same experiment in the lab, the measure value would change a little bit and most importantly we could learn how to connect the circuit for real life.