

# Code Generation by Tree Walking

Amey Karkare

karkare@cse.iitk.ac.in

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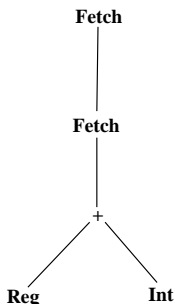
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- ▶ Pushes dynamic programming to a pre-processing stage prior to code-generation time.
- ▶ Simplifies dynamic programming effort by assuming unbounded number of registers.
- ▶ Only cases taken into account are different patterns matching a node.
- ▶ Say something about normalising instructions

# Code Generation by Tree Walking – Example

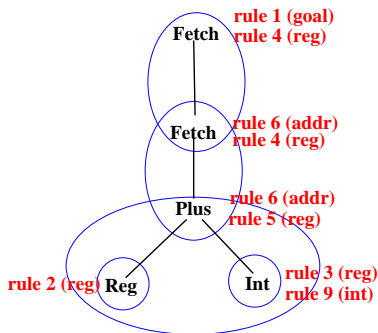
- An example expression tree and an example machine:



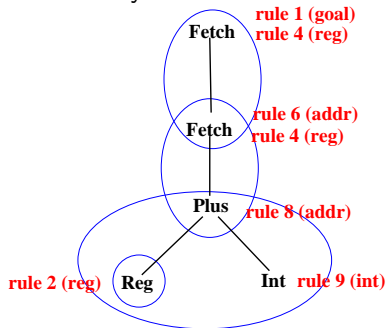
rule no	1	goal <- reg	0	rule cost
	2	reg <- Reg	0	
	3	reg <- int	1	
non-terminal	4	reg <- <div>Fetch   addr</div>	2	pattern
	5	reg <- <div>Plus / \ reg reg</div>	1	terminal
	6	addr <- reg	0	
	7	addr <- int	0	
	8	<div>addr &lt;- Plus / \ reg int</div>	3	rule
	9	int <- Int	0	

# Code Generation by Tree Walking – Example

- The tree can be covered in more than one ways



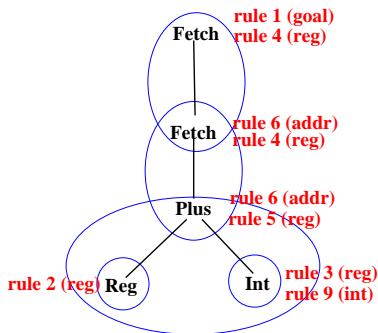
Cost = 6



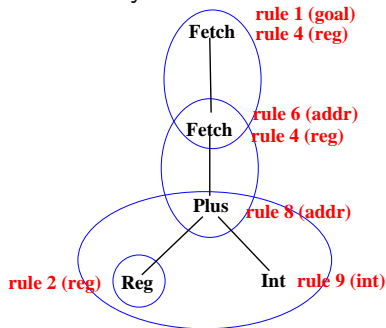
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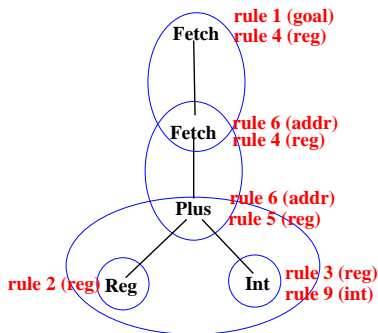
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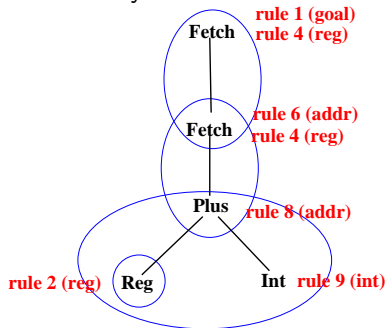


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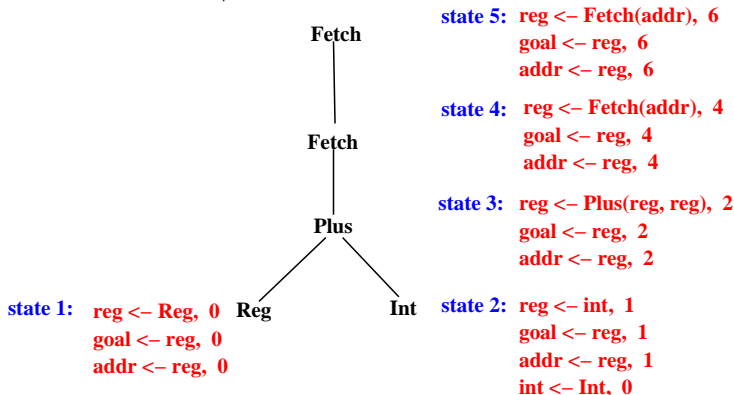
- ▶ We are finally interested in the least cost tree.
- ▶ We also want to do some pre-processing before we get any tree,

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- ▶ How is this done? Given a tree,

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- ▶ How is this done? Given a tree,
  - ▶ traverse the tree bottom up. With the help of a *transition table*, annotate each node of the tree with a state.



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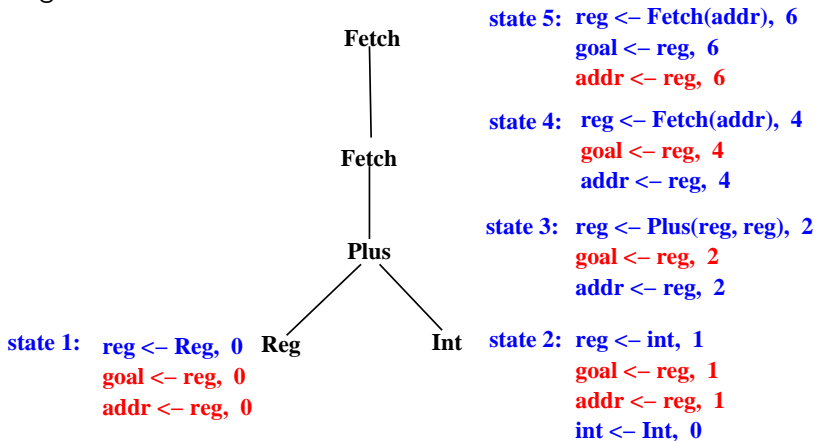
- ▶ *State*: Gives the minimum cost of evaluating a node in the expression tree to different non-terminals.
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# Code Generation by Tree Walking

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- ▶ *Transition table*: Gives
  - ▶ state corresponding to leaf nodes (0-ary terminals).
  - ▶ given the states of children, gives state of interior nodes (n-ary terminals).

# Code Generation by Tree Walking – Example

- ▶ A second top-down pass determines the instructions to be used at each node assuming that the root is to be evaluated in goal.





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- ▶ Cost of reducing Int to goal is
  - cost of reducing Int to int (0) +
  - cost of reducing int to reg (1) +
  - cost of reducing reg to goal (0) +

# Precomputing the Transition Table

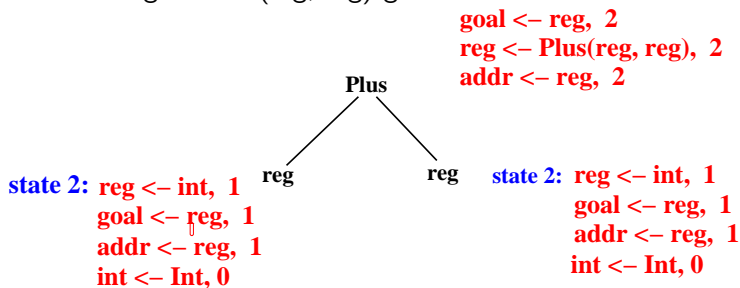
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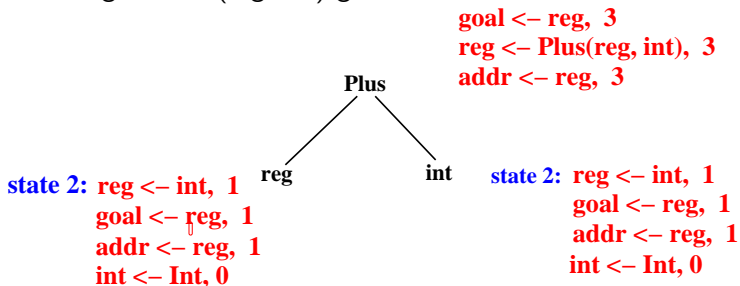
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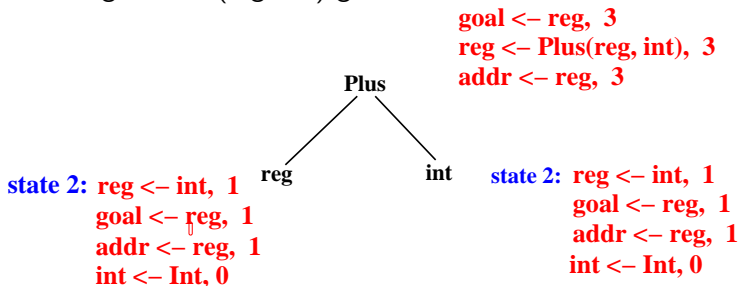
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# Precomputing the Transition Table

- ▶ The rule  $\text{reg} \leftarrow \text{Plus}(\text{reg}, \text{int})$  gives



- ▶ Conclusion: If the leaves of **Plus** are both in state 2, then **Plus** will be in

**state 6:**  $\text{goal} \leftarrow \text{reg}, 2$   
 $\text{reg} \leftarrow \text{Plus}(\text{reg}, \text{reg}), 2$   
 $\text{addr} \leftarrow \text{reg}, 2$

# Precomputing the Transition Table

- ▶ Similarly, we should also find the transitions for Plus on pairs  $(\text{state1}, \text{state1})$ ,  $(\text{state1}, \text{state2})$ ,  $(\text{state2}, \text{state6})$

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- ▶ ...
- ▶ Will this process always terminate?

# Precomputing the Transition Table

- Consider computation of the state at Fetch, with reg in the state shown.

**Fetch**

**reg**

**reg  $\leftarrow$  Plus(reg, reg), 0**  
**goal  $\leftarrow$  reg, 0**  
**addr  $\leftarrow$  reg, 0**

# Precomputing the Transition Table

- Consider computation of the state at Fetch, with reg in the state shown.

**Fetch**

**reg**

**reg** ← **Plus(reg, reg), 0**  
**goal** ← **reg, 0**  
**addr** ← **reg, 0**

- Successive computation of the states for Fetch yield:

**Fetch**

**reg**

**reg** ← **Fetch(addr), 2**  
**goal** ← **reg, 2**  
**addr** ← **reg, 2**

**reg** ← **Plus(reg, reg), 0**  
**goal** ← **reg, 0**  
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**Fetch**

**reg**

**reg** ← **Fetch(addr), 4**  
**goal** ← **reg, 4**  
**addr** ← **reg, 4**

**reg** ← **Plus(reg, reg), 2**  
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Obviously not.

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- ▶ Does this make the resulting transition table different?  
Obviously not.
- ▶ Does this necessarily lead to a finite number of states?

# Relativization of states

- ▶ Consider a machine with only these two instructions involving Fetch.

**reg** ← **Fetch**

**reg**

**cost = 1**

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**int**

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- ▶ Practical solution: If cost difference between any pair of terminals is greater than a threshold, instruction set is rejected.
- ▶ Typical instruction sets do not lead to divergence.



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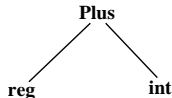
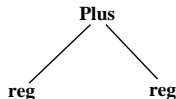
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  - ▶ State reduction by projecting out irrelevant items
  - ▶ State reduction by triangle trimming.

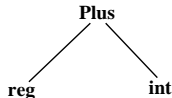
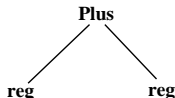
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- Also assume that there are two states:

**state 1:**  $\text{goal} \leftarrow \text{reg}, 0$   
 $\text{reg} \leftarrow \text{Reg}, 0$   
 $\text{addr} \leftarrow \text{reg}, 0$

**state 2:**  $\text{goal} \leftarrow \text{reg}, 1$   
 $\text{reg} \leftarrow \text{int}, 1$   
 $\text{addr} \leftarrow \text{reg}, 1$   
 $\text{int} \leftarrow \text{Int}, 0$



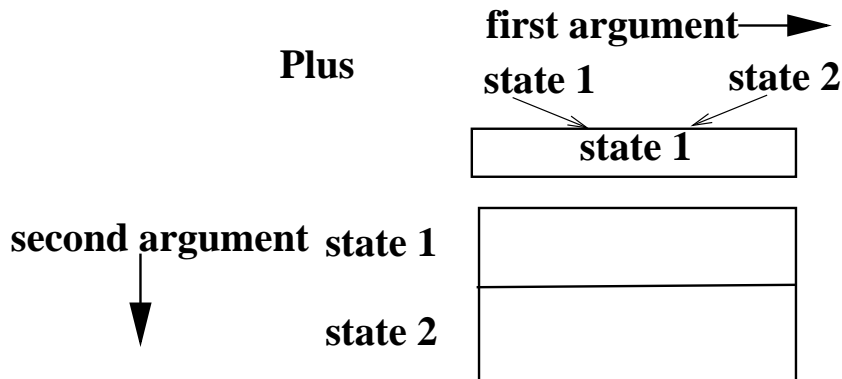
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- ▶ The normal transition table for Plus:

<b>Plus</b>		<b>first argument</b> →	
		<b>state 1</b>	<b>state 2</b>
<b>second argument</b> ↓	<b>state 1</b>		
	<b>state 2</b>		

# State Reduction by Projecting out Irrelevant Items

- ▶ Since the first argument of Plus is a reg, we can project the  $\text{int} \leftarrow \dots \text{item}$  out of both the states. The resulting transition table for Plus is:

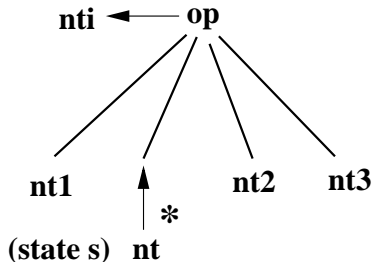


# State Reduction by Triangle Trimming

- ▶ Assume that a state  $s$  has two items  $nt \leftarrow \dots$  and  $nt' \leftarrow \dots$ .  
Under what conditions can we say that  $nt \leftarrow \dots$  is subsumed by  $nt' \leftarrow \dots$  and thus can be removed from  $s$ .

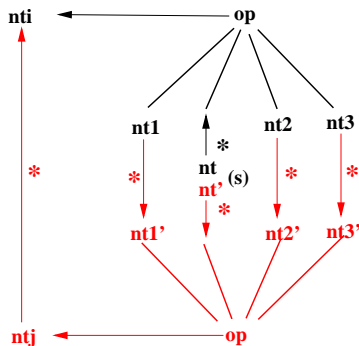
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- ▶ Assume that the state has been used in the context of the operator  $op$  at the argument position shown



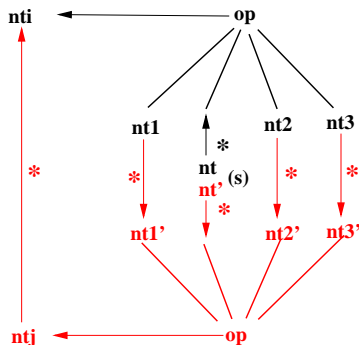
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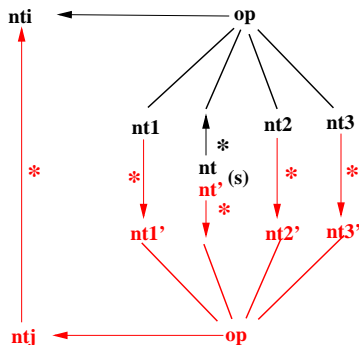
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- ▶ The cost of the rule  $nti \leftarrow \dots$  and the black chain reductions should be less than the rule  $ntj \leftarrow \dots$  and the red chain reductions.
- ▶ Further this should be true in all contexts in which  $s$  can be used.

# BURG – A Code Generation Tool

- ▶ **B**ottom **U**p **R**ewriting based code **G**enerator



# BURG – A Code Generation Tool

- ▶ Bottom Up Rewriting based code Generator
- ▶ Sample BURG input.

```
%{
  #define NODEPTR_TYPE treepointer
  #define OP_LABEL(p) ((p)->op)
  #define LEFT_CHILD(p) ((p) -> left)
  #define RIGHT_CHILD(p) ((p) -> right)
  #define STATE_LABEL(p) ((p) -> state_label)
%}

%start goal
%term Assign=1 Constant=2 Fetch=3 Four=4
%term Mul=5 Plus=6

%%
con: Constant                = 1 (0);
con: Four                    = 2 (0);
non-terminals addr: con      = 3 (0);
addr: Plus(con, reg)         = 4 (0);
addr: Plus(con, Mul(Four, reg)) = 5 (0);
pattern reg: Fetch(addr)     = 6 (1);
reg: Assign(addr, reg)       = 7 (1);
goal: reg                    = 8 (0);
```

Annotations in the image:

- BURG's name for node type** points to `treepointer`.
- user's name for node type** points to `treepointer`.
- macros to traverse tree** points to the macro definitions.
- terminals** points to the terminal definitions.
- rule number** points to the numbers in the rule definitions.
- cost** points to the numbers in parentheses in the rule definitions.
- rule** points to the rule definitions.
- non-terminals** points to `addr`.
- pattern** points to `reg`.

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written by user around BURG generated functions.
    - ▶ Starts with the root of the subject tree and the non-terminal goal.
    - ▶ At each node selects a rule for evaluating the node.
    - ▶ Passes control back to user function with an integer identifying the rule. Actions corresponding to the rule to be managed by the user.

# BURG – A Code Generation Tool

- ▶ Here is an outline of a code-generator produced with the help of BURG. Constructs in red are BURG generated.

```
parse(NODEPTR_TYPE p) {  
    burg_label(p)    /* label the tree */  
    reduce(p, 1)     /* and reduce it, goal = 1*/  
}  
  
reduce(NODEPTR_TYPE p, int goalint) {  
    int ruleno = burg_rule(STATE_LABEL(p), goalint);  
    short *nts = burg_nts[ruleno];  
    NODEPTR_TYPE kids[10];  
    int i;  
    /* ... do something with this node... */  
    /* process the children of this node */  
    burg_kids(p, ruleno, kids);  
    for (i = 0; nts[i]; i++)  
        reduce(kids[i], nts[i]);  
}
```