CS738: Advanced Compiler Optimizations Static Single Assignment (SSA)

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Agenda

- SSA Form
- Constructing SSA form
- Properties and Applications

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 - May be assigned multiple times if program is executed

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 - Directly access points where relevant data flow information is avaliable

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 - ⇒ A use of a variable is reached by exactly one definition
- Control flow like traditional programs
- ► Some *magic* is needed at *join* nodes

Example

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i = 0;
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i = i + 1;
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j = i * 5;
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i1 = 0;

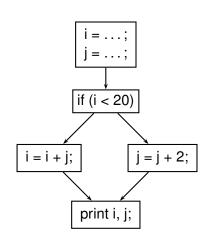
...

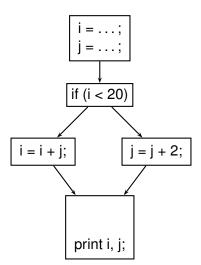
i2 = i1 + 1;

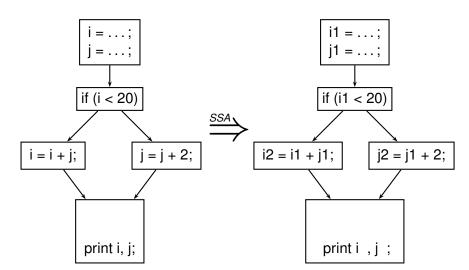
...

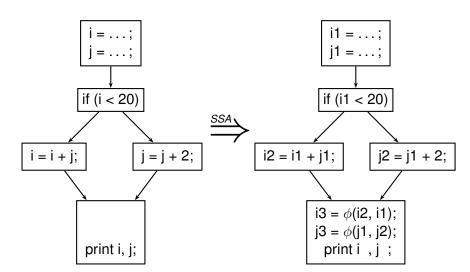
j1 = i2 * 5;
```

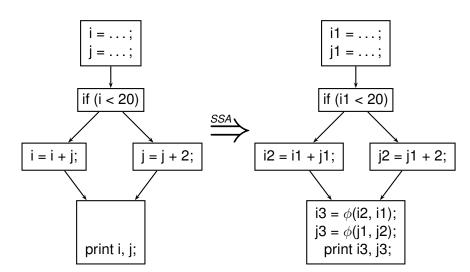
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i = ...;
j = ...;
if (i < 20)
   i = i + j;
else
   j = j + 2;
print i, j;</pre>
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```
j1 = \ldots;
if (i1 < 20)
  i2 = i1 + i1;
  j2 = j1 + 2;
i3 = \phi(i2, i1);
j3 = \phi(j1, j2);
print i3, j3;
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 - Practically, the inefficiency is compensated by dead code elimination and register allocation passes

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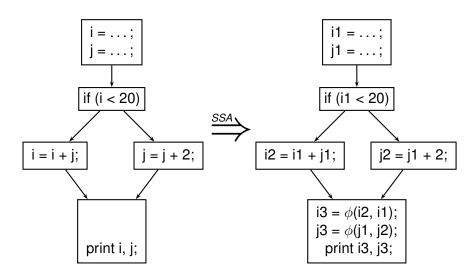
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- ightharpoonup n-ary ϕ function at n-way join node
- gets the value of i-th argument if control enters through i-th edge
 - \blacktriangleright Ordering of ϕ arguments according to the edge ordering is important

SSA Example (revisit)



Construction of SSA Form

Assumptions

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Computing Dominators

Equation

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Initial Conditions:

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where N is the set of all nodes, n_{Entry} is the node corresponding to the Entry block.

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Note that efficient methods exist for computing dominators



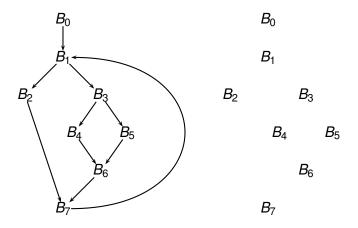
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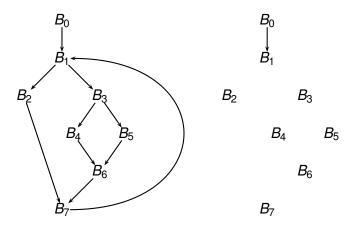
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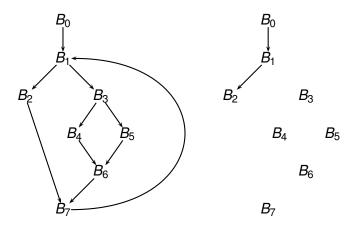
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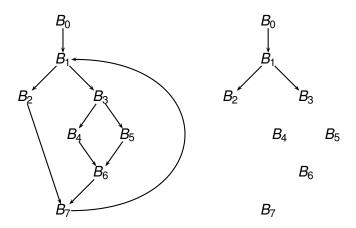
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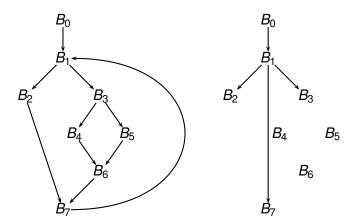
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- Dominator Tree
 - A tree showing all immediate dominator relationships

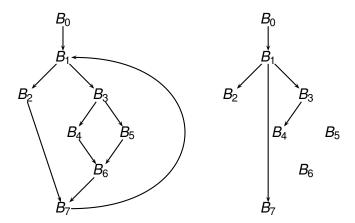


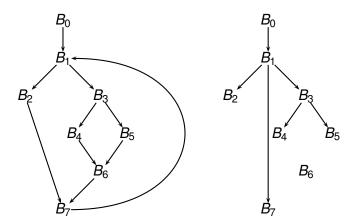


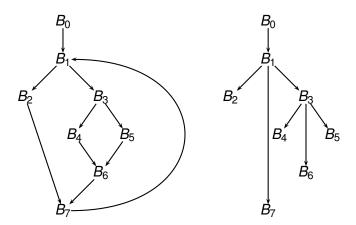












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- ▶ Why do you think DF(x) is important for any x?
 - Think about the information originated in *x*.

Computing DF

- PARENT(x) denotes parent of node x in the dominator tree.
- CHILDERN(x) denotes childern of node x in the dominator tree.
- PRED and SUCC from CFG.

$$\mathsf{DF}(x) = \mathsf{DF}_{\mathsf{local}}(x) \cup \left(\bigcup_{z \in \mathsf{CHILDERN}(x)} \mathsf{DF}_{\mathsf{up}}(z)\right)$$

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$$DF(S) = \bigcup_{x \in S} DF(x)$$

$$DF^{1}(S) = DF(S)$$

$$DF^{i+1}(S) = DF(S \cup DF^{i}(S))$$

▶ $DF^+(S)$ is the fixed point of DF^i computation.

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- Rename variables
- ▶ Why DF⁺? Why not only DF?

n-times

```
foreach variable \boldsymbol{v} {
```

```
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foreach variable v { S = Entry \cup \{B_n \mid v \text{ defined in } B_n\} Compute \mathsf{DF}^+(S) foreach n in \mathsf{DF}^+(S) { insert \phi-function for v at the start of B_n }
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