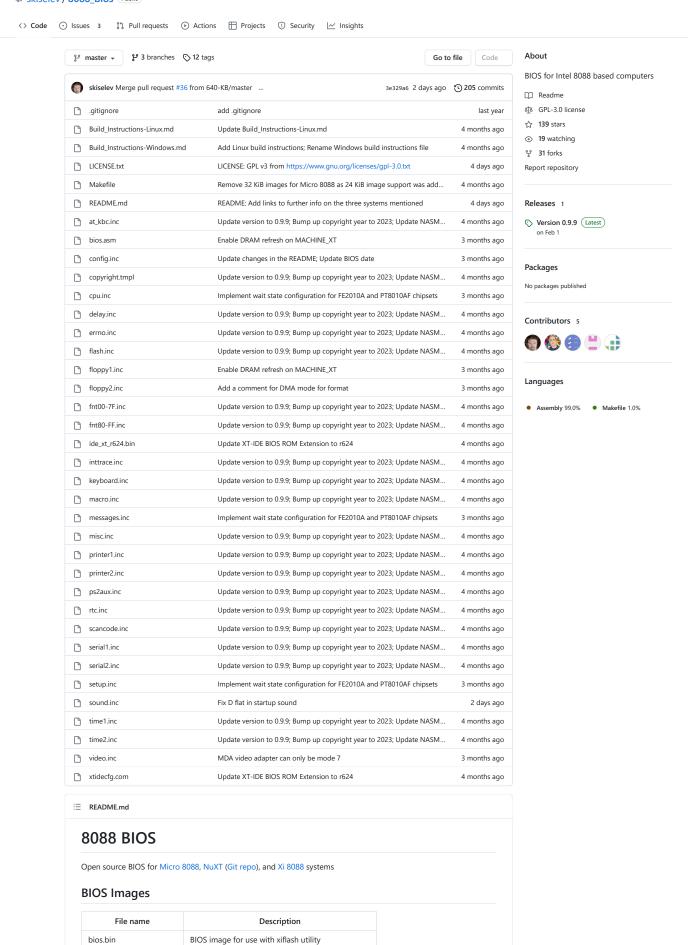
☐ skiselev / 8088\_bios Public



File name	Description
bios-micro8088.bin	BIOS image for Micro 8088 Version 1.1
bios-micro8088-xtide.bin	BIOS image for Micro 8088 Version 1.1 with XT-IDE
bios-xi8088.bin	BIOS image for Xi 8088 Version 2.0
bios-xi8088-xtide.bin	BIOS image for Xi 8088 Version 2.0 with XT-IDE
bios-sergey-xt.bin	BIOS image for Sergey's XT Version 1.0
bios-sergey-xt-xtide.bin	BIOS image for Sergey's XT Version 1.0 with XT-IDE

#### **Build Instructions**

- Linux Build Instructions
- · Windows Build Instructions

## **Release Notes**

## Changes

- Version 0.9.9
  - o Port floppy BIOS from Multi-Floppy BIOS extension
  - Add chipset detection for Faraday FE2010A and Proton PT8010AF chipsets. Disable memory refresh on these
    chipsets
  - o Add a setup option for configuring wait states for Faraday FE2010A and Proton PT8010AF chipsets
  - Add a setup configuration option to disable power-on memory test. Disabling the memory test speeds up the boot process
  - Add support for SST39SF020A and SST39SF040 Flash ROMs. Note that unused address lines on these chips need to be connected to the ground
  - Update XT-IDE BIOS ROM Extension to r624. Add corresponding xtidecfg.com utility. The XT-IDE BIOS ROM image provided with 8088 BIOS is configured for XT-CF-Lite at port 0x320. Use the xtidecfg.com utility to reconfigure that if needed prior to building the BIOS
  - o Fix keyboard scan codes for several key combinations: Ctrl-b, Ctrl-v, Ctrl-q, Alt-q, Ctrl-+
  - Fix an issue with BIOS resetting equipment list after VGA BIOS extension initialization, which resulted in a missing or incorrect video output then VGA is connected to a monochrome display
- Version 0.9.8
  - o Merge Micro 8088 and Xi 8088 to master branch (use same code base)
  - $\circ~$  Implement BIOS setup option for F0000-F7FFF area extension ROM scan
  - o Fix FDC data rate setting on systems with 360KB/720KB drives
  - Update the number of floppies in equipment\_word when saving configuration
  - $\circ \ \ \text{Revise POST codes. Display POST code prior to an action, such as device test or subsystem initialization}$
  - o Micro 8088: RTC autodetect fixes
  - $\circ~$  Micro 8088: Fix floppy drives configuration
  - o Xi 8088: AT / PS/2 keyboard controller initialization code improved
- Version 0.9.7
  - o Micro 8088: Implement RTC support and autodetect (based on the code contributed by Aitor Gomez)
  - o Micro 8088: Implement setting 40x25 CGA mode using DIP switches
  - o Xi 8088: Implement CPU clock frequency configuration in the BIOS setup
- Version 0.9.6
  - Micro 8088: Implement CPU clock frequency configuration in the BIOS setup utility (contributed by Aitor Gomez)
  - $\circ~\mbox{\rm Fix}$  the issue where BIOS would activate RTS when sending a character
  - o Fix the issue with 24 hours clock rollover
- Version 0.9.5
  - $\circ~$  Micro 8088: Fix reading serial port status
  - o Micro 8088: Implement checksum for the Flash ROM configuration space
  - o Fix booting without XTIDE Universal BIOS
- Version 0.9.4
  - o Micro 8088: Implement BIOS setup utility including saving configuration to the BIOS Flash ROM
  - o Improve build configurability
- Version 0.9.3
  - o Implement turbo mode switching using keyboard on Micro 8088
  - $\circ~$  Fix the issue where the number of serial and printer ports was not updated in the equipment word
  - o Update XT keyboard reset and buffer flush code
  - o Fix the issue where the BIOS would hang when one of the Lock keys is pressed
  - $\circ~$  Increase I/O delay in RTC code to solve 'FF' displayed in the year issue

- Version 0.9.2
  - o Update configuration mechanism to enable support of multiple target platforms
  - o Add initial support for Micro 8088 board using Faraday FE2010A chipset
- Version 0.9.0
  - o No updates except of BIOS date and version
- Version 0.8.2
  - o Fix keypad '\*' interpreted as print screen
  - Output '00' POST code to Port 80h when booting OS
  - o Use OKI-designed 80C88 and Harris-designed 80C88 instead older/newer 80C88
  - Add date and time setup to RTC setup utility
  - o NVRAM setup utility print help only if requested
  - o Minor bug fixes and readability improvements in floppy code
  - o Fix compilation errors with AT\_COMPAT and PS2\_MOUSE disabled
- Changes Version 0.8.1
  - Fix BIOS extension ROM scan procedure. Previously in some cases it was failing to initialize more than one BIOS extension ROM
- Changes Version 0.8
  - o Add serial port (INT 14h) support
  - o Add printer (INT 17h) support
  - Add print screen (INT 5) support
  - o Print BIOS extension ROM addresses on ROM initialization
  - o Add more POST checkpoints, update POST codes
  - o Rename Sergey's XT references to Xi 8088
- Version 0.7e
  - Set DS to the BIOS data segment after calling extension ROM initialization routines. Fixes the bug where POST would stuck following initialization of an extension ROM that doesn't preserve DS. (Reported by Bill Lewis)
- Version 0.7d
  - o Extension ROM scan
    - Include 0F0000h 0F7FFFh area in scan, so that extensions can be added to the system's flash.
  - o POST
    - Reset IOCHK trigger, disable turbo mode
- Version 0.7c
  - o IP
    - Fix boot sector signature address
    - Fix error when booting from floppy (call INT 13h AH=08h before boot)
  - o POST
    - Add DMA initialization
    - Skip memory test if ESC pressed
    - Skip memory test on warm reboot
  - o Keyboard / INT 09h
    - Add support for Ctrl-Break
    - Add support for Pause
  - o Video / INT 10h
    - Functions 06h and 07h: Improve scrolling implementation
    - Function 00h: Fix bug with clearing display in graphics modes
    - Use free font for graphics modes
    - Add graphics font for characters 80h-0FFh
  - o Floppy / INT 13h:
    - Use 2.88M settings for the default disk parameter table. Previosly 160K settings were used and BIOS was failing to boot from disks with more than 8 sectors per track.
    - More clean and effective fdc\_get\_result implementation, fdc\_wait\_input removed as it no longer needed
- Version 0.7b
  - o Initial public release

#### **TODO**

- (high) Add technical documentation
- (med) Finalize extended keyboard support full extended keyboard support
- (med) Beep if no video, install dummy handler
- (low) Xi 8088: Debug mouse issue with Intel 8242
- (low) Xi 8088: Debug issues with Microsoft and Logitech mouse drivers
- (low) Keyboard sound on buffer overflow
- (low) More tests RTC, memory, DMA

- (low) Init display before keyboard, so KBD errors can be displayed. Alternatively store non-fatal errors and display them after display is initialized
- (low) Check possibility of using same EBDA for XT-IDE BIOS and system BIOS PS/2 mouse functions
- (low) BIOS checksum
- (enh) Add PnP extension

# Switches and jumpers settings - Xi 8088

- SW2-8: Display adapter type:
  - o Off = CGA
  - o On = MDA or Hercules
  - o Ignored if Video BIOS is present (EGA / VGA cards)