PLC learning series 8: Instruction List programming

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Basics of Instruction list programming:

Instruction List (IL) is a low level textual language used in **PLC** system, which has a structure similar to a simple machine assembler.

A instructions list provides programs with each instruction on a new line as a sequence of instructions. Each instruction is made up of an operator followed by one or more operands, that is, the operator's subjects.

Example:

LD A (Load A)

AND B (And B)

ST Q (Store result in Q, i.e. output to Q)

In the first line of the program, LD is the operator, A the operand, and the words at the ends of program lines.

LD A is thus the instruction to load A into the memory register. It can then later be called on for further operations. The next line of the program has the Boolean operation AND performed with A and B. The last line has the result stored in Q, that is, output to Q.

Mnemonic codes for instruction list:

| Instruction | Function | Operand | |
|-------------|-----------------------------------|-------------------|--|
| LD | Load NO contact | X, Y, M, S, T, C | |
| LDI | Load NC contact | X, Y, M, S, T, C | |
| AND | Connect NO contact in series | X, Y, M, S, T, C | |
| ANI | Connect NC contact in series | X, Y, M, S, T, C | |
| OR | Connect NO contact in parallel | X, Y, M, S, T, C | |
| ORI | Connect NC contact in parallel | X, Y, M, S, T, C | |
| ANB | Connect a block in series | N/A | |
| ORB | Connect a block in parallel | N/A | |
| MPS | Start of branches. Stores current | N/A | |
| | result of program evaluation | | |
| MRD | Reads the stored current result | N/A | |
| | from previous MPS | | |
| MPP | End of branches. Pops (reads | (reads N/A | |
| | and resets) the stored result in | | |
| | previous MPS | | |
| OUT | Output coil | Y, S, M | |
| SET | Latches the ON status | Y, S, M | |
| RST | Resets contacts, registers or | Y, M, S, T, C, D, | |
| | coils | E, F | |
| MC | Master control Start | N0~N7 | |
| MCR | Master control Reset | N0~N7 | |
| END | Program End | N/A | |
| NOP | No operation | N/A | |
| Р | Pointer | P0~P255 | |
| I | Interrupt program pointer | I | |
| STL | Step ladder start instruction | S | |
| RET | Step ladder return instruction | N/A | |

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For operators, mnemonic codes are used, each code corresponding to an element operator / ladder. The codes used vary somewhat from manufacturer to manufacturer, although a standard was suggested and commonly accepted under IEC 1131-3.

Operands differ for some users which are listed below:

Instruction Code Mnemonics

| 0 | 33 | | 45 | 50 | 3 |
|---------------|------------|---------|---------|---|---|
| IEC 1131-3 | Mitsubishi | OMRON | Siemens | Operation | Ladder Diagram |
| LD | LD | LD | Α | Load operand into | Start a rung with |
| LDN | LDI | LD NOT | AN | result register. Load negative operand into result register. | open contacts. Start a rung with closed contacts. |
| AND | AND | AND | A | Boolean AND. | Series element with |
| ANDN | ANI | AND NOT | AN | Boolean AND with negative operand. | open contacts. Series element with closed contacts. |
| OR | OR | OR | 0 | Boolean OR. | Parallel element with open |
| ORN | ORI | OR NOT | ON | Boolean OR with negative operand. | contacts. Parallel element with closed contacts. An output. |
| ST | ОИТ | OUT | = | Store result register into operand. | |

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Rules for understanding IL:

- There is an instant spell check when entering keywords, separators and remarks.
- If a keyword, a separator or a comment is detected, it is identified with a color shading
- If unauthorized keywords (instructions or operators) are entered, color shading will also identify this.

There is no syntax impact on spaces and tabs, they can be used anywhere.

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