



**Luke Wren**

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Interests: C, Verilog, Python Dislikes: C, Verilog, Python

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## Achievements



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Overview Repositories 80 Projects Packages Stars 45 Sponsoring

## Pinned



**RISCBoy** Public

Portable games console, designed from scratch: CPU, graphics, PCB, and the kitchen sink

C 177 10



**PicoDVI** Public

Bitbanged DVI on the RP2040 Microcontroller

C 822 83



**Hazard3** Public

3-stage RV32IMACZb\* processor with debug

Verilog 56 9



**Pico-DVI-Sock** Public

A DVI Sock board for Pico

128 20



**asciwave** Public

Turn WaveDrom timing diagrams into ASCII art

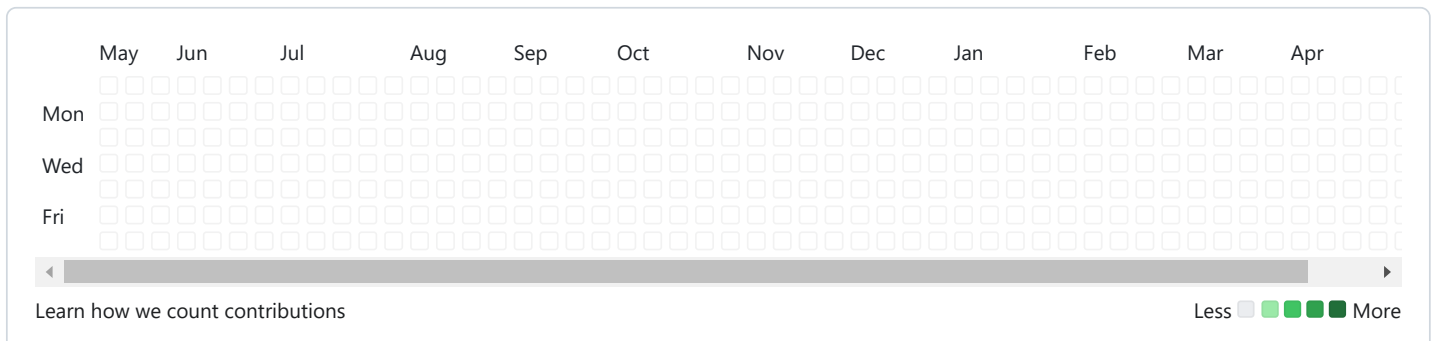
☐ Python    115    5

 **NorForkConditionally**   Public

What's the simplest CPU you can build?

☐ C++    30

## 454 contributions in the last year



## Contribution activity

May 2023

 Opened 1 pull request in 1 repository ① open  
[raspberrypi/picoprobe](#)  
 PIO program improvements May 9

 4 contributions in private repositories May 4 – May 7

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