

New issue

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possible bugs in figures A.20, A.21 #795

Open

aswaterman opened this issue on Dec 16, 2021 · 1 comment

Assignees



aswaterman commented on Dec 16, 2021

Member

I'm copying this from @jrahmeh's issue here: [riscv/riscv-v-spec#765](#)

In section A.7 of the unprivileged ISA (Document Version 20191214-draft), both examples (Figures A.20 and A.21) seem to be incorrect.

In A.20, for outcome a0=1 to occur, a2 would have to be 1, but a2 is initialized to zero and remains zero after the amoswap at (e). One fix is to replace (h) with: sw t1, 0(s0)

In A.21, the outcome "a0=0, a1=1, a2=0" is not forbidden by operational model, it is obtainable with the global memory order: a b c d e f.

One possible fix is to change the outcome to: "a0=1, a1=1, a2=0" and change (f) to: sw t1, 0(s0)

aswaterman mentioned this issue on Dec 16, 2021

Improvements to examples in Konwn Issues section of unprivileged ISA. [riscv/riscv-v-spec#765](#)

Closed


aswaterman assigned daniellustig on Dec 17, 2021

skvadrik commented 9 days ago

I am also confused by these examples. A few more notes:

- In A21, t1 for hart 1 is unused (but it would be used with the fix fo (f) suggested by @jrahmeh)

Assignees

 daniellustig

Labels

None yet

Projects

None yet

Milestone

No milestone

Development

No branches or pull requests

3 participants

