# A Standalone Programmable Signal Processing Unit for Versatile Characterization of MEMS Gyroscopes

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Abstract—This paper reports a stand-alone signal processing and control unit designed to provide flexible characterization of MEMS vibratory gyroscopes. The unit consists of a programmable 32-bit 150 MIPS DSP controller, 16-bit 1 MSPS digital-to-analog and 18-bit analog-to-digital interface circuits, and signal conditioning electronics. The multi-channel analogto-digital interface is optimized for detection of small electrical signals typical for MEMS devices. Digitally controlled conditioning of analog signals allows for high-resolution differential digitization of a wide range of detection signals. The digital-toanalog interface circuit produces a wide range of DC and AC voltages needed for actuation and detection in gyroscopes; a single 5 V supply is used to power the board. The DSP controller allows easy MATLAB/Simulink programming and execution-time data exchange. Performance of the board was experimentally characterized using an anti-phase driven rate gyroscope with multi-degree of freedom sense mode. Using 16-bit conversion, the measured capacitance-change equivalent resolution is 27 aF/VHz. Due to its flexible architecture, the unit is easily customizable for stand-alone and computer controlled operation of a variety of dynamic MEMS.

### I. Introduction

Discrete bench-top instruments such as dynamic signal analyzers and lock-in amplifiers are often used for initial structural and Coriolis characterization of micro-machined gyroscopes [1-2]. However, this approach is not practical for stand-alone field-testing of prototypes and does not allow fast and flexible evaluation of different actuation, detection and control algorithms. General-purpose Digital Signal Processing (DSP) systems such as dSPACE [3] and National Instruments Compact RIO [4] can provide a powerful control solution, but have limited portability, are costly and not optimized to interface capacitive MEMS. Custom made integrated and Printed Circuit Board (PCB) level electronics are commonly used for stand-alone operation of gyroscopes [5-7]; however, change of operational parameters or signal processing and control algorithms often involves circuit redesign and reassembly with different electrical components. In this paper, we report an easily programmable and computer-interfaced yet compact signal processing and control

platform for capacitive micro-machined gyroscopes and other dynamic MEMS.

## II. ELECTRONICS DESIGN

The main hardware components of the proposed platform are a programmable DSP controller, digital-to-analog and analog-to-digital interface circuits equipped with signal conditioning analog electronics, see Fig. 1. The unit was assembled using off the shelf components on a single 112 x 87 mm six-layer PCB. Below we discuss design and implementation of the major circuit blocks and choice of particular components. The main properties of the unit are summarized in Table I.

#### A. Processor

A highly integrated Texas Instruments TMS320F2812 single-chip DSP controller was chosen for the board due to its performance/cost efficiency. This DSP has a maximum internal frequency of 150 MHz (i.e., 6.67 ns cycle time) stabilized by a 30 MHz external quartz resonator, and is equipped with 128 K x 16 Flash memory, 18 K x 16 Single-Access RAM (SARAM). A 16-bit bus with independent data and address transmission lines interfaces the processor with on-board DACs and ADCs. A Serial Peripheral Interface (SPI) and discrete glue logic is used to digitally control potentiometers in the independent analog signal conditioning circuits. The board is linked to a host computer for execution-time data exchange and adjustment of signal processing parameters using an external RS-232C transceiver and an internal Serial Communication Interface (SCI). Programming of the processor is done using a JTAG port.

# B. Digital-to-analog Interface Circuit

The purpose of the digital-to-analog interface circuit is to convert the digital signals generated by the processor into analog waveforms to control gyroscopes and to provide connectivity to external measurement equipment. Based on the high-speed 1 MSPS 16-bit digital-to-analog converter (DAC) TI DAC8820, three types of digital-to-analog interface circuits with different analog signal conditioning were incorporated on the board.

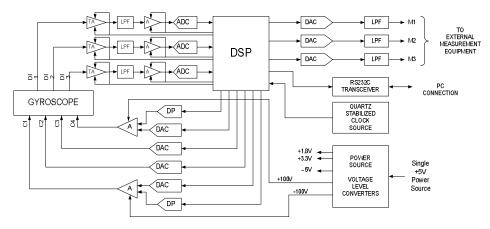


Figure 1. Block diagram of the main hardware components

Two identical but independent actuation channels were designed in order to accommodate gyroscopes with multi-degree of freedom drive modes. Each of the channels consists of a DAC and an additional circuit, which amplifies the generated analog signal and adds a digitally controlled DC bias. These control channels are able to output arbitrary waveform actuation voltages in the range from 0 to  $\pm 100$  V.

Two additional arbitrary waveform low-voltage (-5 to +5 V) channels each based on a simple buffer at the DAC output were also designed to provide accurate probing voltages (carriers) necessary for detection of motion in dynamic microstructures.

A separate group of DAC channels was implemented to enable connectivity of the DSP board to external measurement equipment, such as dynamic signal analyzers, for debugging of algorithms and monitoring of experiments in real time. In these channels, the DACs are followed by high quality four-pole Low-Pass Filters (LPF) to suppress the sampling rate signal. The output signals range from -5 to +5 V.

### C. Analog-to-digital Interface Circuit

Capacitive sensing of drive and sense mode vibratory motion in gyroscopes is typically based on measuring the current induced by the relative motion of capacitive electrodes. To accurately digitize pick-up currents from microgyroscopes, three independent channels were designed and implemented each based on a three-stage fully differential trans-impedance amplifier and a high speed 18-bit ADC.

The three amplification stages are based on TI THS4141 high-speed fully differential amplifiers with 84 dB common mode rejection. The first stage converts the difference of the input currents into a voltage difference across its two outputs with a digitally controlled trans-impedance gain of  $100-110~\rm k\Omega$ . The second stage is a fully differential two-pole antialiasing LPF with the  $-3~\rm dB$  cutoff frequency of approximately 50 kHz. The last stage is a variable gain voltage amplifier. The gain is defined by Digitally Controlled Potentiometers (DCP) AD5290 and can range from 0 to 20 dB. A high-speed ADC TI ADS8482 was used in each detection

channel to convert the analog voltage difference to the digital code with a sampling rate up to 1 MSPS.

## D. Power Handling

A single +5 V stabilized external DC voltage source powers the board. All other voltage levels necessary for operation are formed on the board by the dedicated power converters. To power the analog amplifiers, an additional -5 V voltage is generated onboard. A pair of -100 and +100 V is also generated to power the high-voltage amplifiers in the control voltage channels. An additional +1.9 V is used to power the DSP core, and +3.3 V is used for the digital circuits.

### III. EXPERIMENTAL DEMONSTRATION

In this section we report preliminary characterization of both drive and sense mode functionality of the board with a MEMS gyroscope using electromechanical amplitude modulation (EAM) detection technique, see, for example, [8].

## A. Hardware and Software Configuration

Fig. 2 shows a photograph of a fully assembled signal processing unit and highlights its main components. The ADCs were configured for 16-bit conversions. A MATLAB-based Graphical User Interface (GUI) was developed for the real-time control of the trans-impedance gains, value of the DC component of the driving voltage, as well as amplitudes

TABLE I. MAIN OPERATIONAL PARAMETERS

DSP controller TI TMS320F2812	32-bit 150 MIPS
ADC and DAC conversion update rate	100 kHz
2 DAC AC carrier channels	10 Vp-p, 0.15 mV step
2 DAC AC+DC actuation channels	±100 V, 0.8 mV step
3 DAC monitor channels	10 Vp-p, 0.15 mV step
3 differential ADC with trans-	1—10 MΩ gain,
impedance amplifiers	18-bit conversion

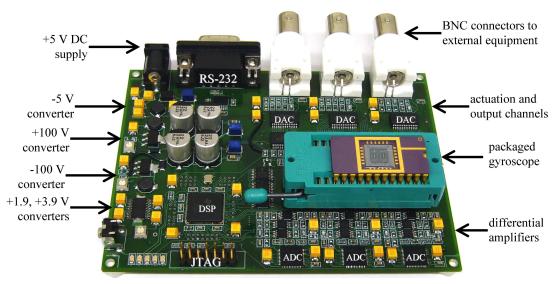


Figure 2. A photograph of an assembled controller board with a packaged gyroscope.

and frequencies of the AC driving and carrier voltages. The interface can be easily modified to control any run-time variables.

#### B. Test Device

An anti-phase driven rate gyroscope with multi-degree of freedom sense mode [9] was used for the experiments. The gyroscope's drive mode consists of two coupled frames driven into anti-phase resonance using a common lateral comb drive electrode in the center of the device. Two separate 2-DOF sense mode resonators are located inside of the drive mode decoupling frames. The drive mode resonant frequency is designed to be in-between the 2-DOF sense mode resonant frequencies for robust off-resonant operation and anti-phase detection of the input angular rate. Fig. 3 shows an SEM of a device fabricated in-house using an SOI process [9]. The gyroscope was packaged and wire-bonded in a

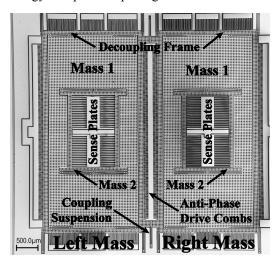


Figure 3. SEM of the gyroscope used for the experimental evaluation of the board.

CDIP-24 package and tested in atmospheric pressure (drive mode-quality factor was approximately 300, sense-mode effective quality factors were approximately 40).

#### C. Drive Mode Detection

In order to actuate the anti-phase motion in the drive mode of the gyroscope, a driving voltage was applied to the central anchored lateral comb electrode. The drive voltage consisted of a 37.5 V DC bias and an AC component at the 1.568 kHz resonant frequency of the device. Using the real-time computer graphical user interface, the amplitude of the AC driving component was adjusted to 14.75 V to achieve the vibration with a nominal 6  $\mu$ m amplitude. A 5 V AC carrier voltage at 20 kHz frequency was applied to the movable mass of the gyroscope to enable EAM detection of motion.

Anti-phase motion of the gyroscope's decoupling frames was detected using drive-mode parallel plate detection capacitors. The pick-up currents from the anchored electrodes were amplified using the on-board three-stage differential transimpedance amplifier and digitized. Fig. 4 shows Power Spectral Density (PSD) of the drive-mode pick-up signals for the cases of single-sided and fully differential capacitive detection. The single-sided detection signal contains feed-through of drive and carrier AC voltages, as well as multiple informational sidebands inherent to parallel plate detection of sinusoidal motion [8].

Differential detection with independently tuned gains has several practical advantages. Unwanted parasitic feed-through of the drive AC signal was suppressed by almost 20 dB. Also, the white noise floor was improved by approximately 10 dB. Most importantly, the 20 kHz carrier signal was suppressed by more than 30 dB to the level below the main informational sidebands, thus improving the useable dynamic range.

In the described experiment, the nominal parallel plate sense capacitance of the gyroscope's drive mode was 0.22

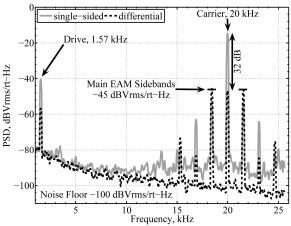


Figure 4. Experimentally measured PSD of the drive-mode modulated pick-up signal during 6 µm amplitude vibrations.

pF. During the 6  $\mu$ m amplitude vibration, the main harmonic of the parallel plate capacitance change had an amplitude of 0.15 pF [8]. For the tested gyroscope, the measured displacement-equivalent resolution was 1.13 nm/ $\sqrt{Hz}$ . By normalizing to the gyroscope's parameters, capacitance-change equivalent resolution of the board with 16-bit ADC was derived to be 0.027 fF/ $\sqrt{Hz}$ .

#### D. Sense Mode Detection

Preliminary characterization of the Coriolis detection functionality of the board was also performed using the antiphase driven gyroscope [9]. Actuation of the drive mode vibration was done as in the previously described experiments; the detection channels were switched from the drive mode to the two differential sense mode capacitors inside one of the decoupling frames. The board was mounted on a rate table, which was configured to produce sinusoidal rotation of a fixed amplitude and frequency. Two different experiments were performed: 3.6 deg rotation at 5 Hz frequency, and 7.2 deg rotation at 2.5 Hz frequency. In both cases, the amplitude of the applied sinusoidal angular rate was 18 deg/s.

Fig. 5 shows PSD of the sense-mode pick-up signal around the left informational sideband. In each experiment, the signal contains two angular rate-modulated sidebands, and a 40 deg/s uncompensated quadrature. The demonstrated resolution is sufficient for scale factor characterization of gyroscopes in various temperature and pressure conditions; it can be improved by using the built-in 18-bit conversion capability, performing demodulation of the EAM signal digitally before outputting analog measurements, increasing the maximum trans-impedance gains, and increasing amplitude and frequency of the carrier AC voltage.

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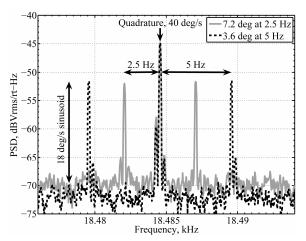


Figure 5. Experimentally measured PSD of the sense-mode modulated pick-up signal during sinusoidal rotation.

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