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My Corporation

SPECrate2017_fp_base = 4.60

SPECrate2017_fp_peak Not Run

CPU2017 License: nnn (Your SPEC license number)

Test Sponsor: My Corporation **Tested by:** My Corporation

Test Date: Nov-2017

Hardware Availability: Software Availability:

Errors (Continued)

538.imagick_r (base) did not have enough runs! 511.povray_r (base) did not have enough runs! 526.blender_r (base) did not have enough runs! 510.parest_r (base) did not have enough runs! 549.fotonik3d_r (base) did not have enough runs! 521.wrf_r (base) did not have enough runs! 519.lbm_r (base) did not have enough runs!

554.roms_r (base) did not have enough runs!

Results Table

	Base							Peak						
Benchmark	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r)/								
507.cactuBSSN_r			_			\n'\								
508.namd_r	1	<u>206</u>	4.60	206	4.61	207	4.59							
510.parest_r					N	Y								
511.povray_r		Λ	\bigcup											
519.lbm_r														
521.wrf_r		1//												
526.blender_r	\wedge	//	\)										
527.cam4_r			7/											
538.imagick_r														
544.nab_r	/		7											
549.fotonik3d_r														
\$54.roms_1		7												

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/mnt/freezedisk/llvm-intptr-br2-correct2-release/lib"
LIBRARY_PATH = "/mnt/freezedisk/llvm-intptr-br2-correct2-release/lib"

PATH = */mmt/freezedisk/llvm-intptr-br2-correct2-release/bin:/mmt/freezedisk/speccpu2017-intptr/bin:/usr/local/sbin:/usr/local/sbin:/usr/sbin:/usr/sbin:/bin:/usr/sbin:/usr/games:/usr/local/games:/snap/bin:/snap/bin:/usr/sbin:/

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Platform Notes

```
Sysinfo program /mnt/freezedisk/speccpu2017-intptr/bin/sysinfo Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f running on freeze4 Fri Nov 3 18:01:19 2017
```

```
SUT (System Under Test) info as seen by some common utilities. For more information on this section, see
```

https://www.spec.org/cpu2017/Docs/confightml#sysinfo

```
From /proc/cpuinfo model name : Intel(R) Core(TM i3-6100 CPU @ 3.7 1 "physical id"s (chips)
```

2 "processors"

cores, siblings (Caution: countying these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 2
siblings : 2
physical 0: cores 0 1

From lscpu:

Architecture:
CPU op-mode(s):
Byte Order:
CPU(s):

CPU(s):

Architecture:
Architecture

On-line CPU(s) list: 0.
Thread(s) per core: 1
Core(s) per socket: 2
Socket(s): 1
NUMA node(s): 1

Vendor ID: GenuineIntel

CPU family: 6
Model: 94

model name: / Intel(R) Core(TM) i3-6100 CPU @ 3.70GHz

3700.000

0,1

Stepping: 3

NUMA node0 CPU(s):

CPU MHx:

CPU max MMz: 3700.0000 CPU min MHz: 800.0000 BogoMIPS: 7392.16 Virtualization: x-TV32K cache: Lli cache: 32K L2 cache: 256K L3 cache: 3072K

Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx est tm2 ssse3 sdbg fma

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Platform Notes (Continued)

cx16 xtpr pdcm pcid sse4_1 sse4_2 x2apic movbe popcht tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch epb intel_pt tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmil avx2 smep bmi2 erms invpcid mpx rdseed adx smap clflushopt xsaveopt xsavec xgetbvl otherm arat pln pts hwp hwp_notify hwp_act_window hwp epp

```
/proc/cpuinfo cache data
   cache size : 3072 KB
From numactl --hardware WARNING; a numadtl
                                             'node'
                                                    might or might not correspond to a
physical chip.
  available: 1 nodes (0)
 node 0 cpus: 0 1
 node 0 size: 7676 MB
 node 0 free: 7226 MB
 node distances:
 node
       0
    0: 10
From /proc/meminfo
   MemTotal:
                     7860684
   HugePages_Total:
                       2048 ki
   Hugepagesize:
/usr/bin/lsb_rglease
   Ubuntu 16.04.3 LTS
From /etc/*release* /etc/*version*
   debian version: stretch/sid
   os-release
      NAME="Ubuntu
      VERSION="16,04.3 LTS (Xenial Xerus)"
      ID=ubuntu
      ID_LIKE=debian
      PRETTY_NAME="Ubuntu 16.04.3 LTS"
      VERSION ID="16.04"
      HOME_VRL="http://www.ubuntu.com/"
      SUPPORT_URL="http://help.ubuntu.com/"
   Linux freeze4 4.4.0-97-generic #120-Ubuntu SMP Tue Sep 19 17:28:18 UTC 2017 x86_64
   x86_64 x86_64 GNU/Linux
run-level 3 Oct 30 07:30
```

(Continued on next page)

Type Size Used Avail Use% Mounted on

SPEC is set to: /mnt/freezedisk/speccpu2017-intptr

Filesystem

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Platform Notes (Continued)

/dev/sda1 ext4 230G 59G 159G 27% /mnt/fxeezedisk

Additional information from dmidecode follows. WARNING: Use caudion when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

(End of data from sysinfo program)

Compiler Version Notes

CXXC 508.namd_r(base)

clang version 6.0.0 (https://github.com/aqjune/clang-intptr.git

b099f9b86954800bcbdee427fc703eb8a30389da)

(https://github.com/aqjune/llvm-intptr.git

072bcfdd83ea02b321f72fc636d5a8384325de92)

Target: x86 64-unknown-linux-guu

Thread model: posix

InstalledDir: /mnt/freezedisk/llvmintptr-br2-correct2-release/bin

Found candidate GCC installation: /usr/lib/gcc/x86_64-linux-gnu/5

Found candidate GCC installation: /usr/lib/gcc/x86_64-linux-gnu/5.4.0

Found candidate GCC installation: /usr/lib/gcc/x86_64-linux-gnu/6

Found candidate GCC installation: /usr/lib/gcc/x86_64-linux-gnu/6.0.0

Selected GCC installation /usr/lib/gcc/x86_64-linux-gnu/5.4.0

Candidate multilib: .;@m64 Selected multilib: .;@m64

Base Runtime Environment

C++ benchmarks:

508.namd_r: No Plags used

Base Compiler Invocation

C++ benchmarks:

508.namd_r: clang++

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Base Portability Flags

508.namd_r: -DSPEC LP64

Base Optimization Flags

C++ benchmarks:

508.namd_r: -m64 -O3 -mavx -z muldefs

Base Other Flags

C++ benchmarks:

508.namd_r: No flags used

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2017-11-03 18:01:19+0900.

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