

Errors

'reportable' flag not set during run

523.xalancbmk r (base) did not have enough runs!

557.xz_r (base) did not have enough runs!

548.exchange2_r (base) did not have enough runs!

505.mcf_r (base) did not have enough runs!

541.leela_r (base) did not have enough runs!

525.x264_r (base) did not have enough runs!

500.perlbench_r (base) did not have enough runs!

531.deepsjeng_r (base) did not have enough runs!

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SPECrate2017_int_base = 3.35

SPECrate2017_int_peak **∜**ot Run

CPU2017 License: nnn (Your SPEC license number)

Test Sponsor: My Corporation Tested by: My Corporation Test Date: Nov-2017

Hardware Availability: Software Availability:

Errors (Continued)

502.gcc r (base) did not have enough runs!

Results Table

	Base							Peak						
Benchmark	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r								\searrow						
502.gcc_r														
505.mcf_r								$\mathbb{N}^{\mathbb{N}}$						
520.omnetpp_r	1	390	3.36	<u>391</u>	3.35	393	3.34							
523.xalancbmk_r							$\overline{}$	ſ						
525.x264_r			>				n							
531.deepsjeng_r														
541.leela_r						1/								
548.exchange2_r						\searrow								
557.xz_r				\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \										

SPECrate2017_int_base = SPECrate2017_int_peak/7

Not Run

3.35

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option ubmit' was used.

General Notes

Environment variables set by runcpu before the start of the run: LD_LIBRARY_RATH = \"/mnt/freezedisk/llvm-intptr-base-5.0-release/lib" LIBRARY_PATH = "/mat/freezedisk/llvm-intptr-base-5.0-release/lib"

disk/speccpu2017-intptr/bin:/usr/local/sbin:/usr/local/bin:/usr/sbin:/usr/bin:/bin:/bin:/usr/games:/usr/local/games:/snap/bin:/snap/bin

Platform Notes

Sysinfo program /mnt/freezedisk/speccpu2017-intptr/bin/sysinfo Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f running on freeze3 Wed Nov 1 05:41:47 2017

SUT (System Under Test) info as seen by some common utilities. For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

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SPECrate2017_int_peak **∀**ot Run

CPU2017 License: nnn (Your SPEC license number)

My Corporation My Corporation Test Date: Nov-2017 Hardware Availability:

Software Availability:

Platform Notes (Continued)

```
From /proc/cpuinfo
  model name : Intel(R) Core(TM) i7-7700 CPU @ 3.60GHz
      1 "physical id"s (chips)
      4 "processors"
   cores, siblings (Caution: counting these is hw and system dependent. The following
   excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
      cpu cores : 4
      siblings : 4
     physical 0: cores 0 1 2
```

GenuineIntel

From lscpu:

Test Sponsor:

Tested by:

%2-bit<mark>/</mark>/ CPU op-mode(s): Byte Order: Little Endian CPU(s): On-line CPU(s) list: 0 Thread(s) per core: 1 Core(s) per socket: Socket(s): NUMA node(s):

Vendor ID: CPU family:

Architecture:

Model: Model name;

Intel(R) Core(TM) i7-7700 CPU @ 3.60GHz Stepping: 3566.250 CPU MHz;

4200.0000 CPU max MHz: 800.0000 CPU min MHz: BogOMIPS: 7199.62 Virtualization: VT-x 1/1d cache: 32K Zi cache: 32K L2 cache: 256K L3 cache: 8192K NUMA node CPU(s): 0 - 3

fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov Flags: pat psg36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc aper fmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch epb intel_pt tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm mpx rdseed adx smap clflushopt xsaveopt xsavec xgetbvl dtherm arat pln pts hwp hwp_notify hwp_act_window hwp_epp

/proc/cpuinfo cache data cache size : 8192 KB

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Test Sponsor:

Tested by:

SPECrate2017_int_base = 3...

SPECrate2017_int_peak Not Run

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Test Date: Nov-2017

Hardware Availability: Software Availability:

Platform Notes (Continued)

```
From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
  available: 1 nodes (0)
  node 0 cpus: 0 1 2 3
  node 0 size: 7687 MB
  node 0 free: 7245 MB
  node distances:
 node
       Ω
    0: 10
From /proc/meminfo
   MemTotal:
   HugePages_Total:
                       2048 k
   Hugepagesize:
/usr/bin/lsb_release -d
   Ubuntu 16.04.3 LTS
From /etc/*release* /etc/*version*
   debian_version: stretch/sid
   os-release:
      NAME="Ubuntu
      VERSION="16.04.3 LTS (Xenial Xerus)"
      ID=ubuntx
      ID_LIKE debian
      PRETTY_NAME="Ubuntu 16.04.3 LTS"
      VERSION_ID="16.04"
      HOME_URL="http://www.ubuntu.com/"
      SUPPORT_URL="http://help.ubuntu.com/"
uname/~a:
   Linux freeze3 4.4.0-97-generic #120-Ubuntu SMP Tue Sep 19 17:28:18 UTC 2017 x86_64
   x86_64 x86_64 GNU/Linux
run level 3 Oct 29 18:30
SPEC is set to: /mnt/freezedisk/speccpu2017-intptr
                  Type Size Used Avail Use% Mounted on
   Filesystem
                        939G 716G 176G 81% /mnt/freezedisk
   /dev/sdb1
                  ext4
Additional information from dmidecode follows. WARNING: Use caution when you interpret
```

this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

(End of data from sysinfo program)

SPEC CPU2017 Integer Rate Result Copyright 2017 Standard Performance Evaluation Corporation My Corporation SPECrate2017_int_base = SPECrate2017_int_peak **∜**ot Run CPU2017 License: nnn (Your SPEC license number) Test Date: Nov-2017 **Test Sponsor:** My Corporation Hardware Availability: Software Availability: **Tested by:** My Corporation Compiler Version Notes CXXC 520.omnetpp_r(base) clang version 6.0.0 (https://github.com/ajune/clang-intptr. b099f9b86954800bcbdee427fc703eb8a3038 (https://github.com/aqjune/llvm-intptr.git d3762c4675cbe5178acef79f01c813bcbfc58cf5 Target: x86_64-unknown-linux-gnu Thread model: posix InstalledDir: /mnt/freezedisk/l(lvm-intptr-base-5.0-rel/ease/bin Found candidate GCC installation: /usr/lib/gcc/x86_64-linux-gnu/5 Found candidate GCC installation: /usrxlib/gcc/x86_64-linux-gnu/5.4.0 Found candidate GCC instal ation: /wsx/lb/gcc/x86_64-linux-gnu/6 Found candidate GCC installation: /usr/lib/gcc/x86_64-linux-gnu/6.0.0 Selected GCC installation: /usx/llb/gcc/x86_64/linux-gnu/5.4.0 Candidate multilib: .;@m64 Selected multilib: .;@m64 **Base Runtime Environment** C++ benchmarks: 520.omnetpp r: No flags use **Base Compiler Invocation** C++ benchmarks: 520 omnetpp_r. clangt **Base Portability Flags** 520.omnetpp_r: -DSPEC LP64 **Base Optimization Flags** C++ benchmarks: (Continued on next page) Page 5 https://www.spec.org/ Standard Performance Evaluation Corporation (info@spec.org)

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SPECrate2017_int_base = 3

SPECrate2017_int_peak Not Run

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Test Sponsor: My Corporation **Tested by:** My Corporation

Test Date: Nov-2017

Hardware Availability: Software Availability:

Base Optimization Flags (Continued)

520.omnetpp_r: -m64 -O3 -mavx -z muldefs

Base Other Flags

C++ benchmarks:

520.omnetpp_r: No flags used

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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