

# SPEC® CPU2017 Integer Rate Result

Copyright 2017 Standard Performance Evaluation Corporation

My Corporation

SPECrate2017\_int\_base = 3.83

SPECrate2017\_int\_peak = Not Run

CPU2017 License: nnn (Your SPEC license number)

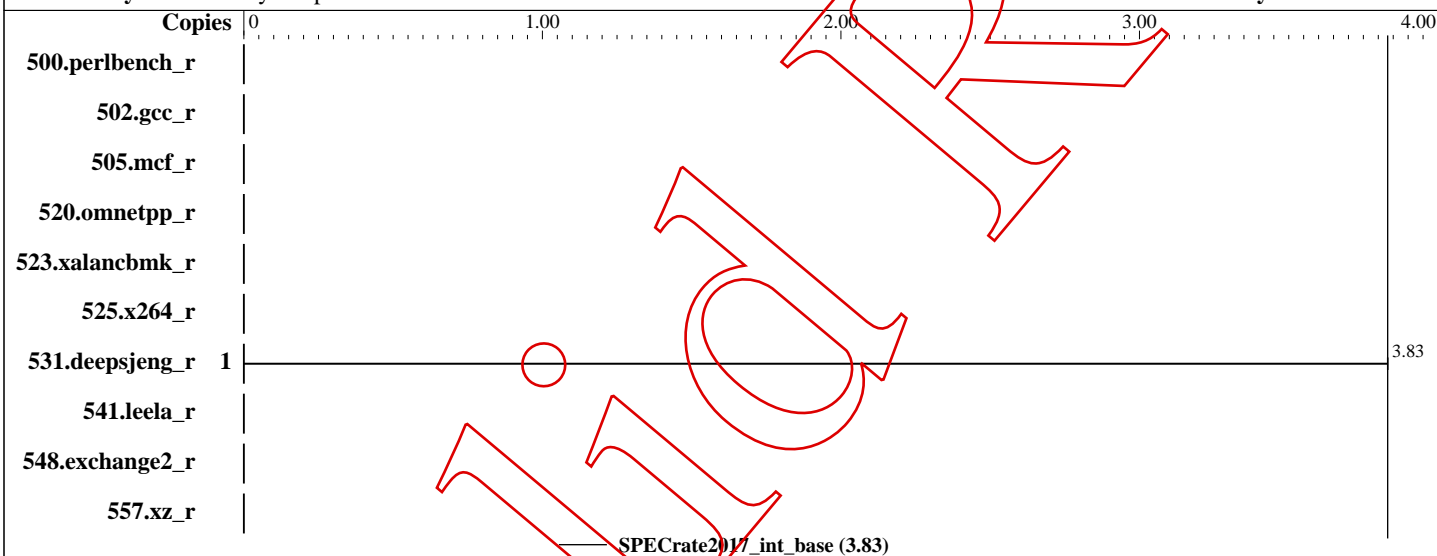
Test Sponsor: My Corporation

Tested by: My Corporation

Test Date: Nov-2017

Hardware Availability:

Software Availability:



## Hardware

CPU Name: Intel Core i5-6600

Max MHz.: 3.90

Nominal: 3.90

Enabled: 4 cores, 1 chip, threads/core

Orderable: 4

Cache L1: 32 KB

L2: 256 KB

L3: 8 MB

Other:

Memory: 7.676 GB fixme: If using DDR3, format is: 'N GB (M x N GB nRxn PCn-nnnnnR-n, ECC)'

Storage: 27 TB add more disk info here

Other:

## Software

OS: Ubuntu 16.04.1 LTS

4.4.0-31-generic

Compiler: C/C++: Version 3.9.0 of Clang, the LLVM Compiler Infrastructure  
Fortran: Version 4.8.2 of GCC, the GNU Compiler Collection  
DragonEgg: Version 3.5.2, the LLVM Compiler Infrastructure

Parallel: No

Firmware:

File System: ext4

System State: Run level 5 (add definition here)

Base Pointers: 64-bit

Peak Pointers: Not Applicable

Other:

## Errors

'reportable' flag not set during run

505.mcf\_r (base) did not have enough runs!

557.xz\_r (base) did not have enough runs!

548.exchange2\_r (base) did not have enough runs!

502.gcc\_r (base) did not have enough runs!

525.x264\_r (base) did not have enough runs!

523.xalancbmk\_r (base) did not have enough runs!

541.leela\_r (base) did not have enough runs!

500.perlbench\_r (base) did not have enough runs!

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520.omnetpp\_r (base) did not have enough runs!

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r														
502.gcc_r														
505.mcf_r														
520.omnetpp_r														
523.xalancbmk_r														
525.x264_r														
531.deepsjeng_r	1	<u>299</u>	<u>3.83</u>	299	3.83	299	3.83							
541.leela_r														
548.exchange2_r														
557.xz_r														

**SPECrate2017\_int\_peak** = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

The config file option 'submit' was used.

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/mnt/freezedisk/llvm-intptr-br2-correct2-release/lib"
LIBRARY_PATH = "/mnt/freezedisk/llvm-intptr-br2-correct2-release/lib"
```

```
Sysinfo program /mnt/freezedisk/speccpu2017-intptr/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on ubuntu-freeze2 Fri Nov 3 16:59:37 2017
```

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

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## Platform Notes (Continued)

From /proc/cpuinfo

model name : Intel(R) Core(TM) i5-6600 CPU @ 3.30GHz

1 "physical id"s (chips)

4 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 4

siblings : 4

physical 0: cores 0 1 2 3

From lscpu:

Architecture: x86\_64

CPU op-mode(s): 32-bit 64-bit

Byte Order: Little Endian

CPU(s): 4

On-line CPU(s) list: 0-3

Thread(s) per core: 1

Core(s) per socket: 4

Socket(s): 1

NUMA node(s): 1

Vendor ID: GenuineIntel

CPU family: 6

Model: 94

Model name: Intel(R) Core(TM) i5-6600 CPU @ 3.30GHz

Stepping: 3

CPU MHz: 3177.281

CPU max MHz: 3900.0000

CPU min MHz: 800.0000

BogoMIPS: 6623.96

Virtualization: VT-x

L1d cache: 32K

L1i cache: 32K

L2 cache: 256K

L3 cache: 6144K

NUMA node0 CPU(s): 0-3

Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov

pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp

lm constant\_tsc art arch\_perfmon pebs bts rep\_good nopl xtopology nonstop\_tsc

aperfperf eagerfpu pni pclmulqdq dtes64 monitor ds\_cpl vmx smx est tm2 ssse3 sdbg

fma cx16 xtpr pdcm pcid sse4\_1 sse4\_2 x2apic movbe popcnt tsc\_deadline\_timer aes

xsave avx f16c rdrand lahf\_lm abm 3dnowprefetch intel\_pt tpr\_shadow vnmi

flexpriority ept vpid fsgsbase tsc\_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm

mpx rdseed adx smap clflushopt xsaveopt xsavec xgetbv1 dtherm arat pln pts hwp

hwp\_notify hwp\_act\_window hwp\_epp

/proc/cpuinfo cache data

cache size : 6144 KB

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## Platform Notes (Continued)

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```
available: 1 nodes (0)
node 0 cpus: 0 1 2 3
node 0 size: 7860 MB
node 0 free: 7463 MB
node distances:
node 0
0: 10
```

From /proc/meminfo

```
MemTotal:      8048764 kB
HugePages_Total: 0
Hugepagesize:   2048 kB
```

/usr/bin/lsb\_release -d

```
Ubuntu 16.04.1 LTS
```

From /etc/\*release\* /etc/\*version\*

```
debian_version: stretch/sid
os-release:
NAME="Ubuntu"
VERSION="16.04.1 LTS (Xenial Xerus)"
ID=ubuntu
ID_LIKE=debian
PRETTY_NAME="Ubuntu 16.04.1 LTS"
VERSION_ID="16.04"
HOME_URL="http://www.ubuntu.com/"
SUPPORT_URL="http://help.ubuntu.com/"
```

uname -a:

```
Linux ubuntu-freeze2 4.4.0-31-generic #50-Ubuntu SMP Wed Jul 13 00:07:12 UTC 2016
x86_64 x86_64 x86_64 GNU/Linux
```

run-level 5 Oct 30 07:30

SPEC is set to: /mnt/freezedisk/speccpu2017-intptr

```
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sdb1        ext4  2.7T  1.7T  907G  66% /mnt/freezedisk
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

(End of data from sysinfo program)

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## Compiler Version Notes

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CXXC 531.deepsjeng\_r(base)

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clang version 6.0.0 (<https://github.com/aqjune/clang-intptr.git>  
b099f9b86954800bcbdee427fc703eb8a30389da)  
(<https://github.com/aqjune/llvm-intptr.git>  
072bcfdd83ea02b321f72fca30d5a8384325de92)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /mnt/freezedisk/llvm-intptr-br2-correct2-release/bin  
Found candidate GCC installation: /usr/lib/gcc/x86\_64-linux-gnu/5  
Found candidate GCC installation: /usr/lib/gcc/x86\_64-linux-gnu/5.4.0  
Found candidate GCC installation: /usr/lib/gcc/x86\_64-linux-gnu/6  
Found candidate GCC installation: /usr/lib/gcc/x86\_64-linux-gnu/6.0.0  
Selected GCC installation: /usr/lib/gcc/x86\_64-linux-gnu/5.4.0  
Candidate multilib: .;@m64  
Selected multilib: .;@m64

-----

## Base Runtime Environment

C++ benchmarks:

531.deepsjeng\_r: No flags used

## Base Compiler Invocation

C++ benchmarks:

531.deepsjeng\_r: clang++

## Base Portability Flags

531.deepsjeng\_r: -DSPEC\_LP64

## Base Optimization Flags

C++ benchmarks:

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## Base Optimization Flags (Continued)

531.deepsjeng\_r: -m64 -O3 -mavx -z muldefs

## Base Other Flags

C++ benchmarks:

531.deepsjeng\_r: No flags used

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For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU2017 v1.0.2 on 2017-11-03 16:59:36+0900.

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