#### SPEC® CPU2017 Integer Rate Result Copyright 2017 Standard Performance Evaluation Corporation My Corporation SPECrate2017\_int\_base = SPECrate2017 int peak **∜**ot Run Test Date: Nov-2017 CPU2017 License: nnn (Your SPEC license number) Hardware Availability: **Test Sponsor:** My Corporation Tested by: Software Availability: My Corporation Copies 0 1.00 3.00 500.perlbench\_r 502.gcc\_r 505.mcf\_r 520.omnetpp\_r 523.xalancbmk\_r 525.x264 r 531.deepsjeng\_r 541.leela r 548.exchange2\_r 557.xz\_r SPECrate2017 int base (3.12) Hardware Software CPU Name: Intel Core i3-6100 OS: Ubuntu 16.04.3 LTS Max MHz.: 4.4.0-97-generic Nominal: C/C++: Version 3.9.0 of Clang, the Compiler: Enabled: cores, 1 chip, threads/core LLVM Compiler Infrastructure Orderable: Fortran: Version 4.8.2 of GCC, the Cache L1: **GNU Compiler Collection** DragonEgg: Version 3.5.2, the L2: LLVM Compiler Infrastructure L3: Other: Parallel: No 7.497 GB fixme: If using DDR3, format is: Firmware: Memory: 'N GB (M x N GB nRxn PCn-nnnnnR-n, ECC)' File System: ext4 270 GB, add more disk info here System State: Run level 3 (add definition here) Storage: **Ø**ther: Base Pointers: 64-bit Peak Pointers: Not Applicable

### **Errors**

Other:

'reportable' flag not set during run

520.omnetpp\_r (base) did not have enough runs!

500.perlbench\_r (base) did not have enough runs!

541.leela\_r (base) did not have enough runs!

505.mcf r (base) did not have enough runs!

525.x264\_r (base) did not have enough runs!

523.xalancbmk\_r (base) did not have enough runs!

502.gcc\_r (base) did not have enough runs!

548.exchange2\_r (base) did not have enough runs!

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Hardware Availability: Software Availability:

### **Errors** (Continued)

531.deepsjeng\_r (base) did not have enough runs!

### Results Table

	Base							Peak						
Benchmark	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r								\						
502.gcc_r														
505.mcf_r								$\mathcal{N}$						
520.omnetpp_r			_ <											
523.xalancbmk_r							$\overline{)}$							
525.x264_r			<b>&gt;</b>				n							
531.deepsjeng_r														
541.leela_r						\ \/								
548.exchange2_r						$\searrow$								
557.xz_r	1	347	3.12	347	3.12)	<u>347</u>	<u>3.12</u>							

SPECrate2017\_int\_base =

No Rin

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

#### **Submit Notes**

The config file option 'submit' was used.

#### **General Notes**

Environment variables set by runcpu before the start of the run:

LD\_LIBRARY\_RATH = "/mnt/freezedisk/llvm-intptr-base-5.0-release/lib"

LIBRARY\_PATH = "/mnt/freezedisk/llvm-intptr-base-5.0-release/lib"

TH = "/mlx/freezedisk/llvm-htptr-bass-5.0-release/bin:/mnt/freezedisk/speccpu2017-intptr/bin:/usr/local/sbin:/usr/local/bin:/usr/sbin:/usr/sbin:/bin:/usr/sbin:/usr/games:/usr/local/games:/snap/bin\*

#### **Platform Notes**

Sysinfo program /mnt/freezedisk/speccpu2017-intptr/bin/sysinfo Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f running on freeze4 Wed Nov 1 20:12:48 2017

SUT (System Under Test) info as seen by some common utilities. For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

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#### Platform Notes (Continued)

```
From /proc/cpuinfo
   model name : Intel(R) Core(TM) i3-6100 CPU @ 3.70GHz
      1 "physical id"s (chips)
      2 "processors"
   cores, siblings (Caution: counting these is hw and system dependent. The following
   excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
      cpu cores : 2
      siblings : 2
      physical 0: cores 0 1
From lscpu:
     Architecture:
                              32-bit<mark>/</mark>/
     CPU op-mode(s):
     Byte Order:
                              Little Endian
     CPU(s):
     On-line CPU(s) list:
                              0,1
     Thread(s) per core:
                              1
     Core(s) per socket:
     Socket(s):
     NUMA node(s):
     Vendor ID:
                              GenuineIntel
     CPU family:
     Model:
     Model name;
                              Intel(R) Core(TM) i3-6100 CPU @ 3.70GHz
     Stepping:
                              3700.000
     CPU MHz;
                              3700.0000
     CPU max MHz:
                              800.0000
     CPU min MHz:
     BogOMIPS:
                              7392.16
     Virtualization:
                              VT-x
     1/1d cache:
                              32K
     Zi cache:
                              32K
     L2 cache:
                              256K
     L3 cache:
                              3072K
     NUMA node CPU(s):
                              0,1
```

fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant\_tsc art arch\_perfmon pebs bts rep\_good nopl xtopology nonstop\_tsc aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds\_cpl vmx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid sse4\_1 sse4\_2 x2apic movbe popcnt tsc\_deadline\_timer aes xsave avx f16c rdrand lahf\_lm abm 3dnowprefetch epb intel\_pt tpr\_shadow vnmi flexpriority ept vpid fsgsbase tsc\_adjust bmi1 avx2 smep bmi2 erms invpcid mpx rdseed adx smap clflushopt xsaveopt xsavec xgetbv1 dtherm arat pln pts hwp hwp\_notify hwp\_act\_window hwp\_epp

/proc/cpuinfo cache data
 cache size : 3072 KB

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#### Platform Notes (Continued)

```
From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
  available: 1 nodes (0)
  node 0 cpus: 0 1
  node 0 size: 7676 MB
  node 0 free: 7241 MB
  node distances:
 node
       Ω
    0: 10
From /proc/meminfo
   MemTotal:
                    7860684
   HugePages_Total:
   Hugepagesize:
/usr/bin/lsb_release -d
   Ubuntu 16.04.3 LTS
From /etc/*release* /etc/*version*
   debian_version: stretch/sid
   os-release:
      NAME="Ubuntu
      VERSION="16.04.3 LTS (Xenial Xerus)"
      ID=ubuntx
      ID_LIKE debian
      PRETTY_NAME="Ubuntu 16.04.3 LTS"
      VERSION_ID="16.04"
      HOME_URL="http://www.ubuntu.com/"
      SUPPORT_URL="http://help.ubuntu.com/"
uname/~a:
   Linux freeze4 4.4.0-97-generic #120-Ubuntu SMP Tue Sep 19 17:28:18 UTC 2017 x86_64
   x86_64 x86_64 GNU/Linux
run level 3 Oct 30 07:30
SPEC is set to: /mnt/freezedisk/speccpu2017-intptr
                  Type Size Used Avail Use% Mounted on
   Filesystem
                               56G 162G 26% /mnt/freezedisk
   /dev/sda1
                        230G
                  ext4
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

(End of data from sysinfo program)

#### SPEC CPU2017 Integer Rate Result Copyright 2017 Standard Performance Evaluation Corporation My Corporation SPECrate2017\_int\_base = SPECrate2017\_int\_peak **∜**ot Run CPU2017 License: nnn (Your SPEC license number) Test Date: Nov-2017 **Test Sponsor:** My Corporation Hardware Availability: Software Availability: **Tested by:** My Corporation **Compiler Version Notes** CC 557.xz\_r(base) clang version 6.0.0 (https://github.com/agjune/clang-intptr. b099f9b86954800bcbdee427fc703eb8a3038 (https://github.com/aqjune/llvm-intptr.git d3762c4675cbe5178acef79f01c813bcbfc58cf5 Target: x86\_64-unknown-linux-gnu Thread model: posix InstalledDir: /mnt/freezedisk/l(lvm-intptr-base-5.0-rel/ease/bin Found candidate GCC installation: /usr/lib/gcc/x86\_64-linux-gnu/5 Found candidate GCC installation: /usr lib/gcc/x86\_64-linux-gnu/5.4.0 Found candidate GCC installation: /wsx/lbb/gcc/x86\_64-linux-gnu/6 Found candidate GCC installation: /usr/lib/gcc/x86\_64-linux-gnu/6.0.0 Selected GCC installation: /usx/llb/gcc/x86\_64/linux-gnu/5.4.0 Candidate multilib: .;@m64 Selected multilib: .;@m64 **Base Runtime Environment** C benchmarks: 557.xz\_r: No flags used **Base Compiler Invocation** C benchmarks 557.xz\_r: clang **Base Portability Flags** 557.xz\_r: -DSPEC LP64 **Base Optimization Flags** C benchmarks: (Continued on next page) Page 5 Standard Performance Evaluation Corporation (info@spec.org) https://www.spec.org/

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**Base Optimization Flags (Continued)** 

 $557.xz_r: -m64 - z muldefs - 03 - mavx$ 

Base Other Flags

C benchmarks:

557.xz\_r: No flags used

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