

Copyright 2017 Standard Performance Evaluation Corporation

# My Corporation

SPECrate2017\_fp\_base = 5.63

SPECrate2017\_fp\_peak Not Run

CPU2017 License: nnn (Your SPEC license number)

**Test Sponsor:** My Corporation **Tested by:** My Corporation

Test Date: Nov-2017

Hardware Availability: Software Availability:

### **Errors** (Continued)

544.nab\_r (base) did not have enough runs!
521.wrf\_r (base) did not have enough runs!
554.roms\_r (base) did not have enough runs!
549.fotonik3d\_r (base) did not have enough runs!
507.cactuBSSN\_r (base) did not have enough runs!
503.bwaves\_r (base) did not have enough runs!
519.lbm\_r (base) did not have enough runs!

508.namd\_r (base) did not have enough runs!

#### Results Table

	Base							Peak						
Benchmark	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r						)/								
507.cactuBSSN_r			_			\n'\								
508.namd_r														
510.parest_r	1	465	5.63	// 464	5,63	464	5.64							
511.povray_r		<i>^</i>	$\bigcup$											
519.lbm_r				$\setminus J$										
521.wrf_r		, //												
526.blender_r	$\wedge$	\'	\	)										
527.cam4_r			7/											
538.imagick_r														
544.nab_r			7											
549.fotonik3d_r														
\$54.roms_1	2015	7												

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

#### **Submit Notes**

The config file option 'submit' was used.

#### **General Notes**

Environment variables set by runcpu before the start of the run:
LD\_LIBRARY\_PATH = "/mnt/freezedisk/llvm-intptr-br2-correct2-release/lib"
LIBRARY\_PATH = "/mnt/freezedisk/llvm-intptr-br2-correct2-release/lib"

PATH = \*/mnt/freezedisk/llvm-intptr-br2-correct2-release/bin:/mnt/freezedisk/speccpu2017-intptr/bin:/usr/local/sbin:/usr/local/sbin:/usr/sbin:/usr/sbin:/usr/sbin:/usr/games:/usr/local/games:/snap/bin:/snap/bin:/usr/s

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#### **Platform Notes**

```
Sysinfo program /mnt/freezedisk/speccpu2017-intptr/bin/sysinfo Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f running on freeze4 Fri Nov 3 18:12:01 2017
```

```
SUT (System Under Test) info as seen by some common utilities. For more information on this section, see
```

https://www.spec.org/cpu2017/Docs/config.html#sysinfo

```
From /proc/cpuinfo model name : Intel(R) Core(TM) i3-6100 CPU @ 3.70GHz

1 "physical id"s (chips)
```

1 "physical id"s (chips)
2 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 2
siblings : 2
physical 0: cores 0 1

From lscpu:

CPU MHx:

Architecture:
CPU op-mode(s):
Byte Order:
CPU(s):
On-line CPU(s) list:

86\_64
32-bit, 64-bit
ittle Endian
2

Thread(s) per core: 1
Core(s) per socket: 2
Socket(s): 1
NUMA node(s): 1

Vendor ID: GenuineIntel

CPU family: 6
Wodel: 94

model name: / Intel(R) Core(TM) i3-6100 CPU @ 3.70GHz

3700.000

0,1

Stepping: 3

NUMA node0 CPU(s):

CPU max MMz: 3700.0000 CPU min MHz: 800.0000 BogoMIPS: 7392.16 Virtualization: x-TV32K cache: Lli cache: 32K L2 cache: 256K L3 cache: 3072K

Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant\_tsc art arch\_perfmon pebs bts rep\_good nopl xtopology nonstop\_tsc aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds\_cpl vmx est tm2 ssse3 sdbg fma

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#### Platform Notes (Continued)

cx16 xtpr pdcm pcid sse4\_1 sse4\_2 x2apic movbe popcht tsc\_deadline\_timer aes xsave avx f16c rdrand lahf\_lm abm 3dnowprefetch epb intel\_pt tpr\_shadow vnmi flexpriority ept vpid fsgsbase tsc\_adjust bmil avx2 smep bmi2 erms invpcid mpx rdseed adx smap clflushopt xsaveopt xsavec xgetbvl otherm arat pln pts hwp hwp\_notify hwp\_act\_window hwp epp

```
/proc/cpuinfo cache data
   cache size : 3072 KB
From numactl --hardware WARNING; a numadtl
                                             'node'
                                                     might or might not correspond to a
physical chip.
  available: 1 nodes (0)
 node 0 cpus: 0 1
 node 0 size: 7676 MB
 node 0 free: 7239 MB
 node distances:
 node
       0
    0: 10
From /proc/meminfo
   MemTotal:
                     7860684
   HugePages_Total:
                       2048 ki
   Hugepagesize:
/usr/bin/lsb_rglease
   Ubuntu 16.04.3 LTS
From /etc/*release* /etc/*version*
   debian version: stretch/sid
   os-release
      NAME="Ubuntu
      VERSION="16,04.3 LTS (Xenial Xerus)"
      ID=ubuntu
      ID_LIKE=debian
      PRETTY_NAME="Ubuntu 16.04.3 LTS"
      VERSION ID="16.04"
      HOME_VRL="http://www.ubuntu.com/"
      SUPPORT_URL="http://help.ubuntu.com/"
```

Linux freeze4 4.4.0-97-generic #120-Ubuntu SMP Tue Sep 19 17:28:18 UTC 2017 x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 3 Oct 30 07:30

SPEC is set to: /mnt/freezedisk/speccpu2017-intptr Filesystem Type Size Used Avail Use% Mounted on

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#### Platform Notes (Continued)

/dev/sda1 ext4 230G 59G 159G 27% /mnt/fxeezedisk

Additional information from dmidecode follows. WARNING: Use caudion when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

(End of data from sysinfo program)

#### Compiler Version Notes

CXXC 510.parest\_r(base)

------

clang version 6.0.0 (https://github.com/aqjune/clang-intptr.git

b099f9b86954800bcbdee427fc703eb8a30389da)

(https://github.com/aqjune/llvm-intptk.git

072bcfdd83ea02b321f72fg36d5a8384325de92)

Target: x86 64-unknown-linux-guu

Thread model: posix

InstalledDir: /mnt/freezedisk/llvmintptr-br2-correct2-release/bin

Found candidate GCC installation /usr/lib/gcc/x86\_64-linux-gnu/5

Found candidate GCC installation: /usr/lib/gcc/x86\_64-linux-gnu/5.4.0

Found candidate GCC installation: /usr/lib/gcc/x86\_64-linux-gnu/6

Found candidate GCC installation: /usr/lib/gcc/x86\_64-linux-gnu/6.0.0

Selected GCC installation /usr/lib/gcc/x86\_64-linux-gnu/5.4.0

Candidate multilib: .;@m64 Selected multilib: .;@m64

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#### **Base Runtime Environment**

C++ benchmarks:

510.parest\_r: No flags used

#### **Base Compiler Invocation**

C++ benchmarks:

510.parest\_r: clang++

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Base Portability Flags

510.parest\_r: -DSPEC LP64

**Base Optimization Flags** 

C++ benchmarks:

510.parest\_r: -m64 -O3 -mavx -z muldefs

**Base Other Flags** 

C++ benchmarks:

510.parest\_r: No flags used

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2017-11-03 18:12:01+0900.

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