

Errors

'reportable' flag not set during run

541.leela_r (base) did not have enough runs!

505.mcf_r (base) did not have enough runs!

505.mci_1 (vase) and not have enough runs:

557.xz_r (base) did not have enough runs!

502.gcc_r (base) did not have enough runs!

548.exchange2_r (base) did not have enough runs!

520.omnetpp_r (base) did not have enough runs!

500.perlbench_r (base) did not have enough runs!

523.xalancbmk_r (base) did not have enough runs!

(Continued on next page)

Copyright 2017 Standard Performance Evaluation Corporation

My Corporation

SPECrate2017_int_base =

SPECrate2017_int_peak **∜**ot Run

CPU2017 License: nnn (Your SPEC license number)

Test Sponsor: My Corporation **Tested by:** My Corporation Test Date: Nov-2017

Hardware Availability: Software Availability:

Errors (Continued)

525.x264 r (base) did not have enough runs!

Results Table

	Base							Peak						
Benchmark	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r								\						
502.gcc_r														
505.mcf_r														
520.omnetpp_r			>											
523.xalancbmk_r														
525.x264_r			>				n							
531.deepsjeng_r	1	269	4.26	<u>269</u>	4.26	269	4.26							
541.leela_r						1/								
548.exchange2_r				/		\checkmark								
557.xz_r														

SPECrate2017_int_base = SPECrate2017_int_peak/7

4.26 Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option ubmit' was used.

General Notes

Environment variables set by runcpu before the start of the run: LD_LIBkaRY_RATH = \'/mnt/freezedisk/llvm-intptr-br2-correct2-release/lib" LIRRARY_PATH > "/mmt/freezedisk/llvm-intptr-br2-correct2-release/lib"

Platform Notes

Sysinfo program /mnt/freezedisk/speccpu2017-intptr/bin/sysinfo Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f running on freeze4 Fri Nov 3 17:07:07 2017

SUT (System Under Test) info as seen by some common utilities. For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

(Continued on next page)

Copyright 2017 Standard Performance Evaluation Corporation

My Corporation

Test Sponsor:

Tested by:

SPECrate2017_int_base = 4.2

SPECrate2017_int_peak Not Run

CPU2017 License: nnn (Your SPEC license number)

My Corporation
My Corporation

Test Date: Nov-2017

Hardware Availability: Software Availability:

Platform Notes (Continued)

```
From /proc/cpuinfo
   model name : Intel(R) Core(TM) i3-6100 CPU @ 3.70GHz
      1 "physical id"s (chips)
      2 "processors"
   cores, siblings (Caution: counting these is hw and system dependent. The following
   excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
      cpu cores : 2
      siblings : 2
      physical 0: cores 0 1
From lscpu:
     Architecture:
                              32-bit<mark>/</mark>/
     CPU op-mode(s):
     Byte Order:
                              Little Endian
     CPU(s):
     On-line CPU(s) list:
                              0,1
     Thread(s) per core:
                              1
     Core(s) per socket:
     Socket(s):
     NUMA node(s):
     Vendor ID:
                              GenuineIntel
     CPU family:
     Model:
     Model name;
                              Intel(R) Core(TM) i3-6100 CPU @ 3.70GHz
     Stepping:
                              3700.000
     CPU MHz;
                              3700.0000
     CPU max MHz:
                              800.0000
     CPU min MHz:
     BogOMIPS:
                              7392.16
     Virtualization:
                              VT-x
     1/1d cache:
                              32K
     Zi cache:
                              32K
     L2 cache:
                              256K
     L3 cache:
                              3072K
     NUMA node CPU(s):
                              0,1
     Flags:
```

fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch epb intel_pt tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms invpcid mpx rdseed adx smap clflushopt xsaveopt xsavec xgetbv1 dtherm arat pln pts hwp hwp_notify hwp_act_window hwp_epp

/proc/cpuinfo cache data
 cache size : 3072 KB

(Continued on next page)

Copyright 2017 Standard Performance Evaluation Corporation

My Corporation

Test Sponsor:

Tested by:

SPECrate2017_int_base = 4.2

SPECrate2017_int_peak Not Run

CPU2017 License: nnn (Your SPEC license number)

My Corporation
My Corporation

Test Date: Nov-2017 Hardware Availability:

Software Availability:

Platform Notes (Continued)

```
From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
  available: 1 nodes (0)
  node 0 cpus: 0 1
  node 0 size: 7676 MB
  node 0 free: 7236 MB
  node distances:
 node
       Ω
    0: 10
From /proc/meminfo
   MemTotal:
                    7860684
   HugePages_Total:
   Hugepagesize:
/usr/bin/lsb_release -d
   Ubuntu 16.04.3 LTS
From /etc/*release* /etc/*version*
   debian_version: stretch/sid
   os-release:
      NAME="Ubuntu
      VERSION="16.04.3 LTS (Xenial Xerus)"
      ID=ubuntx
      ID_LIKE debian
      PRETTY_NAME="Ubuntu 16.04.3 LTS"
      VERSION_ID="16.04"
      HOME_URL="http://www.ubuntu.com/"
      SUPPORT_URL="http://help.ubuntu.com/"
uname/~a:
   Linux freeze4 4.4.0-97-generic #120-Ubuntu SMP Tue Sep 19 17:28:18 UTC 2017 x86_64
   x86_64 x86_64 GNU/Linux
run level 3 Oct 30 07:30
SPEC is set to: /mnt/freezedisk/speccpu2017-intptr
                  Type Size Used Avail Use% Mounted on
   Filesystem
                               59G 159G 27% /mnt/freezedisk
   /dev/sda1
                        230G
                  ext4
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

(End of data from sysinfo program)

SPEC CPU2017 Integer Rate Result Copyright 2017 Standard Performance Evaluation Corporation My Corporation SPECrate2017_int_base = SPECrate2017_int_peak **∜**ot Run CPU2017 License: nnn (Your SPEC license number) Test Date: Nov-2017 **Test Sponsor:** My Corporation Hardware Availability: Software Availability: **Tested by:** My Corporation **Compiler Version Notes** CXXC 531.deepsjeng_r(base) clang version 6.0.0 (https://github.com/adjune/clang-intptr. b099f9b86954800bcbdee427fc703eb8a30389da) (https://github.com/aqjune/llvm-intptr.git 072bcfdd83ea02b321f72fca30d5a8384325de92 Target: x86_64-unknown-linux-gnu Thread model: posix InstalledDir: /mnt/freezedisk/llvm-intptr-br2-correct2-release/bin Found candidate GCC installation: /usr/lib/gcc/x86_64-linux-gnu/5 Found candidate GCC installation: /usrxlib/gcc/x86_64-linux-gnu/5.4.0 Found candidate GCC instal ation: /wsx/lb/gcc/x86_64-linux-gnu/6 Found candidate GCC installation: /usr/lib/gcc/x86_64-linux-gnu/6.0.0 Selected GCC installation: /usx/llb/gcc/x86_64/linux-gnu/5.4.0 Candidate multilib: .;@m64 Selected multilib: .;@m64 **Base Runtime Environment** C++ benchmarks: 531.deepsjeng_r: No flags us **Base Compiler Invocation** C++ benchmarks: 531.deepsjeng_r: clang **Base Portability Flags** 531.deepsjeng_r: -DSPEC LP64 **Base Optimization Flags** C++ benchmarks: (Continued on next page) Page 5 Standard Performance Evaluation Corporation (info@spec.org) https://www.spec.org/

Copyright 2017 Standard Performance Evaluation Corporation

My Corporation

SPECrate2017_int_base = 4

SPECrate2017_int_peak Not Run

CPU2017 License: nnn (Your SPEC license number)

Test Sponsor: My Corporation **Tested by:** My Corporation

Test Date: Nov-2017

Hardware Availability: Software Availability:

Base Optimization Flags (Continued)

531.deepsjeng_r: -m64 -O3 -mavx -z muldefs

Base Other Flags

C++ benchmarks:

531.deepsjeng_r: No flags used

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2017-11-03 17:07:06+0900.

Report generated on 2017-11-03 17:20:40 by CPU2017 PDF formatter v5748.