

A

A

B

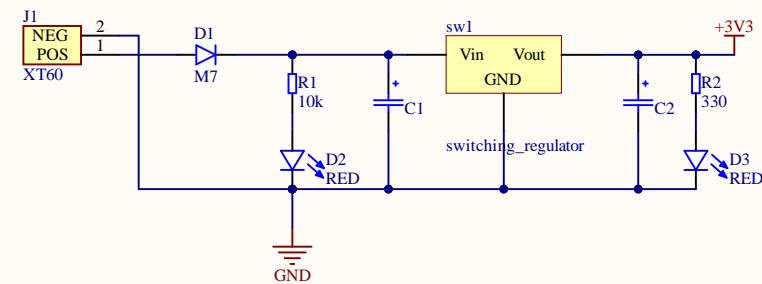
B

C

C

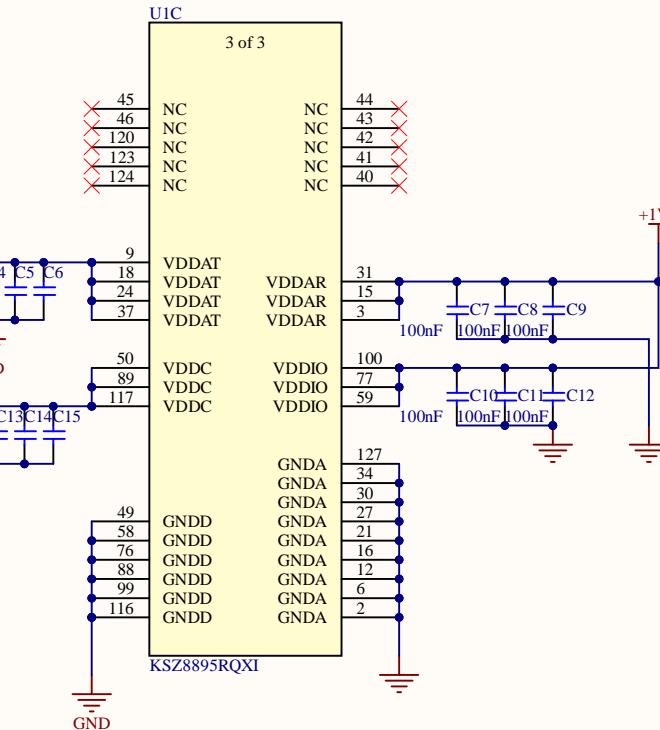
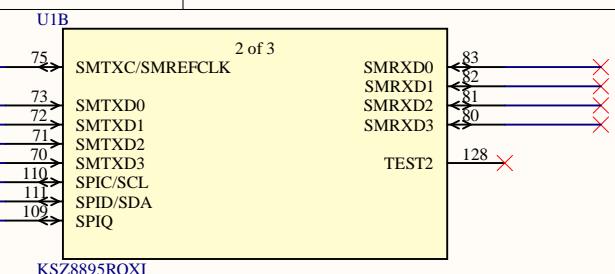
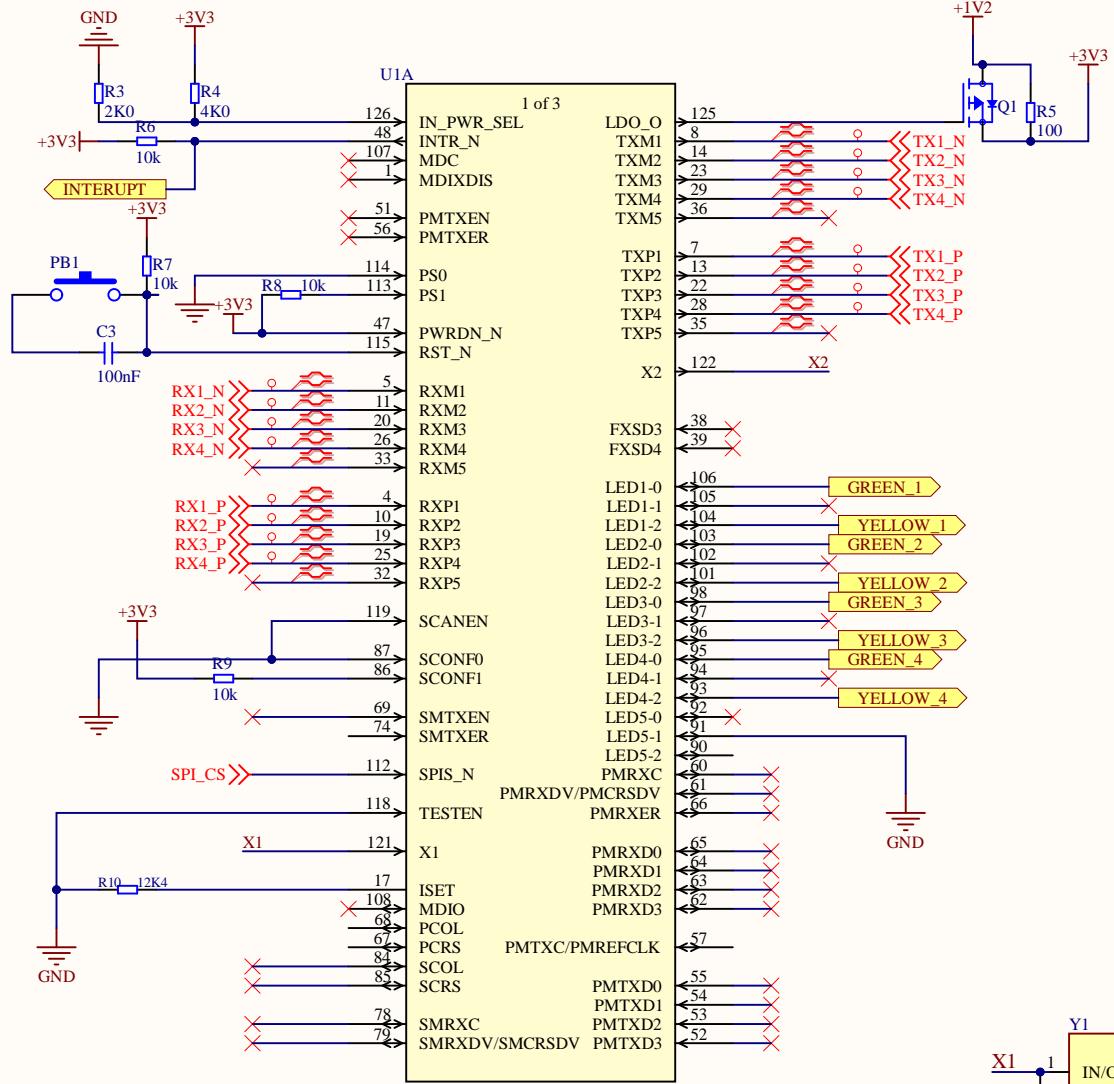
D

D



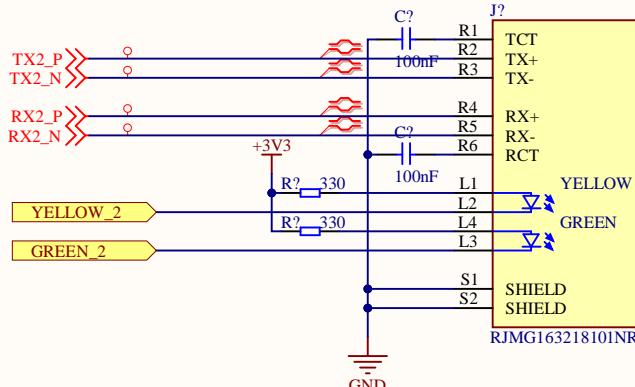
Title POWER REGULATION AND INDICATOR LEDs (C) DESIGNED BY ABDIRAHMAN FAISAL		
Size A4	Number	Revision A
Date: 2/22/2026	Sheet of 1 of 4	
File: C:\Users\..\overview.SchDoc		Drawn By: ABDIRAHMAN FAISAL ABDI

SIGNAL HANDLING BETWEEN ETHERNET NODES

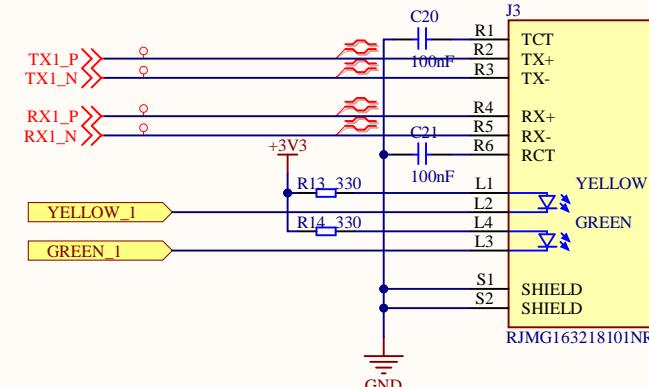


Title ETHERNET SWITCH IC		(C) DESIGNED BY ABDIRAHMAN FAISAL
Size A4	Number	Revision A
Date:	2/22/2026	Sheet of 1 of 4
File:	C:\Users\.\EthernetSwitch.SchDoc	Drawn By: ABDIRAHMAN FAISAL ABDI

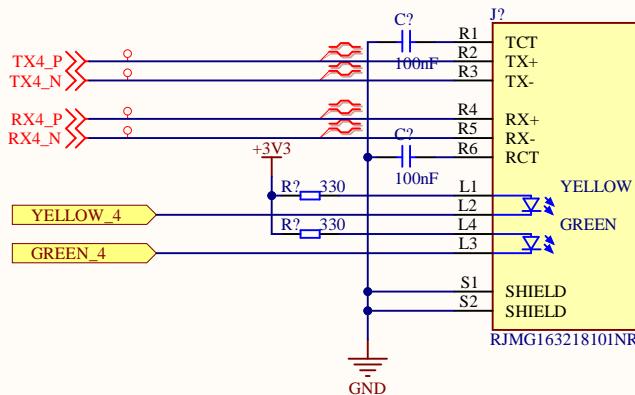
RJ45 WITH INTEGRATED MAGNETIC



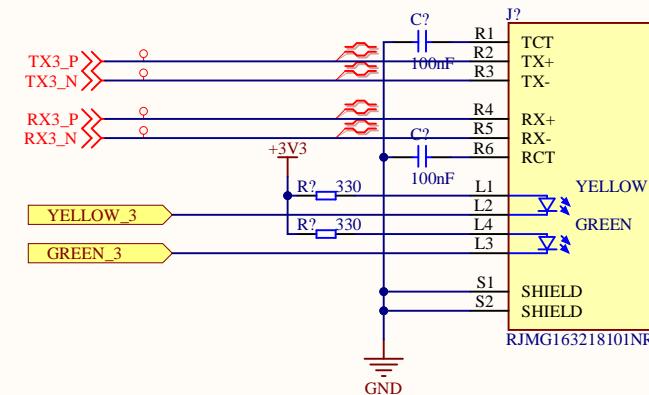
RJ45 WITH INTEGRATED MAGNETIC



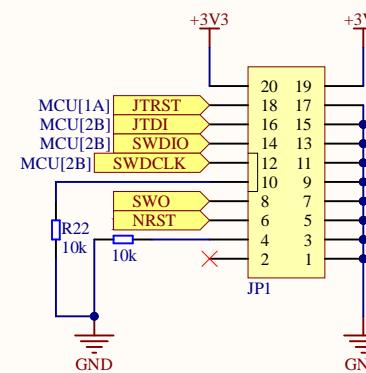
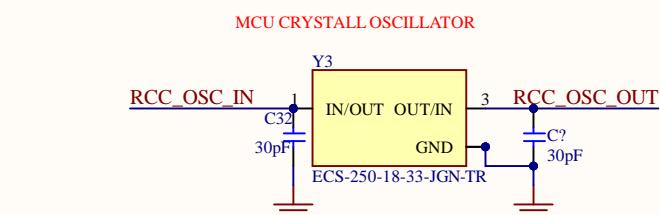
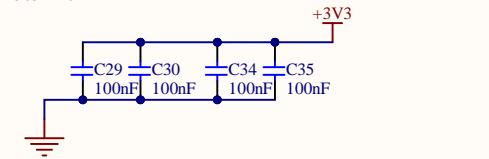
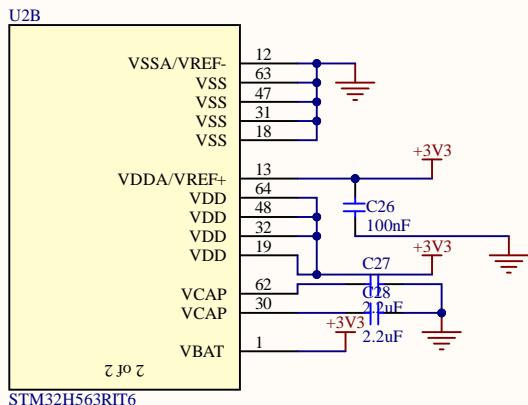
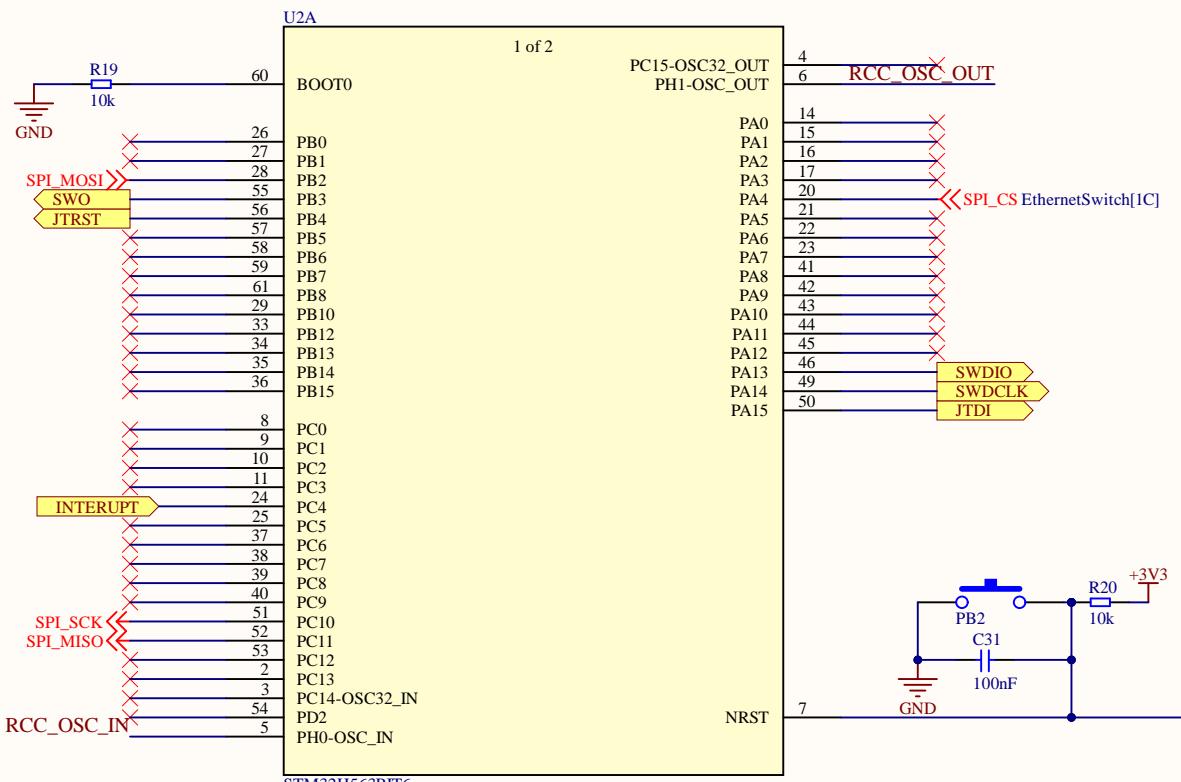
RJ45 WITH INTEGRATED MAGNETIC



RJ45 WITH INTEGRATED MAGNETIC



Title INTEGRATED RJ45 FOR NODES CONNECTION (C) DESIGNED BY ABDIRAHMAN FAISAL		
Size A4	Number	Revision A
Date: 2/22/2026	Sheet of 3 of 4	
File: C:\Users\..\RJ45_JACK_SWITCH.SchD	Drawn By: ABDIRAHMAN FAISAL ABDI	



Title **STM32H563RIT6 CONNECTIONS** (C) DESIGNED BY ABDIRAHMAN FAISAL

Size A4	Number	Revision A
Date:	2/22/2026	Sheet of 4 of 4
File:	C:\Users\..\MCU.SchDoc	Drawn By:ABDIRAHMAN FAISAL ABDI