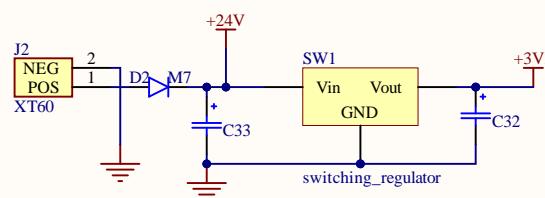


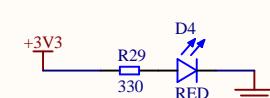
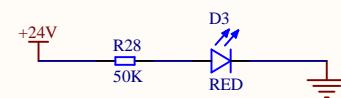
A

A

24V --> +3V3

B

B

24V INDICATOR**+3V3 INDICATOR**

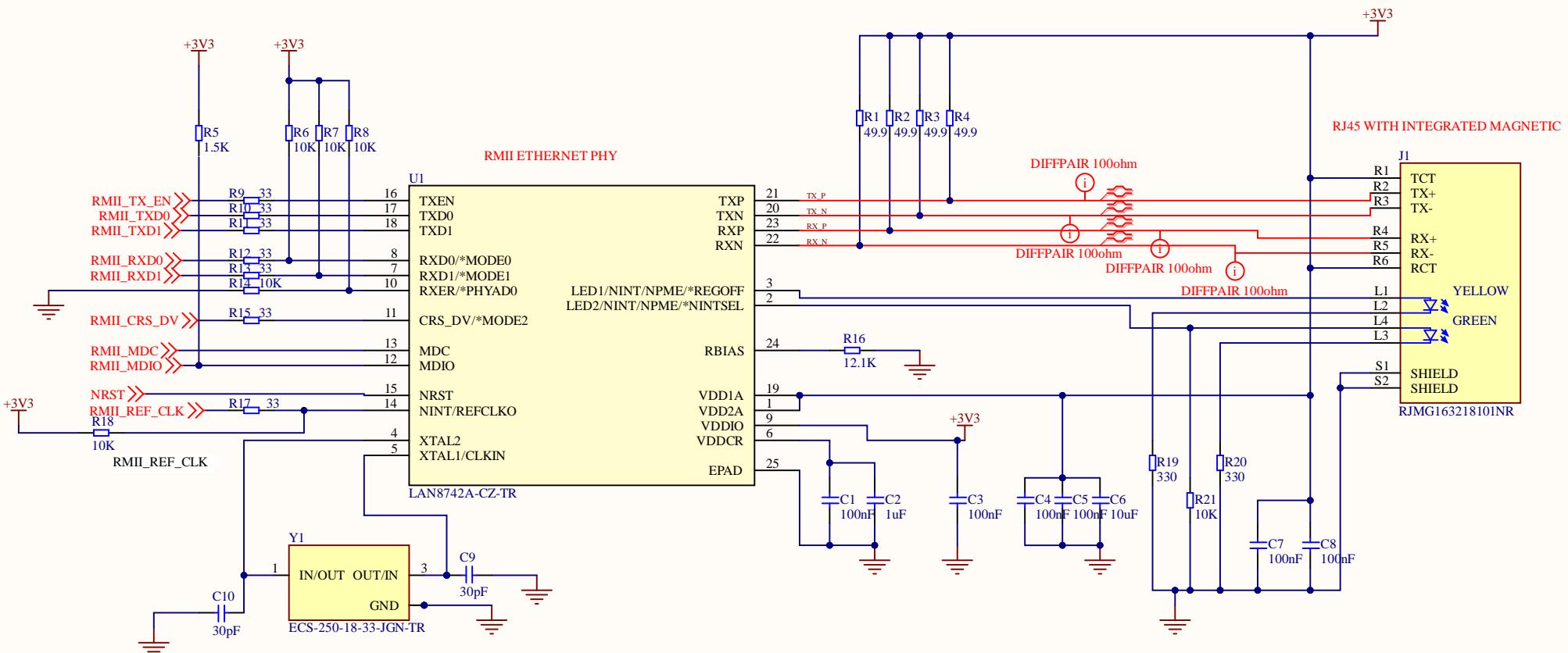
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C

Title POWER REGULATION AND INDICATOR LEDs (C) DESIGNED BY ABDIRAHMAN FAISAL		
Size A4	Number	Revision A
Date: 2/19/2026	Sheet of 1 of 4	
File: C:\Users\..\OVERVIEW.SchDoc		Drawn By: ABDIRAHMAN FAISAL ABDI

A

A



B

B

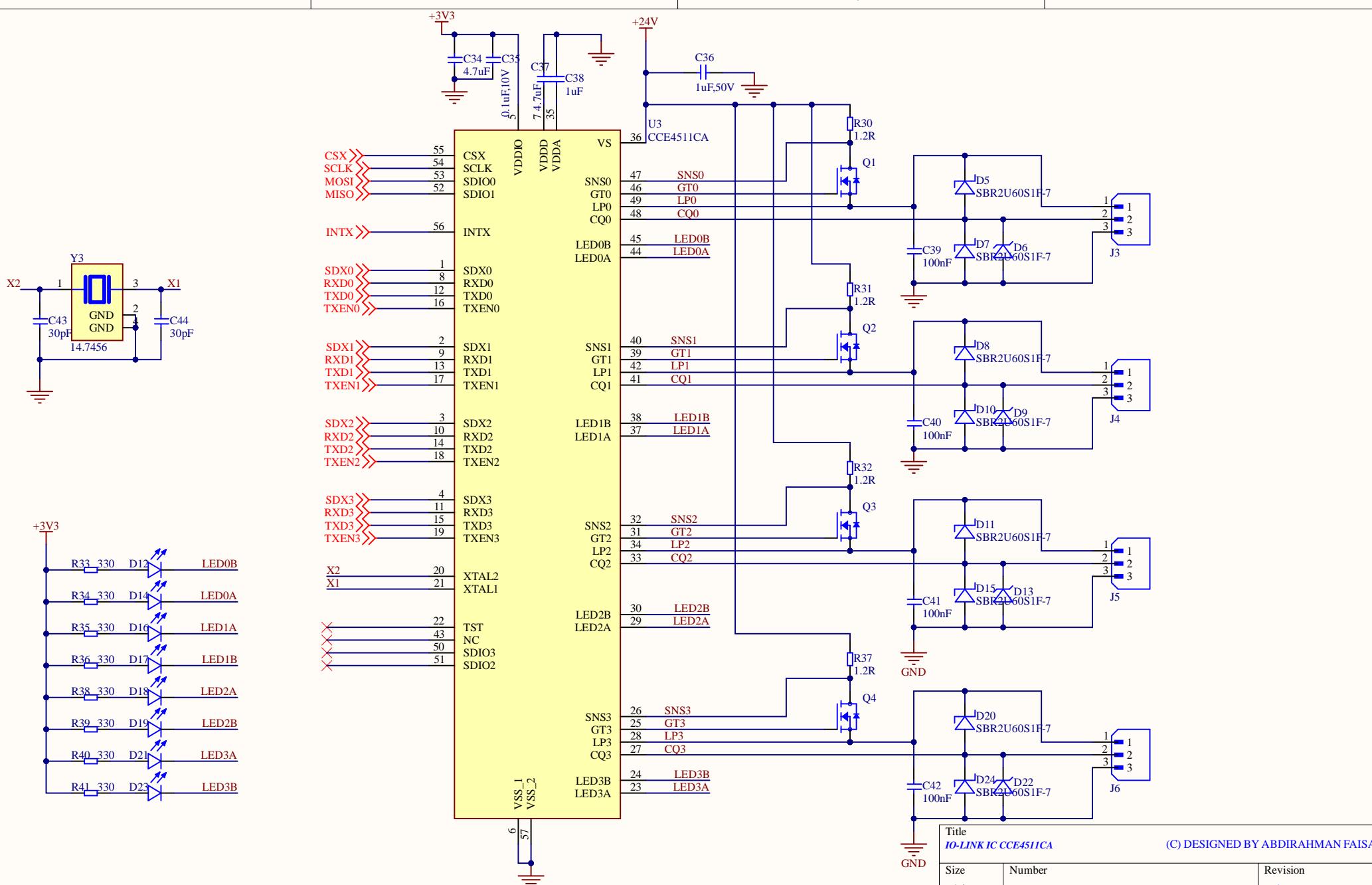
C

C

D

D

Title ETHERNET PHY WITH INTEGRATED RJ45 (C) DESIGNED BY ABDIRAHMAN FAISAL		
Size A4	Number	Revision A
Date: 2/19/2026	Sheet of 2 of 4	
File: C:\Users\..\ETHERNET.SchDoc		Drawn By: ABDIRAHMAN FAISAL ABDI



Size	Number	Revision
A4		A
Date:	2/19/2026	Sheet of 3 of 4
File:	C:\Users\...\IO-LINK TRANSCEIVER.Sch	Drawn By: ABDIRAHMAN FAISAL ABDI

