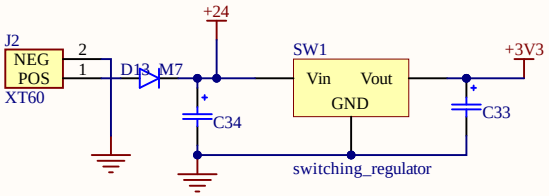
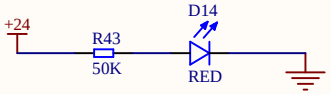
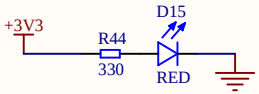
24V --> +3V3



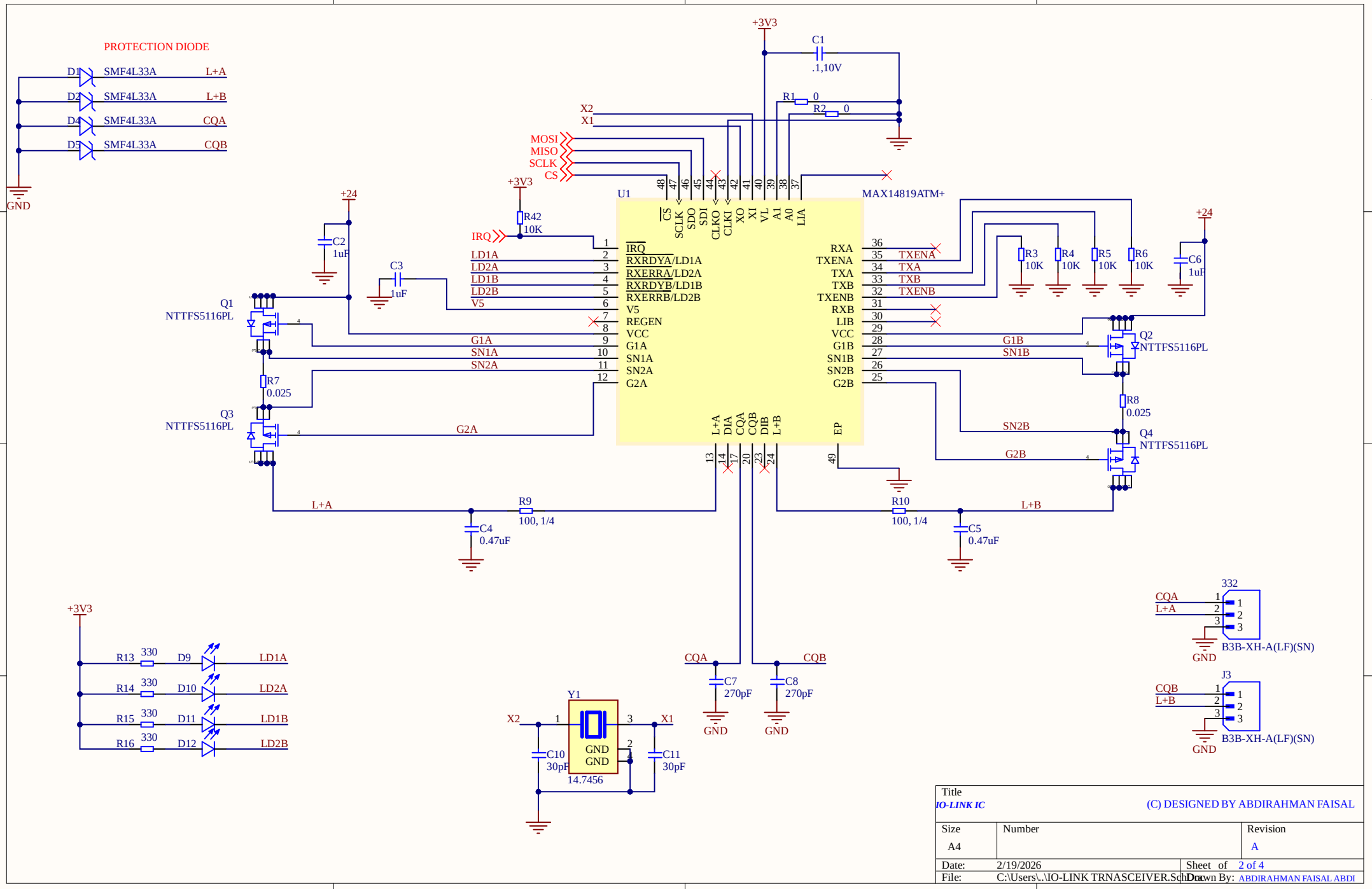
24V INDICATOR

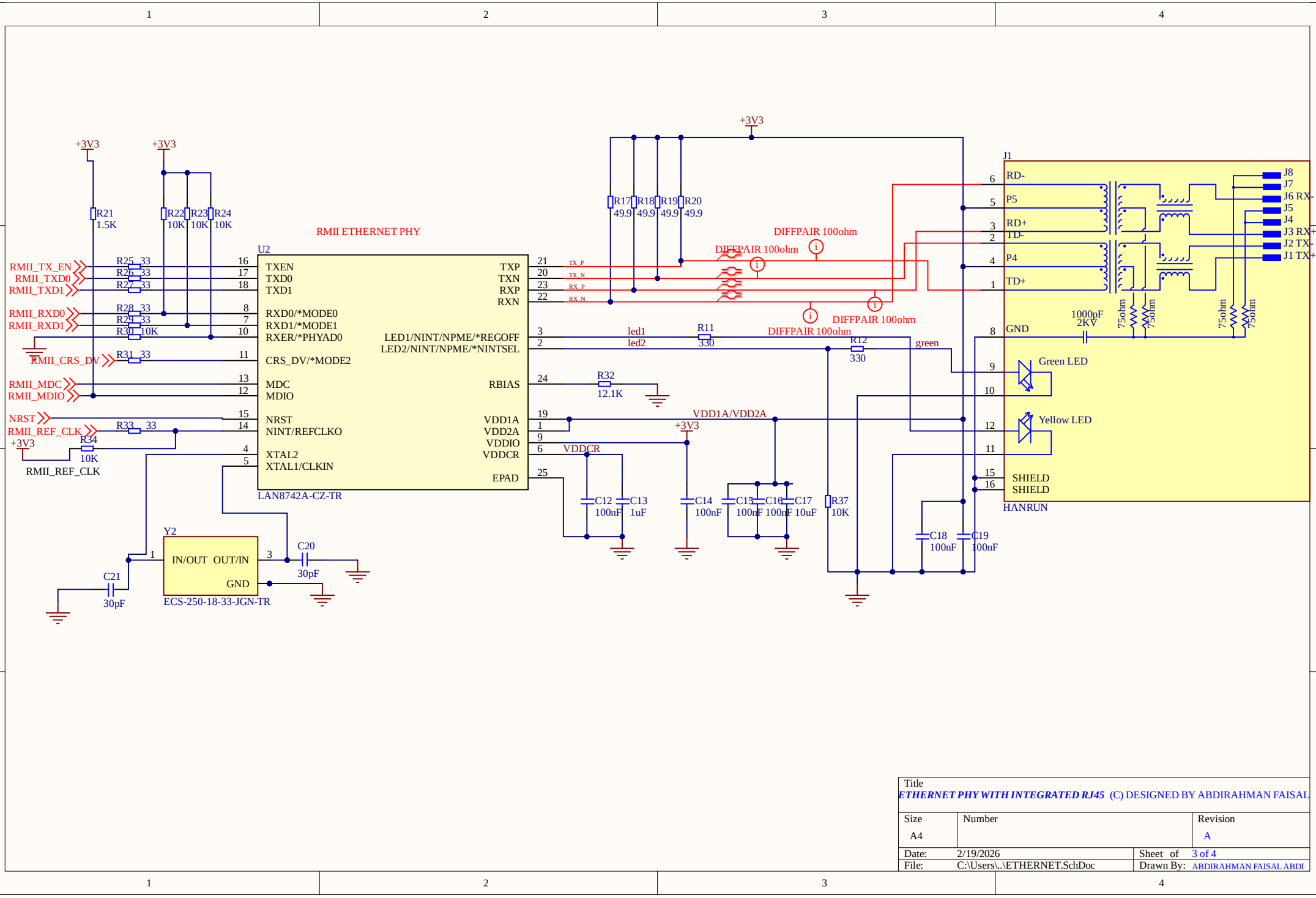


+3V3 INDICATOR



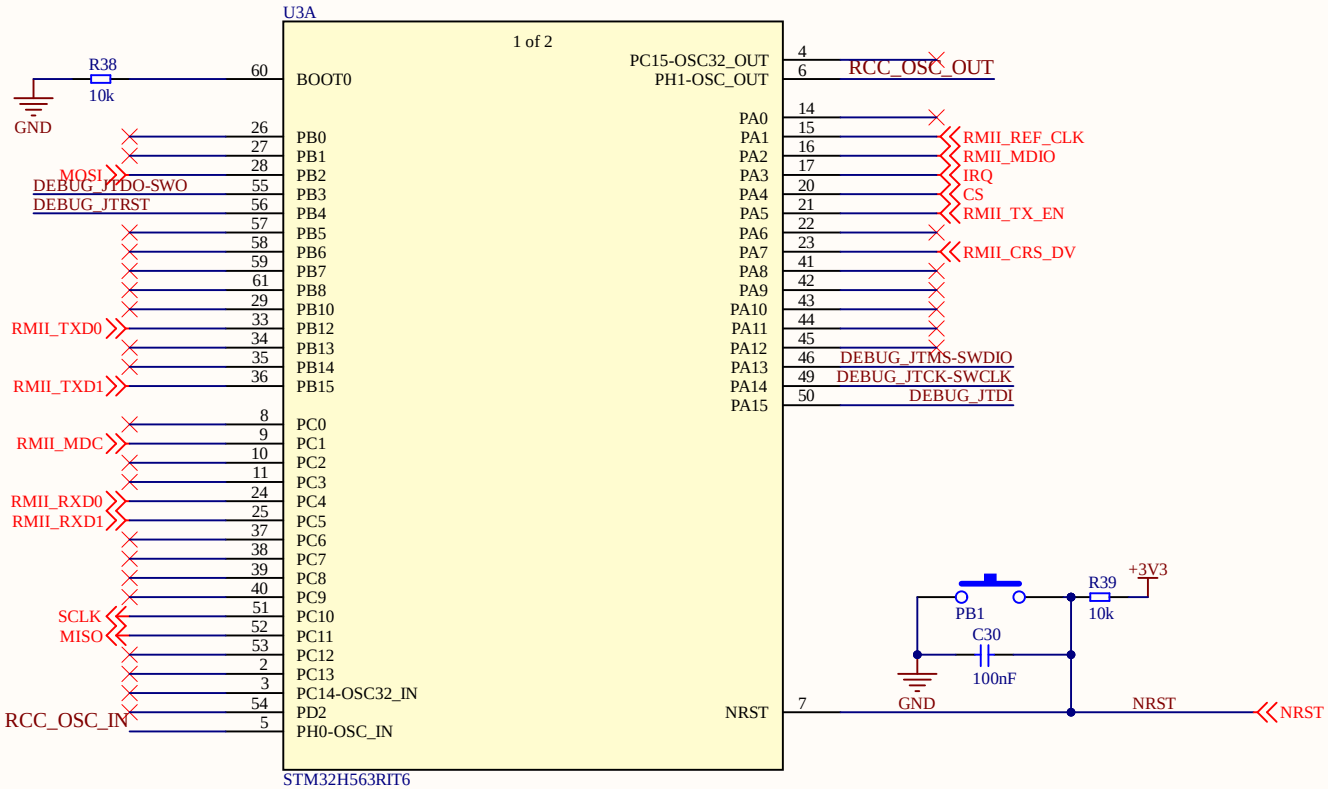
Title: ***POWER REGULATION AND INDICATOR LEDS*** (C) DESIGNED BY ABDIRAHMAN FAISAL		
Size: A4	Number:	Revision: A
Date: 2/19/2026	Sheet of: 1 of 4	
File: C:\Users\..\OVERVIEW.SchDoc	Drawn By: ABDIRAHMAN FAISAL ABDI	

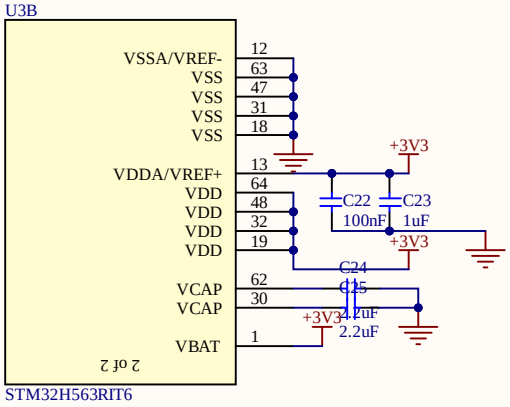

PROTECTION DIODE
D1 SMF4L33A L+A
D2 SMF4L33A L+B
D4 SMF4L33A CQA
D5 SMF4L33A CQB
GND
+3V3
C1
.1,10V
R1 0
R2 0
X2
X1
MOSI
MISO
SCLK
CS
U1
MAX14819ATM+
IRQ
LD1A
LD2A
LD1B
LD2B
V5
REGEN
VCC
G1A
SN1A
SN2A
G2A
RXA
TXENA
TXA
TXB
TXENB
RXB
LIB
G1B
SN1B
SN2B
G2B
EP
R42 10K
C2 1uF
C3 1uF
+24
R3 10K
R4 10K
R5 10K
R6 10K
C6 1uF
Q1 NTTFS5116PL
Q2 NTTFS5116PL
Q3 NTTFS5116PL
Q4 NTTFS5116PL
R7 0.025
R8 0.025
R9 100, 1/4
R10 100, 1/4
C4 0.47uF
C5 0.47uF
L+A
L+B
CQA
CQB
C7 270pF
C8 270pF
Y1
14.7456
C10 30pF
C11 30pF
R13 330
R14 330
R15 330
R16 330
D9
D10
D11
D12
LD1A
LD2A
LD1B
LD2B
332
B3B-XH-A(LF)(SN)
J3
Title
IO-LINK IC
(C) DESIGNED BY ABDIRAHMAN FAISAL
Size A4
Number
Revision A
Date: 2/19/2026
Sheet of 2 of 4
File: C:\Users\..\IO-LINK TRNASCEIVER.SchDoc
Drawn By: ABDIRAHMAN FAISAL ABDI

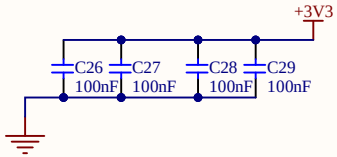


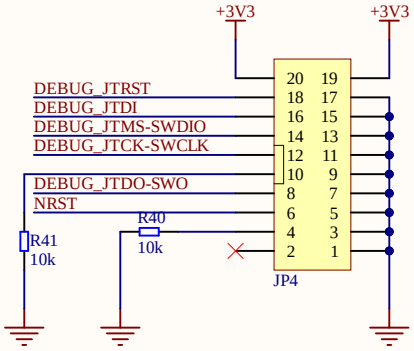
Title	*ETHERNET PHY WITH INTEGRATED RJ45* (C) DESIGNED BY ABDIRAHMAN FAISAL	
Size: A4	Number:	Revision: A
Date: 2/19/2026	Sheet of 3 of 4	
File: C:\Users\..\ETHERNET.SchDoc	Drawn By: ABDIRAHMAN FAISAL ABDI	

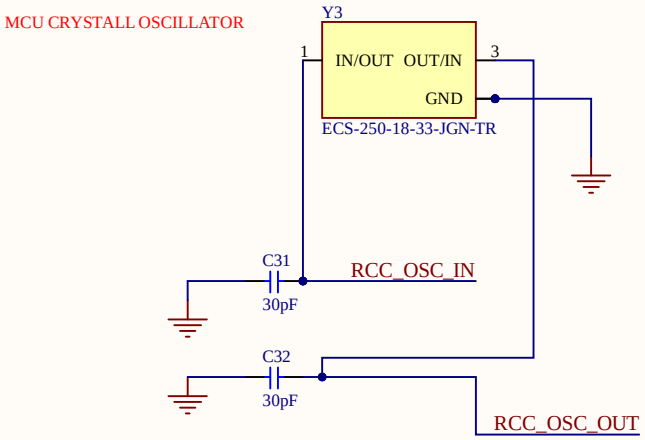
SPI3











Title: *STM32H563RIT6 CONNECTIONS*		(C) DESIGNED BY ABDIRAHMAN FAISAL
Size: A4	Number:	Revision: A
Date: 2/19/2026		Sheet of: 4 of 4
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