

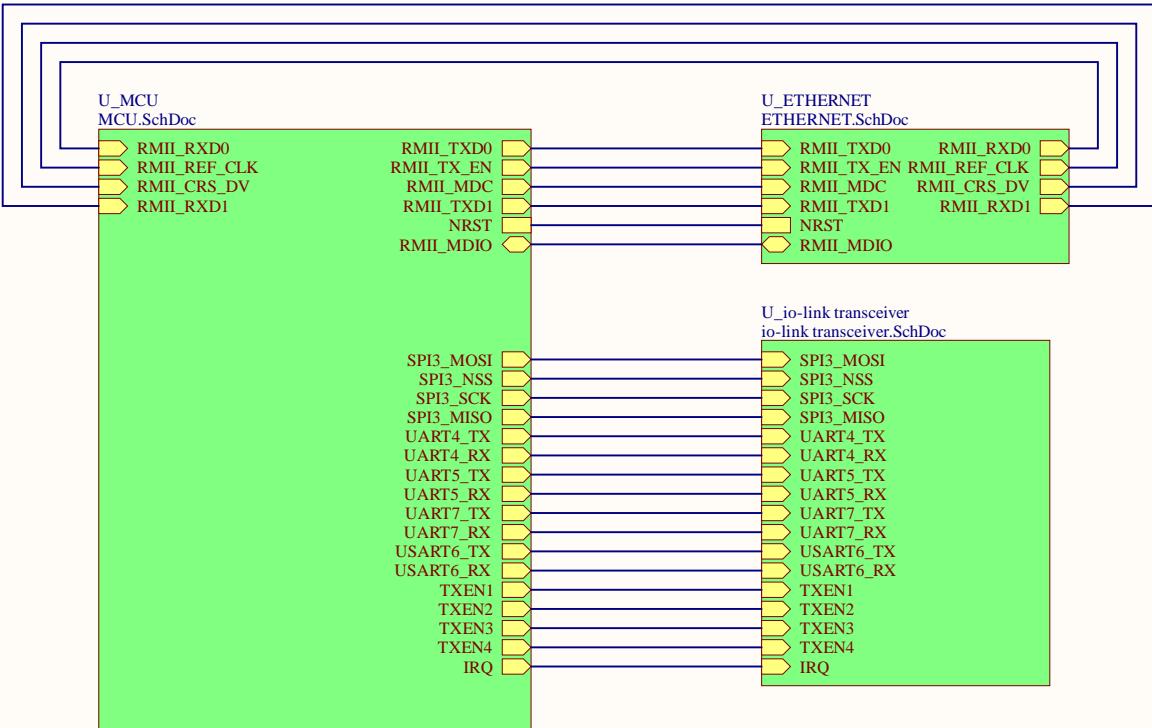
A

A

SCHEMATIC OVERVIEW

B

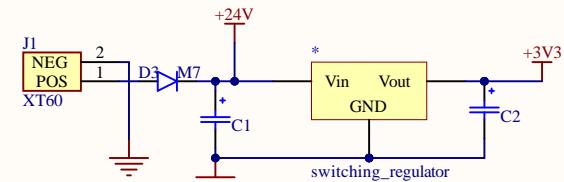
B



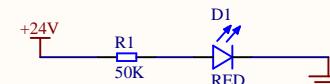
C

C

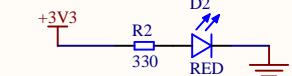
24V \rightarrow +3V3



24V INDICATOR



+3V3 INDICATOR



Title <u>TOP OVERVIEW AND POWER</u>		
(C) DESIGNED BY ABDIRAHMAN FAISAL		
Size A4	Number	Revision A
Date: 2/19/2026	Sheet of 1 of 4	
File: C:\Users\..\overview.SchDoc		Drawn By: ABDIRAHMAN FAISAL ABDI

A

A

B

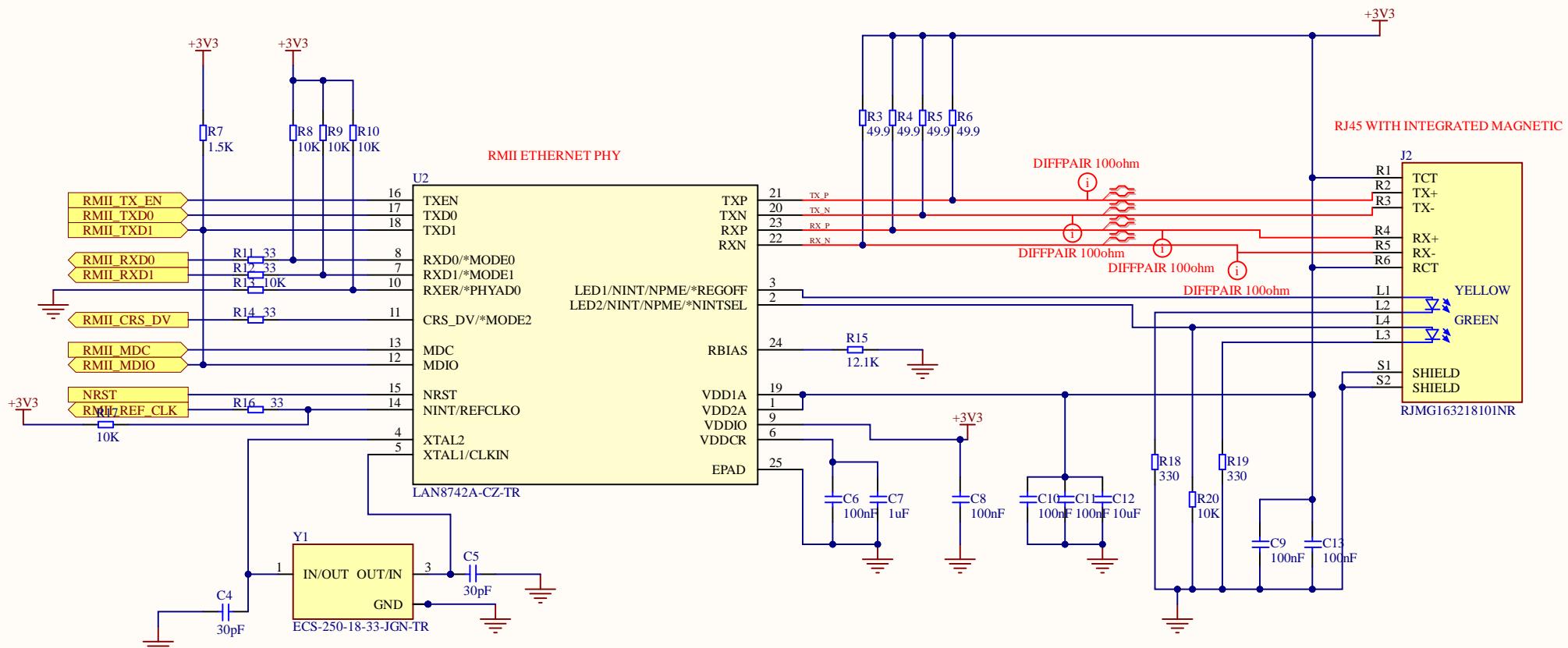
B

C

C

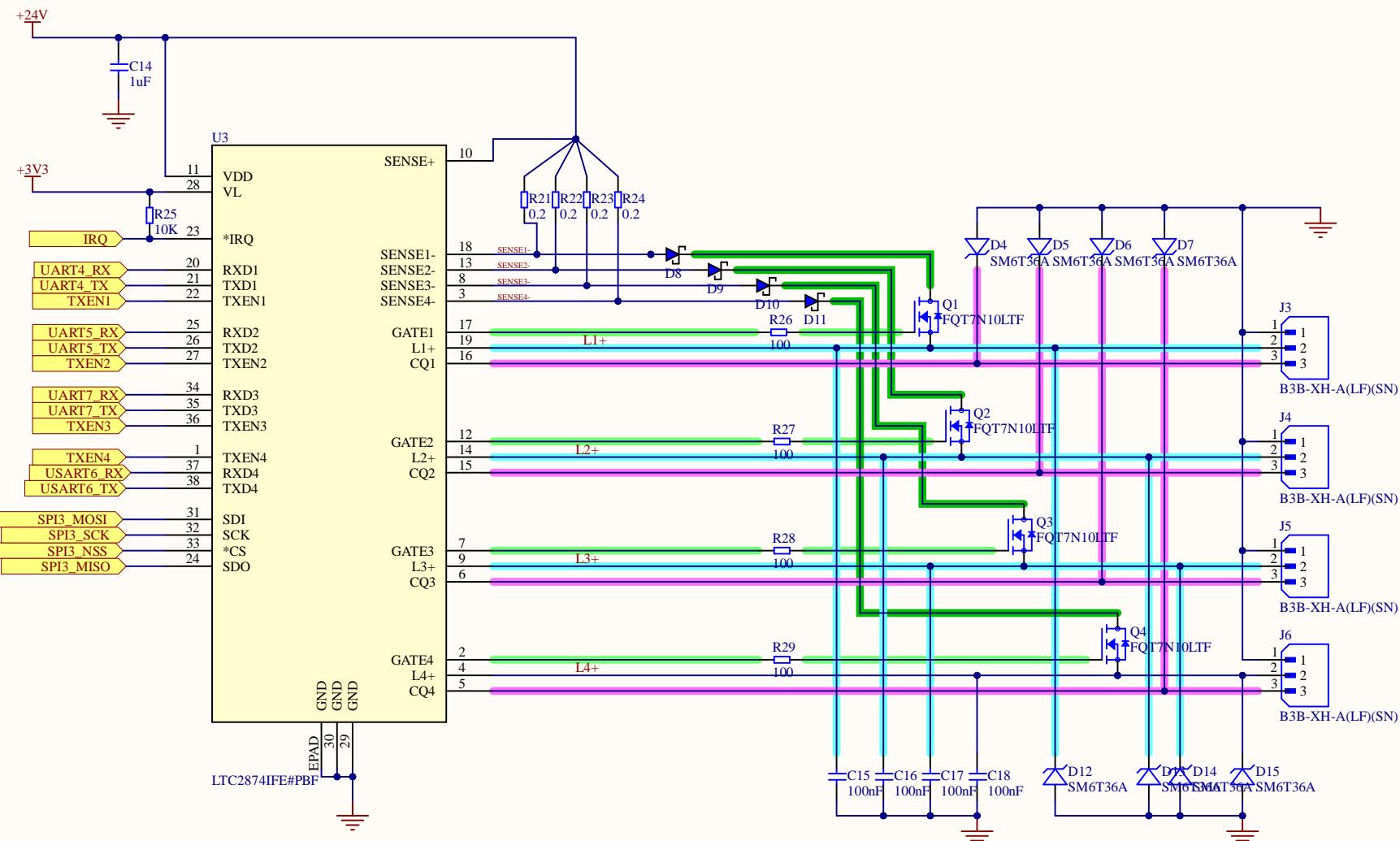
D

D



Title ETHERNET PHY WITH INTEGRATED RJ45 (C) DESIGNED BY ABDIRAHMAN FAISAL		
Size A4	Number	Revision A
Date: 2/19/2026	Sheet of 2 of 4	
File: C:\Users\..\ETHERNET.SchDoc		Drawn By: ABDIRAHMAN FAISAL ABDI

SIGNAL HANDLING BETWEEN MCU AND LASERS



Title IO-LINK TRANSCEIVER IC		
(C) DESIGNED BY ABDIRAHMAN FAISAL		
Size A4	Number	Revision A
Date: 2/19/2026	Sheet of 3 of 4	
File: C:\Users\..\io-link transceiver.SchDoc		Drawn By: ABDIRAHMAN FAISAL ABDI

