

NOT RECOMMENDED FOR NEW DESIGN USE <u>S1KP1M</u>



DFLR1800

1.0A SURFACE MOUNT GLASS PASSIVATED RECTIFIER PowerDI123

Product Summary (@TA = +25°C)

VRRM (V)	lo (A)	V _F Max (V)	IR Max (µA)
800	1	1.1	10

Description and Applications

Packaged in the compact thermally efficient PowerDI[®]123 package, the DIODES™ DFLR1800 provides high surge capacity and high efficiency. It is ideally suited for use in:

- AC-DC adaptors/chargers
- DC-DC converters
- Power supplies

Features and Benefits

- Ideally Suited for Automated Assembly
- Patented Interlocking Clip Design for High Surge Capacity, US Patent #7,095,113
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at

https://www.diodes.com/products/automotive/automotive-products/.

 This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.

https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Package: PowerDI123
- Package Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
 - Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: Cathode Band
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.01 grams (Approximate)

PowerDI123



Top View

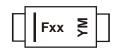
Ordering Information (Note 4)

Part Number	Marking Code	Package	Packing		
Part Number	Marking Code	Package	Qty.	Carrier	
DFLR1800-7	F18 or F18	PowerDI123	3000	Tape & Reel	

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- B. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



Fxx = Product Type Marking Code F18 or F18 = DFLR1800 YM = Date Code Marking Y = Year (ex: J = 2022) M = Month (ex: 9 = September)

Date Code Key

Date Code Rey												
Year	2010		2022	2023	2024	2025	2026	2027	2028	2029	2030	2031
Code	Х		J	K	L	М	N	0	Р	R	S	Т
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load. For capacitance load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	Vrrm Vrwm Vr	800	V
Average Rectified Output Current (See Figure 4)	lo	1.0	А
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	IFSM	25	А

Thermal Characteristics

Characteristic	Symbol	Тур	Max	Unit
Thermal Resistance, Junction to Ambient Air (Note 5)	Reja	134	_	°C/W
Thermal Resistance, Junction to Soldering Point (Note 6)	Reus		6	°C/W
Operating and Storage Temperature Range	TJ, TSTG		-65 to +150	°C

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Conditions
Reverse Breakdown Voltage (Note 7)	V(BR)R	800		-	V	I _R = 10µA
Forward Voltage Drop	VF	1111	0.65 0.60 0.48 0.94 0.83	1.1 1.0	٧	IF = 1.0mA, T _J = 0°C IF = 1.0mA, T _J = +25°C IF = 1.0mA, T _J = +85°C IF = 1.0A, T _J = +25°C IF = 1.0A, T _J = +125°C
Reverse Leakage Current (Note 7)	IR	17		10 150	μΑ	V _R = 800V, T _J = +25°C V _R = 800V, T _J = +125°C
Reverse Recovery Time	trr	1-2	1.6	_	μs	IF = 0.5A, IR = 1A, IRR = 0.25A
Total Capacitance	Ст	+	10	_	pF	$V_R = 4.0V_{DC}$, $f = 1MHz$

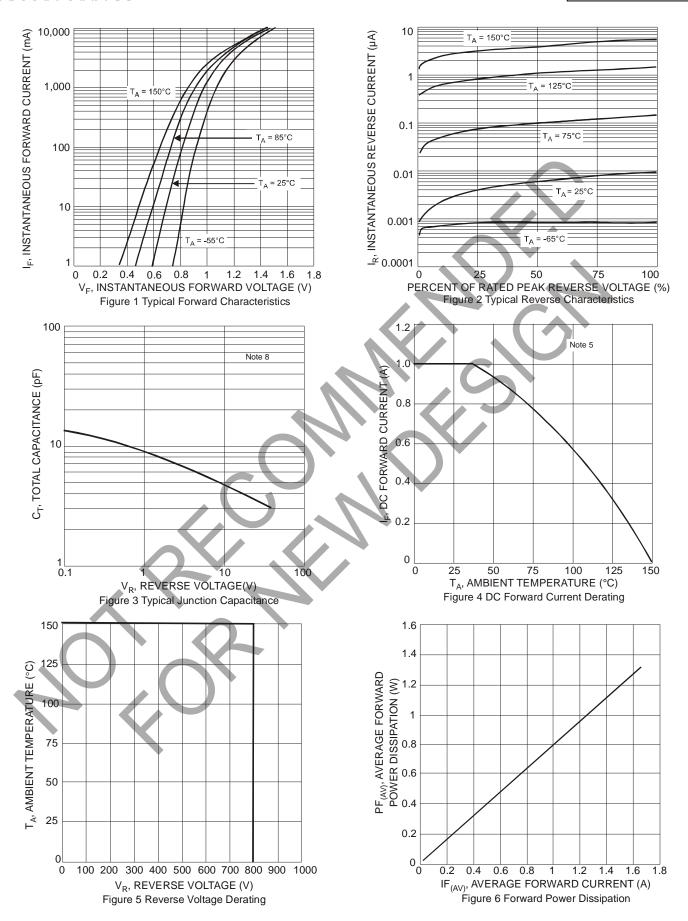
Notes:

- 5. Device mounted on 1in, x 1in., FR-4 PCB; 2oz Cu pad layout as shown on Diodes Incorporated's suggested pad layout, which can be found on website at http://www.diodes.com/package-outlines.html. T_A = +25°C.

 6. Theoretical R_{BJS} calculated from the top center of the die straight down to the PCB/cathode tab solder junction.
- 7. Short duration test pulse used to minimize self-heating effect.







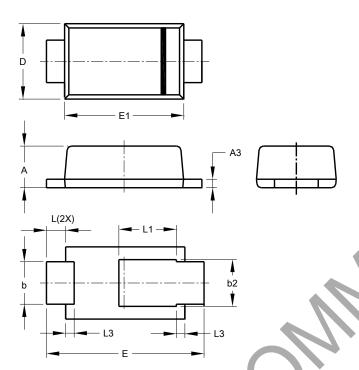
Note: 8. Measured at 1.0MHz and applied reverse voltage of 4.0V DC.



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI123

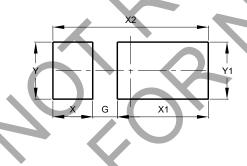


PowerDI123							
Dim	Min	Max	Тур				
Α	0.93	1.00	0.98				
A3	0.15	0.25	0.20				
b	0.85	1.25	1.00				
b2	1.025	1.125	1.10				
D	1.63	1.93	1.78				
Е	3.50	3.90	3.70				
E1	2.60	3.00	2.80				
L	0.40	0.50	0.45				
L1	1.25	1.40	1.35				
L3	0.125	0.275	0.20				
All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI123



Dimensions	Value
פווטופווזטווט	(in mm)
G	0.65
X	1.05
X1	2.40
X2	4.10
Y	1.50
Y1	1.50



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