

Akshay Rajhans

Senior Research Scientist

MathWorks
3 Apple Hill Drive, Natick, MA 01760
✉ Akshay.Rajhans@mathworks.com
📄 arajhans.github.io

Professional Experience

- Jul 16– **Senior Research Scientist**, *MathWorks*, Natick, MA.
Advanced research and technology development with a focus on *cyber-physical systems*, co-organize MathWorks research conferences, co-manage a collaborative research grant program, support cultivation of intellectual property, contribute to long-term research strategy, represent MathWorks in the research community.
- Jul 13–Jul 16 **Senior Software Engineer**, *MathWorks*, Natick, MA.
Work on core semantics of Simulink.
- Aug 09–Dec 09 **Intern**, *Bosch Research and Technology Center*, Pittsburgh, PA.
Developed a new approach to non-intrusive load monitoring using hybrid system state estimation. Co-inventor on **U.S. Patent # 8209062**. See the **Patents** section for the citation.
- Jan 08–Jun 08 **Research Staff**, *GRASP Laboratory, University of Pennsylvania*, Philadelphia, PA.
Worked on improving the STRONG Toolbox originally developed during my masters thesis. This toolbox formed the basis of a Ph.D. thesis of a student at Rensselaer Polytechnic Institute whose Ph.D. Thesis Committee I served on. See the **Ph.D. Thesis Committee** section for a citation.
- Aug 05–Dec 05 **Manager**, *IBU Application Engineering*, Cummins India Limited, Pune, India.
Application engineering for electronic control of diesel engines in mining, marine, defense, rail, compressors, oil rigs, fire pumps, automotive and off-highway construction equipment.
- Aug 03–Aug 05 **Operations Management Program Participant**, Cummins India Limited, Pune, India.
Research, development and application engineering of electronic controls for diesel engines and their applications.

Education

- **Ph.D.**, Electrical and Computer Engineering, *Carnegie Mellon University*, Pittsburgh, PA, U.S.A.
- **M.S.**, Electrical Engineering, *University of Pennsylvania*, Philadelphia, PA, U.S.A.
- **B.E.**, Electronics and Telecommunication, *University of Pune*, Pune, India.

Invited Talks

- “*Heterogeneous Model-Based Design of Tomorrow's Cyber-Physical Systems*,” ECE Department Colloquium, Tufts University, Medford, MA, November 2017. **Host**: Prof. Usman Khan.
- “*Model-Based Design of Next Generation Cyber-Physical Systems*,” MIT LIDS, IDSS, MITeI, Lincoln Labs, NSF and IWR Workshop on Rethinking Modeling, Simulations and Control for the Changing Electric Energy Industry, Massachusetts Institute of Technology, Cambridge, MA, September 2017. **Hosts**: Prof. Marija Ilić and Prof. Ekaterina Kostina.
- “*Challenges and Opportunities for Intelligent Transportation Systems*,” Robotica 2017, Newton, MA, June 2017. **Host**: Dr. Waseem Naqvi, AUVSI New England Chapter President (Chair).
- “*Model-Based Design of Connected Autonomous Vehicles*,” 2nd IEEE Summer School on Connected and Autonomous Vehicles, Worcester Polytechnic Institute, Worcester, MA, May 2017. **Hosts**: Prof. Alexander Wyglinski and Prof. Raghvendra Cowlagi (Program Chairs).
- “*Model-Based Design Challenges for Cyber-Physical Systems*,” Expeditions in Computer Augmented Program Engineering (ExCAPE) Principal Investigators' (PI) Meeting, University of Pennsylvania, Philadelphia, PA, May

2017. **Host:** Prof. Rajeev Alur (Principal Investigator).

- “*Safety in Freely-Composed Cyber-Physical Systems—Challenges and Opportunities*,” with Pieter Mosterman, Exploring the Dimensions of Trustworthiness: Challenges and Opportunities Workshop, National Institute of Standards and Technology (NIST), Gaithersburg, MD, August, 2016. **Host:** Dr. Edward Griffor (Program Chair).
- “*Recent Advancements in MathWorks Verification and Validation Tools and Techniques*”, CPS V&V I&F Workshop 2016, May 2016, Carnegie Mellon University. **Host:** Prof. André Platzer.
- “*Verification of Systems Using Robust Temporal Logic Testing*”, Specification and Verification Center, School of Computer Science, Carnegie Mellon University, September 2008. **Host:** Prof. Ed Clarke.
- “*Robustness of Temporal Logic Specifications for Testing of Signals*”, Specification and Verification Center, School of Computer Science, Carnegie Mellon University, August 2008. **Host:** Prof. Ed Clarke.

■ Panels

- **Panelist**, *Why do we need holistic concern-driven engineering?*, CPS Framework Open Source Workshop, National Institute for Standards and Technology (NIST), Rockville, MD, September 2017.
- **Panelist**, *Safety of Connected Autonomous Vehicles*, First International Workshop on the Safety of Connected Autonomous Vehicles (SCAV), CPS Week, Pittsburgh, PA, May 2017.
- **Student Panelist**, *Prospective Student Open House*, ECE Department, Carnegie Mellon University, February 2012.

■ Ph.D. Thesis Committee

- Yi Deng, ECSE Department at Rensselaer Polytechnic Institute. **Advisor:** Prof. A. Agung Julius. **Thesis Title:** “*The Application of Trajectory-Based Analysis for Hybrid Systems*.” Defended July 2015.

■ Technical Committees

- IEEE Technical Committee on Homeland Security

■ Industry Advisory Committees and Boards

- Autonomous Vehicles Industrial Advisory Committee, Worcester Polytechnic Institute
- Global Professional Advisory Community, Association for Computing Machinery

■ Conference Program Committee (PC)

- (2018) **Industry Board Member**, International Symposium on Circuits and Systems (ISCAS), **Industry Vice Chair**, IFAC Conference on Analysis and Design of Hybrid Systems (ADHS), **Awards Chair**, Hybrid Systems: Computation and Control (HSCC), **PC Member**, Hybrid Systems: Computation and Control (HSCC), International Conference on Informatics in Control, Automation and Robotics (ICINCO)
- (2017) **PC Chair**, MathWorks Research Summit, **PC Chair**, MathWorks Asia Research Summit, **CPS Track Chair**, Winter Simulation Conference (WSC), **Demo and Poster Chair**, Hybrid Systems: Computation and Control (HSCC), **PC Member**, Summer Simulation Multi-Conference (SummerSim), Hybrid Systems: Computation and Control (HSCC), International Conference on Informatics in Control, Automation and Robotics (ICINCO), Computational Intelligence Techniques for Testing and Validating Complex CPSs (CITest_CPS), Workshop on Formal Co-Simulation of Cyber-Physical Systems (CoSim-CPS)
- (2016) **PC Member**, Hybrid Systems: Computation and Control (HSCC)
- (2015) **PC Member**, International Conference on Cyber-Physical Systems (ICCPs), Conference on Analysis and Design of Hybrid Systems (ADHS), Summer Simulation Multi-Conference (SummerSim)
- (2014), **PC Member**, Summer Simulation Multi-Conference (SummerSim), **Repeatability Evaluation Committee Member**, Hybrid Systems: Computation and Control (HSCC)

Student Competitions

- (2017) **MathWorks Technical Lead**, *CAT Vehicle Challenge*, an autonomous vehicle modeling and simulation competition hosted by University of Arizona, **Instructor**: Prof. Jonathan Sprinkle.
- (2017, 2016) **Judge**, *CPS V&V Grand Prix*, Course Competition for 15-424/15-624/15-824: Foundations of Cyber-Physical Systems, Carnegie Mellon University, **Instructor**: Prof. André Platzer.

Patents

- PP1. Burton Andrews, Diego Benitez, Badri Raghunathan and **Akshay Rajhans**, "*Method for Non-Intrusive Load Monitoring using a Hybrid System State Estimation Approach*", U.S. Patent # 8209062, granted on June 26, 2012. Also filed as European and International Patents # EP 2514068 A1 and # WO 2011084390 A1.

Theses

- T1. **Akshay Rajhans**, "Multi-Model Heterogeneous Verification of Cyber-Physical Systems", Ph.D. Thesis, Department of Electrical and Computer Engineering, Carnegie Mellon University, 2013. **Advisor**: Prof. Bruce H. Krogh.
- T2. **Akshay Rajhans**, "Development of a Robust Testing Toolbox for Hybrid Systems", M.S.E. Thesis, Department of Electrical and Systems Engineering, University of Pennsylvania, 2007. **Advisor**: Prof. George J. Pappas.

Journal Publications

- J1. **Akshay Rajhans**, Ajinkya Bhawe, Ivan Ruchkin, Bruce H. Krogh, David Garlan, André Platzer and Bradley Schmerl, "*Supporting Heterogeneity in Cyber-Physical System Architectures*", IEEE Transactions on Automatic Control, Special issue on Cyber-Physical Systems, Volume 59, Issue 12, Pages 3178-3193.
- J2. Matthias Althoff, **Akshay Rajhans**, Bruce H. Krogh, Soner Yaldiz, Xin Li and Larry Pileggi, "*Formal Verification of Phase-Locked Loops Using Reachability Analysis and Continuization*", Communications of the ACM, Volume 56, Issue 10, Pages 97-104. **Research Highlight for the October 2013 issue**.
- J3. **Akshay Rajhans**, Shang-Wen Cheng, Bradley Schmerl, David Garlan, Bruce H. Krogh, Clarence Agbi, and Ajinkya Bhawe, "*An Architectural Approach to the Design and Analysis of Cyber-Physical Systems*", Electronic Communications of the EASST, Volume 21, 2009.

Book Chapters

- B1. Yi Deng, **Akshay Rajhans**, and A. Agung Julius, "*STRONG: A Trajectory-Based Verification Toolbox for Hybrid Systems*", in Kaustubh Joshi, Markus Siegle, Mariëlle Stoelinga and Pedro R. D'Argenio, editors, Lecture Notes in Computer Science, *10th International Conference, QEST 2013, Buenos Aires, Argentina, August 27-30, 2013. Proceedings*, Volume 8054, Pages 165-168, Springer, 2013.
- B2. Alexandre Donzé, Bruce H. Krogh, and **Akshay Rajhans**, "*Parameter Synthesis for Hybrid Systems with an Application to Simulink Models*", in Rupak Majumdar and Paulo Tabuada, editors, Lecture Notes in Computer Science, *Hybrid Systems: Computation and Control, 12th International Conference, HSCC 2009, San Francisco, CA, USA, April 13-15, 2009. Proceedings*, Volume 5469, Pages 165-179, Springer, 2009.

Conference Papers

- C1. **Akshay Rajhans** and Bruce H. Krogh, "*Compositional Heterogeneous Abstraction*", in Proceedings of the 16th ACM International Conference on Hybrid Systems: Computation and Control (HSCC), 2013.
- C2. **Akshay Rajhans** and Bruce H. Krogh, "*Heterogeneous verification of cyber-physical systems using behavior relations*", in Proceedings of the 15th ACM International Conference on Hybrid Systems: Computation and Control (HSCC), 2012.
- C3. **Akshay Rajhans**, Ajinkya Bhawe, Sarah Loos, Bruce H. Krogh, André Platzer, and David Garlan, "*Using Parameters in Architectural Views to Support Heterogeneous Design and Verification*", in Proceedings of the 50th IEEE Conference on Decision and Control (CDC), 2011.
- C4. Matthias Althoff, **Akshay Rajhans**, Bruce H. Krogh, Soner Yaldiz, Xin Li, and Larry Pileggi, "*Formal Verification*

- of Phase-Locked Loops Using Reachability Analysis and Continuization", in Proceedings of the IEEE/ACM International Conference on Computer-Aided Design (ICCAD), 2011. **William J. McCalla Best Paper Award**.
- C5. Ajinkya Bhawe, David Garlan, Bruce H. Krogh, **Akshay Rajhans**, and Bradley Schmerl, "Augmenting Software Architectures with Physical Components", in Proceedings of the Embedded Real Time Software and Systems Conf. (ERTS²), 2010.

Peer Reviewed Extended Abstracts

- A1. Matthias Althoff, **Akshay Rajhans**, Bruce H. Krogh, Soner Yaldiz, Xin Li, Larry Pileggi, "Using Continuization in Reachability Analysis for the Verification of a Phase-Locked Loop", Frontiers in Analog Circuit (FAC) Synthesis and Verification, co-located with Computer-Aided Verification (CAV) 2011, Snowbird, UT.
- A2. Ajinkya Bhawe, David Garlan, Bruce H. Krogh, Sarah Loos, André Platzer, **Akshay Rajhans**, Bradley Schmerl, "Multi-View Consistency in Architectures for Cyber-Physical Systems", Safe and Secure Systems & Software Symposium (S5) 2011, Beavercreek, OH.

Other Miscellaneous Writing

- O1. **Akshay Rajhans**, "EGO Insider's Guide", ECE Graduate Organization (EGO), Carnegie Mellon University, 2012. Edited. Available at <http://www.ece.cmu.edu/~ego/files/insiders/guide2012.pdf>.

Honors

- Selected on ACM's **Global Practitioner Advisory Community**, 2017.
- Work featured as **Research Highlight** in *Communications of the ACM* magazine, 2013.
- Work featured in *Innovation with Impact*, Carnegie Mellon University, 2013.
- William J. McCalla Best Paper Award**, ACM/IEEE International Conference on Computer-Aided Design, 2011.
- Carnegie Institute of Technology **Dean's Fellowship**, August 2008–May 2013.
- Ranked in **top 0.48%** in India (percentile score of 99.52), Common Admission Test, 2005.
- National Talent Search (NTS) Scholarship, finalist, India, 1997.
- Maharashtra Talent Search (MTS) Scholarship, State-level rank: **35** (1996), **15** (1995).
- Middle School Scholarship, Maharashtra, India. State-level rank: **16** (1991-1994).

Service for Social Cause

- (2017-2019) **Member, Organizing Committee**, New England Marathi Mandal, a non-profit, www.nemm.org.
- (2008-2011) **Member, Board of Directors**, Maharashtra Mandal Pittsburgh, a non-profit, www.mmpgh.org.
- (2009) **Laboratory Instructor**, Summer Engineering Experience for Girls (SEE), a day-long summer camp for high-school students at Carnegie Mellon University, **Instructor**: Prof. Bruno Sinopoli.
- (2002) **Instructor**, Social Educational Activity, organized by the IEEE Bombay Section Region 10 to create awareness amongst high-school students, **Topic**: Mobile Communications.

Software

Developer Simulink, Stateflow, SimEvents, STRONG.

Languages MATLAB, C++, C, some Java.