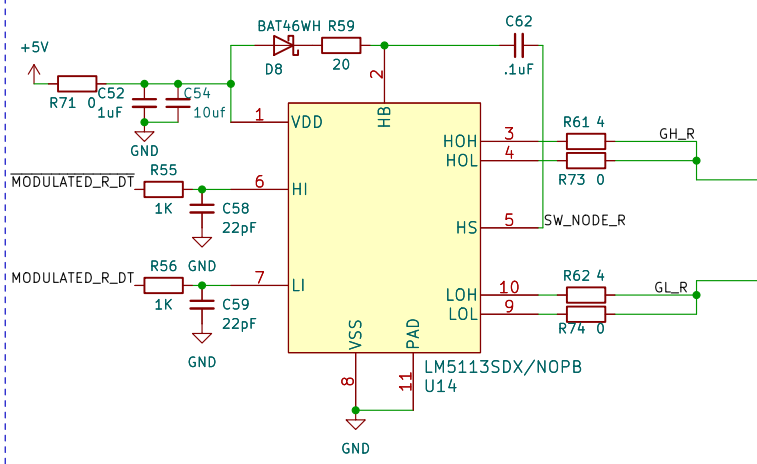
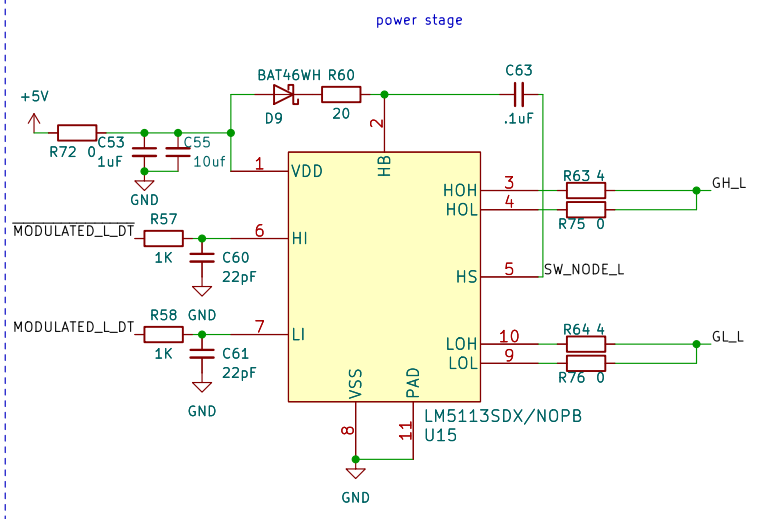


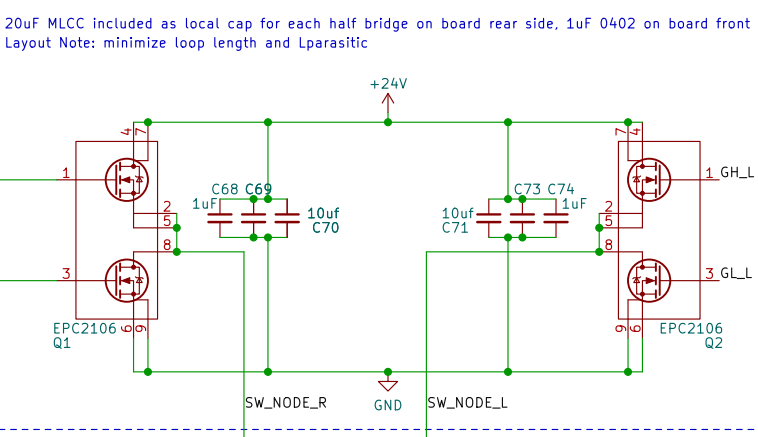
### GaN gate driver stage



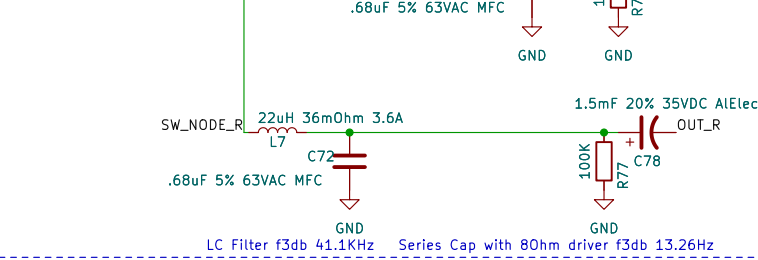
bypass & bootstrap caps well exceed required C based on Qg Qrr & fsw  
gate resistors chosen to source 1A and sink 2.2A, for Ton .9nS and Toff .41nS  
7.2MHz input filter, to prevent input pulses < 3nS  
at 500K, pulse width = 1uS. Max frequency is 333KHz



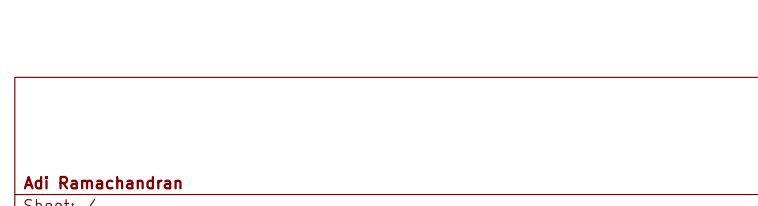
### GaN half bridge stage



20uF MLCC included as local cap for each half bridge on board rear side, 1uF 0402 on board front  
Layout Note: minimize loop length and Lparasitic



LC Filter f3db 41.1KHz Series Cap with 80hm driver f3db 13.26Hz



Adi Ramachandran		
Sheet: /	File: amp.kicad_sch	
Title: Class-D GaN Audio Amplifier		
Size: User	Date: 2023-01-09	Rev: 0.0
KiCad E.D.A.	KiCad (6.0.7)	Id: 1/1