

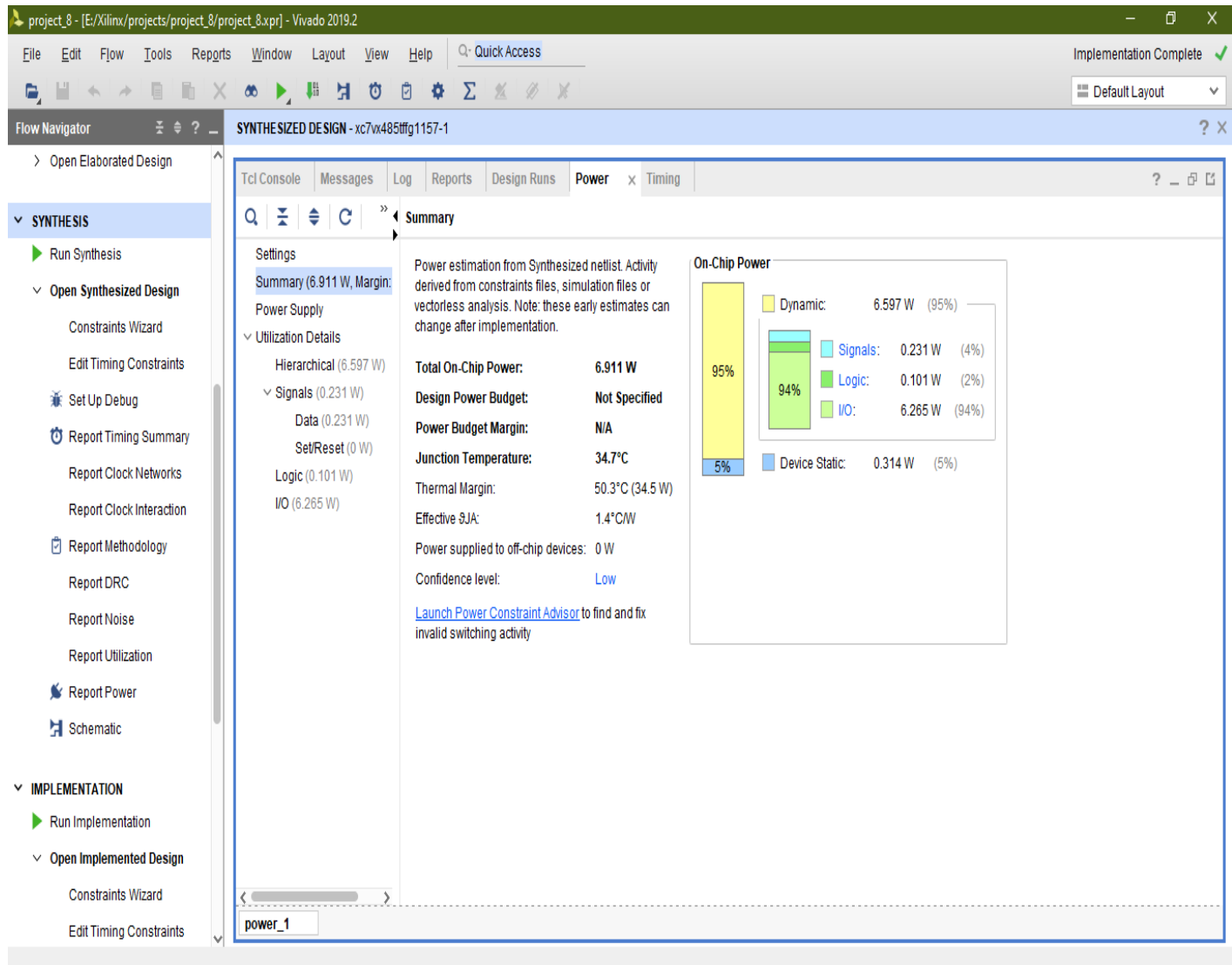
Assignment 2

Submitted by- Archit Rathi

300065384

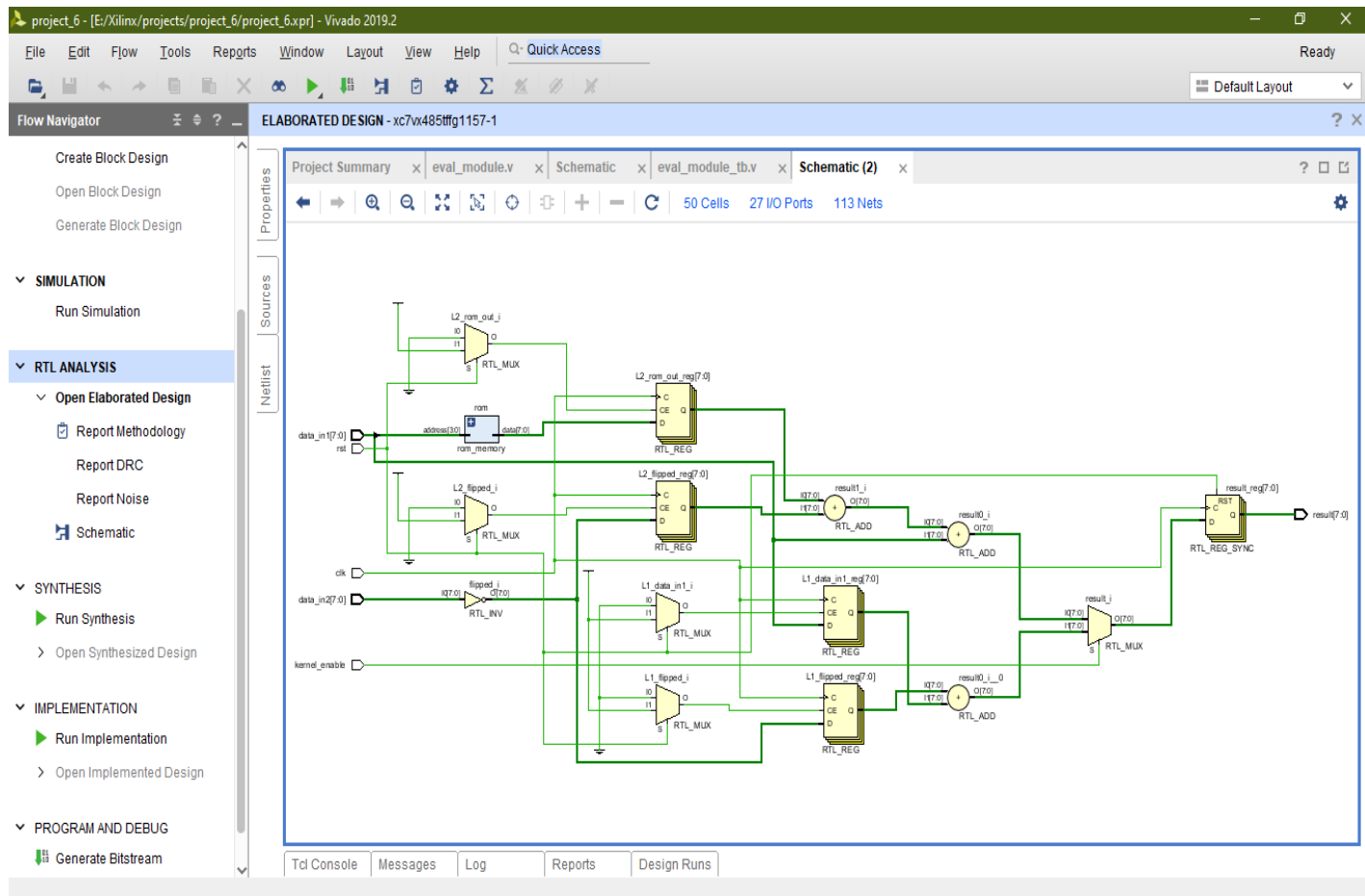
Power Analysis

Original Implementation

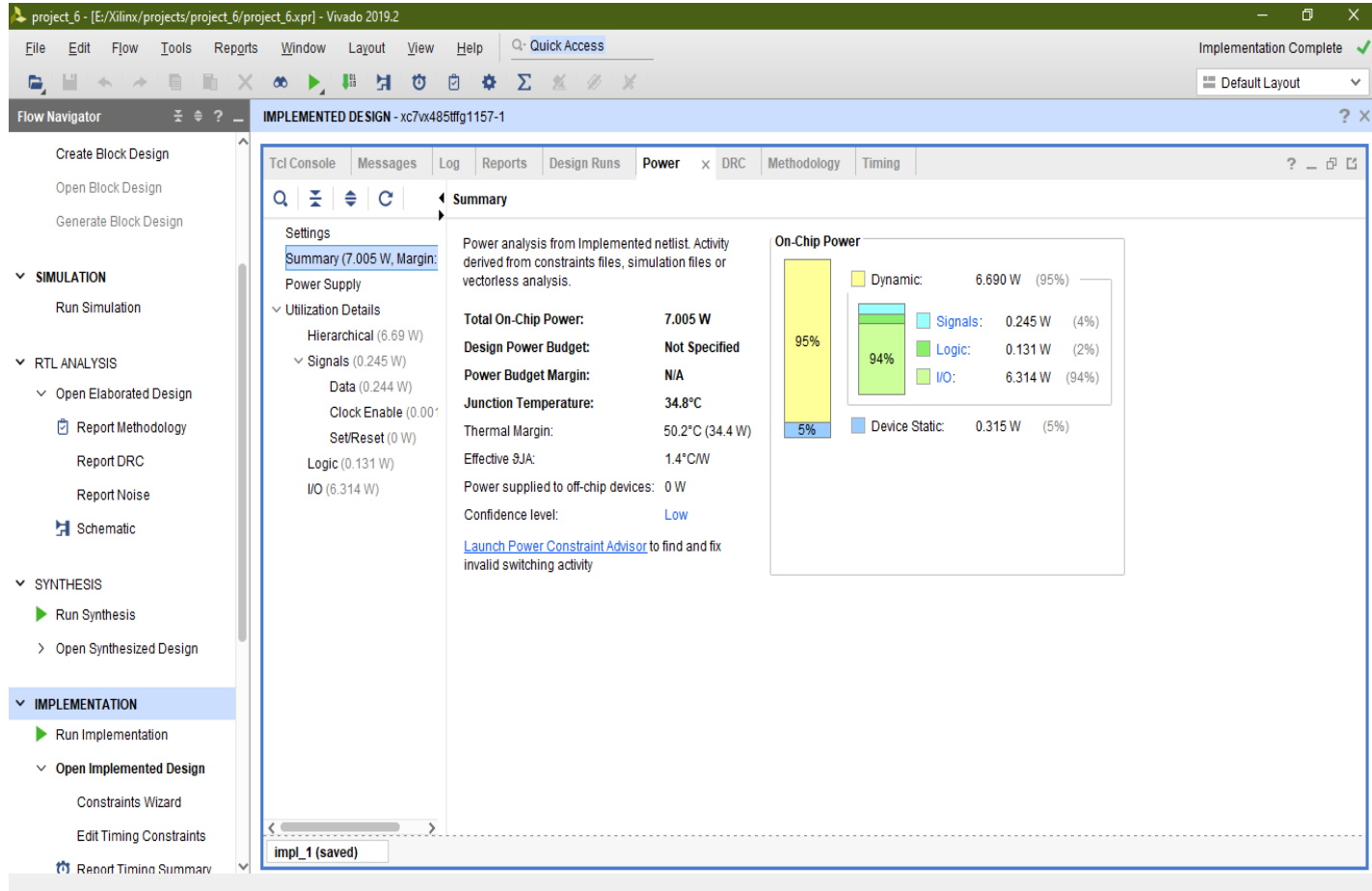


Pipelining

Schematic

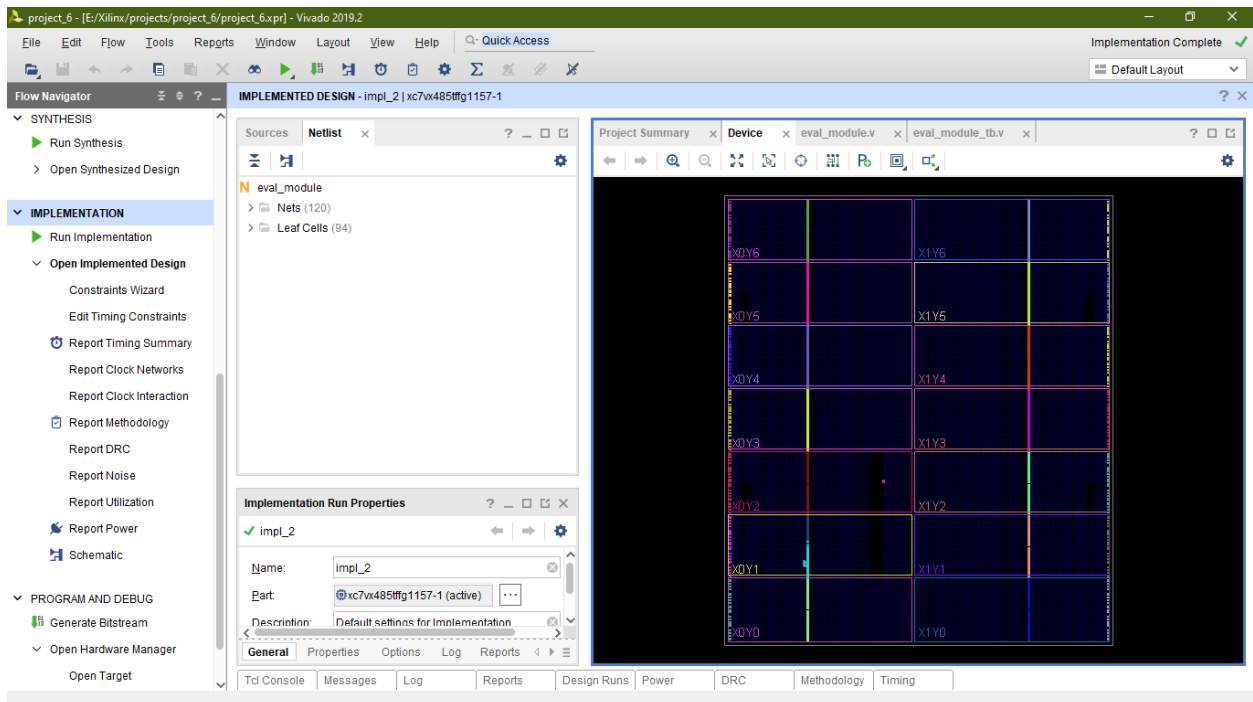


Power Analysis



After Power Optimization using Clock Gating

Schematic



Power Analysis

