



## Lecture: 26

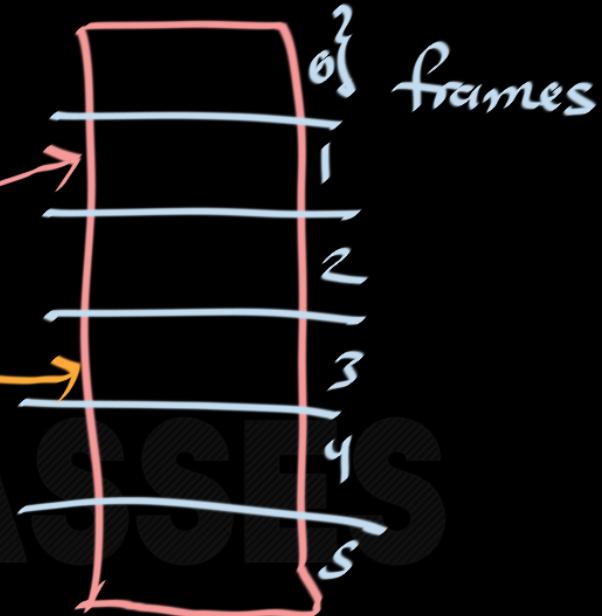
# CLASSES

## Paging

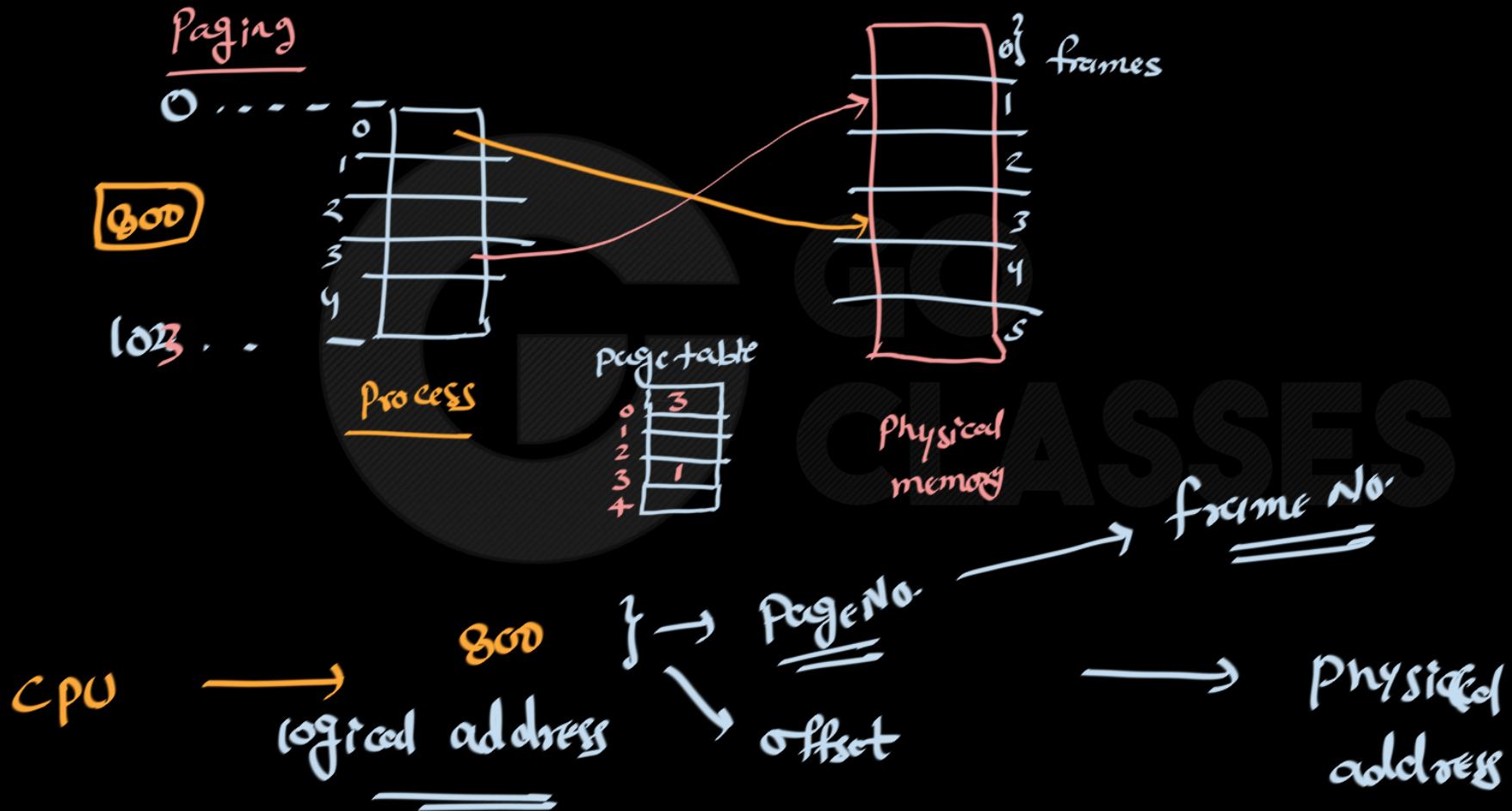


process

page table	
0	3
1	
2	
3	1
4	



Physical  
memory



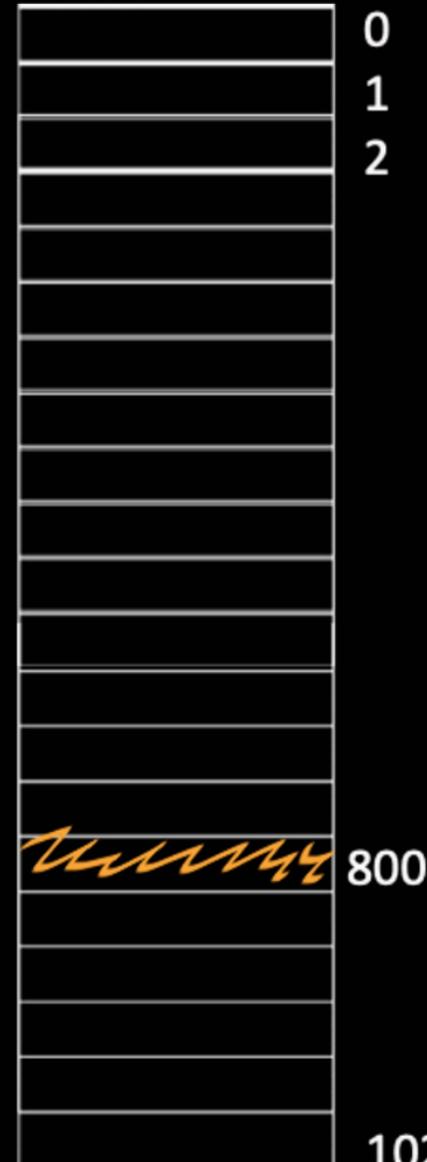
Each page contains = 8 Bytes  
800 will be in 100<sup>th</sup> page number

physical address  
of 803 ?



Pg No	Frame No
0	
1	
100	5
$2^7 - 1$	

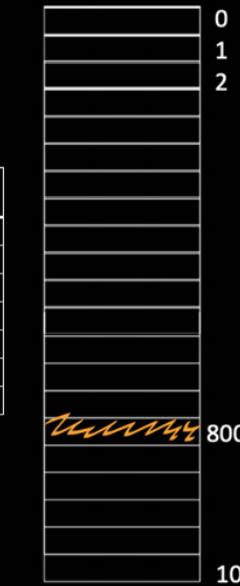
$2^7$  Entries



Each page contains = 8 Bytes  
800 will be in 100<sup>th</sup> page number

physical address  
of 803 ?

Pg No	Frame No
0	
1	
100	5
$2^7 - 1$	



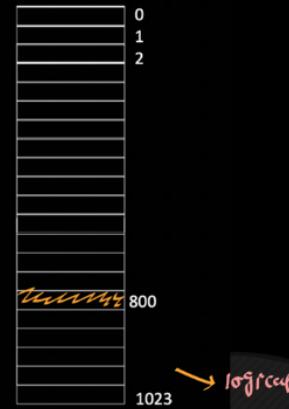
$$\frac{803}{\text{Page size}} = \underbrace{100. \text{xx}}_{\text{Page No.}} \Rightarrow \text{frame No.} = 5$$

$803 / 2^3 = 3$

Each page contains = 8 Bytes  
800 will be in 100<sup>th</sup> page number

physical address  
of 803?

Pg No	Frame No
0	
1	
100	5
$2^7 - 1$	



$$\frac{803}{\text{page size}} \rightarrow 2^3$$

$$= \underbrace{100. \text{xx}}_{\text{Page No.}} \Rightarrow \begin{aligned} 803 \% 2^3 &= 3 \\ \text{frame No.} &= 5 \end{aligned}$$

Physical address =  $8 \times 5 + 3 = 43$

Physical address

$$803 = \overbrace{100 \times 2^3}^{\text{Page size}} + \underbrace{3}_{\text{remainder}}$$

we will do division in binary

(Reason: because page size is in power of 2)

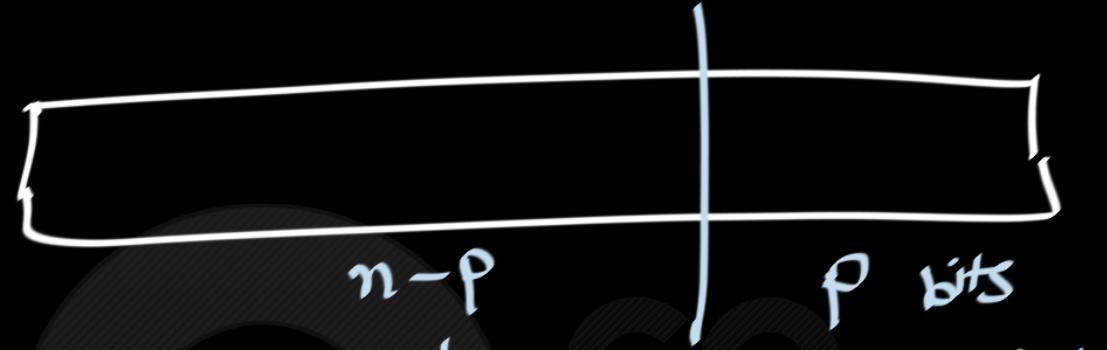
$$803 = \overbrace{100 \times 2^3}^{\text{Page size}} + 3 \quad \text{remainder}$$

we will do division in binary

(Reason: because page size is in power of 2)

$$803 \equiv \begin{array}{c|c} 1100100 & 011 \\ \hline \text{Page No.} & \text{offset} \end{array}$$

logical address =  $n$  bits

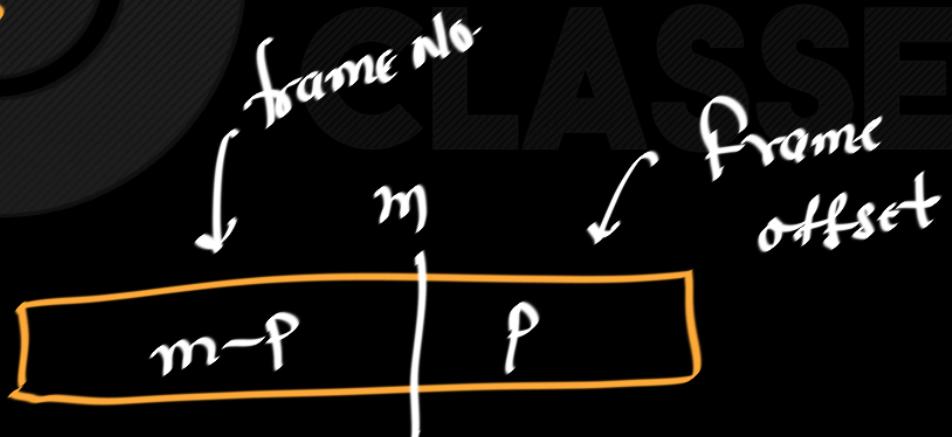


$n-p$   
page No.

$p$  bits  
page offset

Page Size =  $2^p$

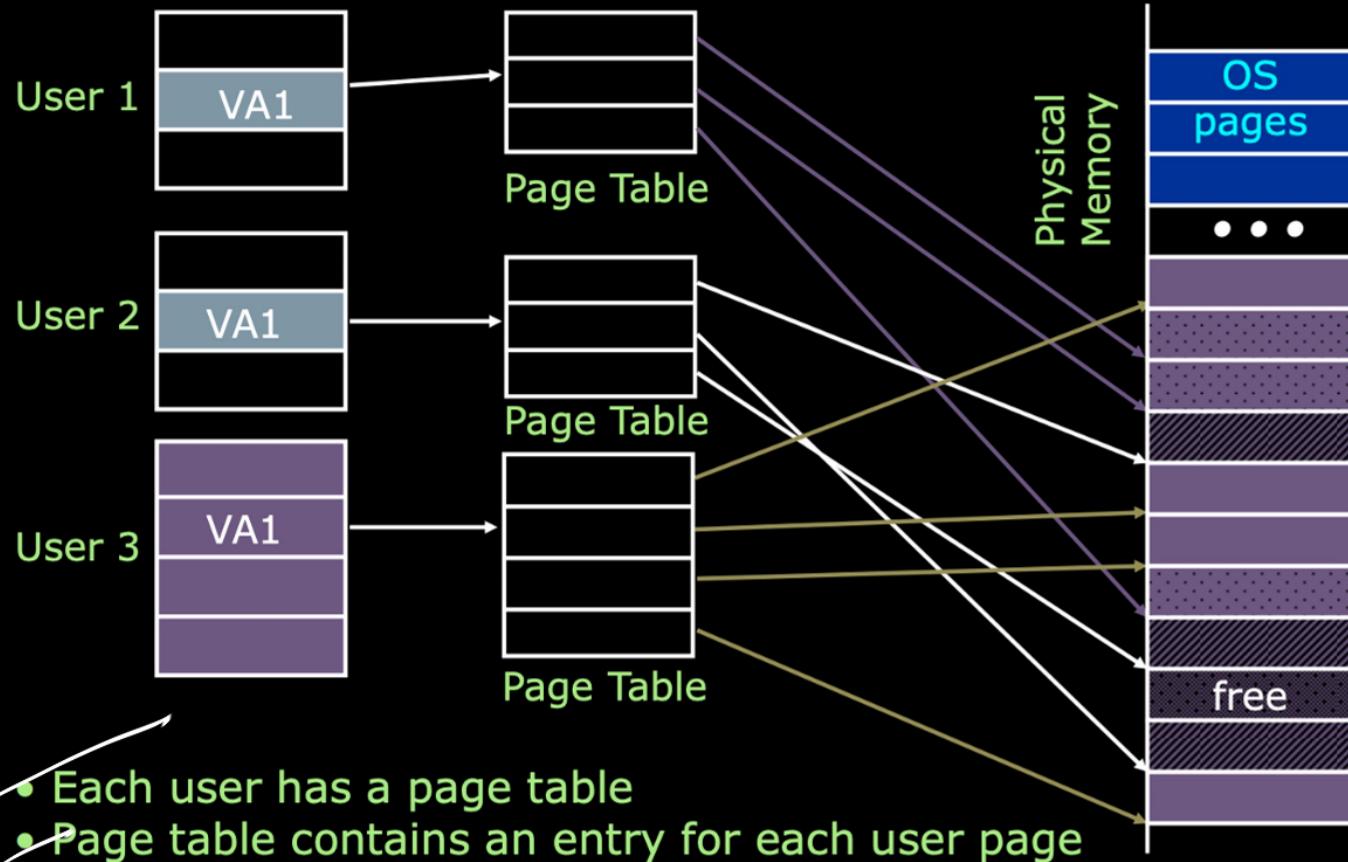
Physical  
address



$m$

frame  
offset

$m-p$  |  $p$



<http://csg.csail.mit.edu/6.823S15/lectures/L07.pdf>

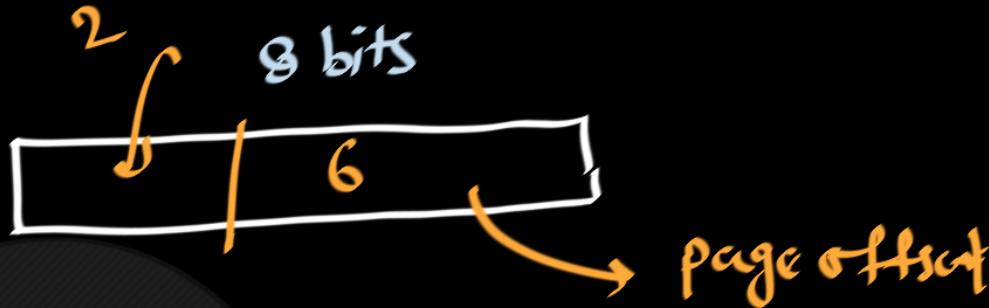


## Page translation exercise

- 8-bit virtual address, 10-bit physical address, and each page is 64 bytes
  - How many virtual pages?
  - How many physical pages?
  - How many entries in page table?
  - Given page table = [2, 5, 1, 8], what's the physical address for virtual address 241?

5

VA =

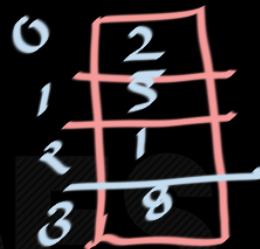


PA =



$$\text{Page size} = 64 \text{ B} = 2^6 \text{ B}$$

*One entry for each page*



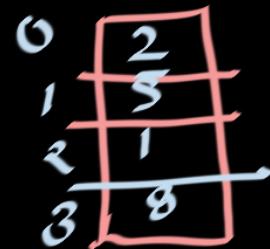
- How many virtual pages?  $\rightarrow 2^2 = 4$
- How many physical pages?  $\rightarrow 2^4 = 16$
- How many entries in page table?  $= 4$
- Given page table = [2, 5, 1, 8], what's the physical address for virtual address 241?

VA 241  $\Rightarrow$  PA?

decimal :

$$\frac{241}{64} = 3$$

$$241 = \underbrace{3 \times 64}_{\text{Page No.}} + \underbrace{49}_{\text{offset}}$$



$$8 \times 64 + 49 = \underbrace{561}_{\text{physical address}}$$

VA 241  $\Rightarrow$  PA?

0	2
1	5
2	1
3	8

decimal :  $\frac{241}{64} = 3$

$$241 = \underbrace{3 \times 64}_{\text{Page No.}} + \underbrace{49}_{\text{offset}}$$

$$8 \times 64 + 49 = \underbrace{561}_{\text{physical address}}$$

Binary :

$$241 = \overline{11} \bigg| \overline{110001}$$

Page No.      Page offset

$$\text{Page No.} = 3 \Rightarrow \text{frame no.} \Rightarrow 8$$

Binary :

$$291 = 11|110001$$

Page No.

page no. = 3  $\Rightarrow$

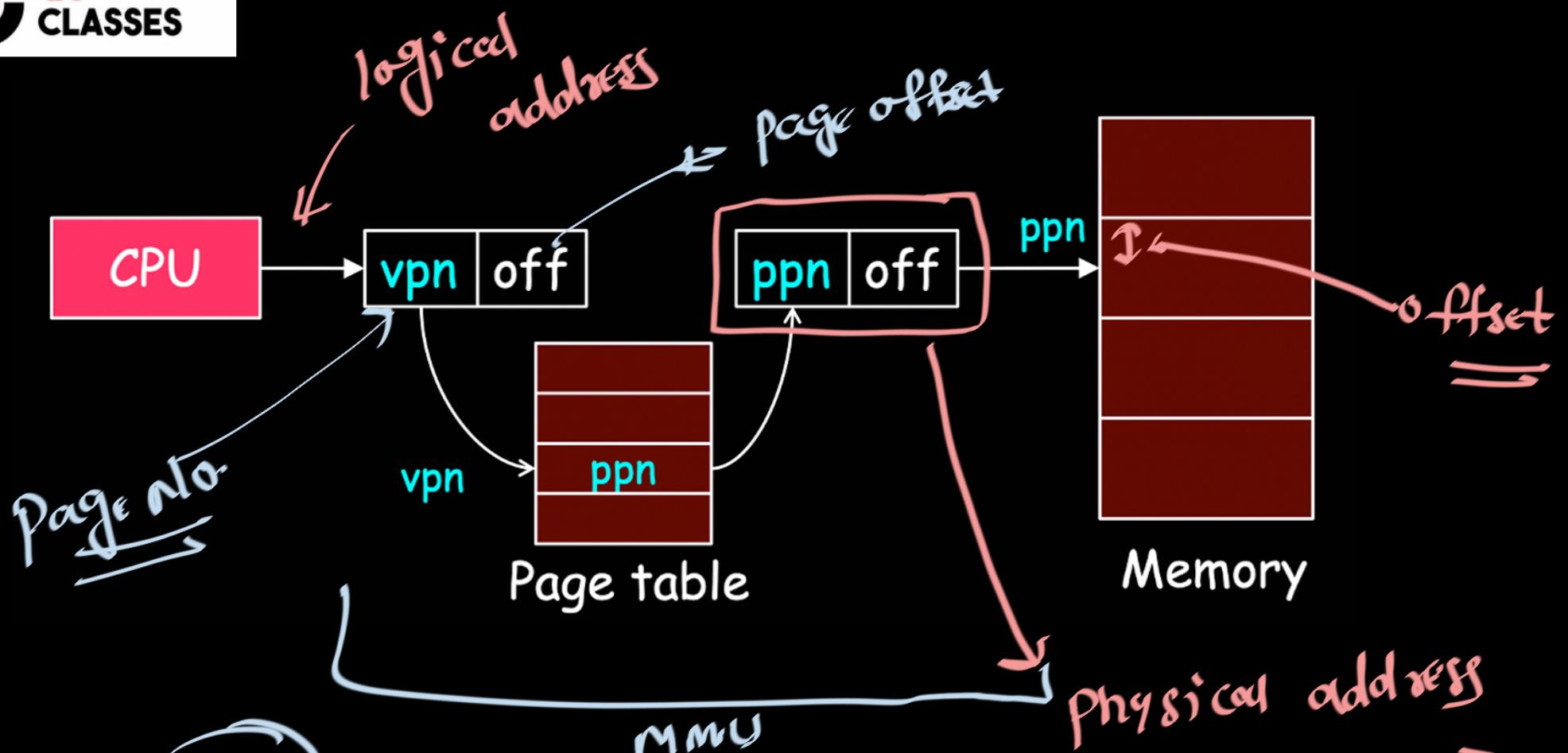
frame no.  $\Rightarrow$  8

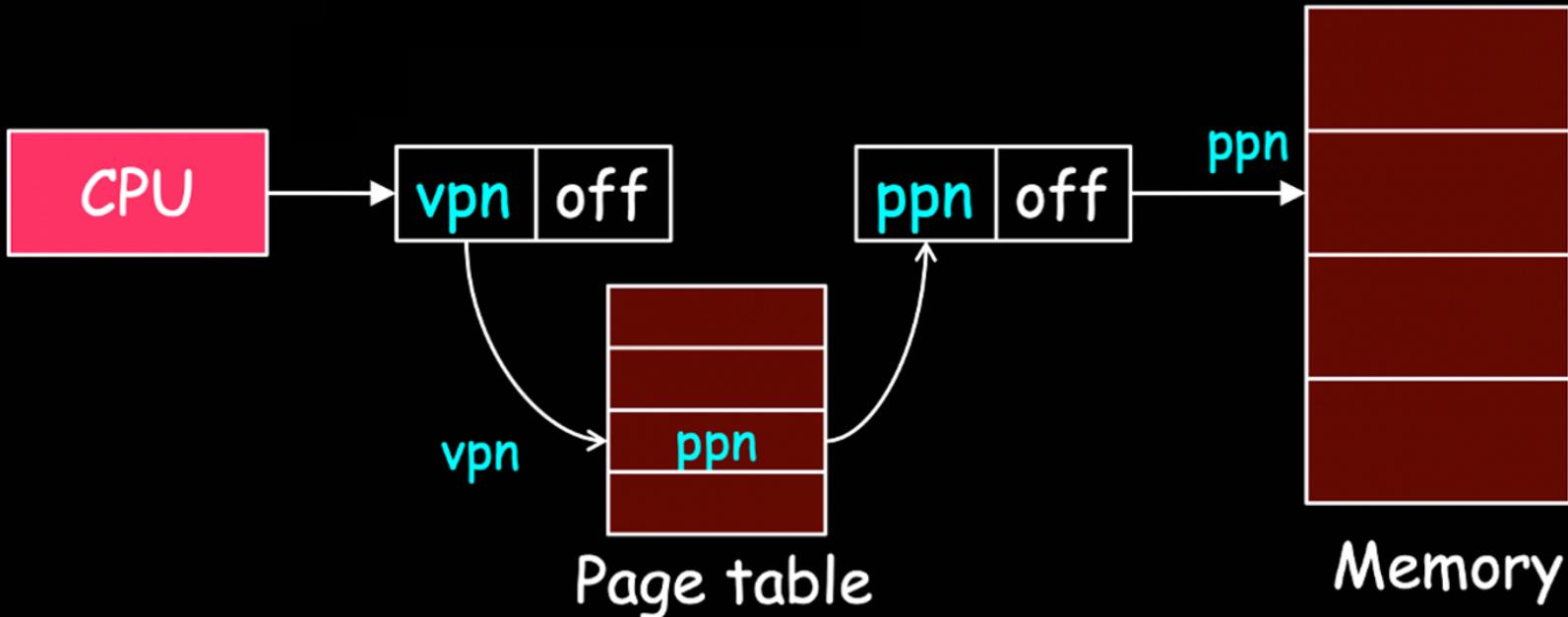
Physical  
address

$$\begin{array}{c|c} 1000 & 11\ 0001 \\ \hline \text{frame No.} & \text{frame offset} \end{array} \Rightarrow 561$$

Address translation in the  
hardware (MMU) for flat page  
table

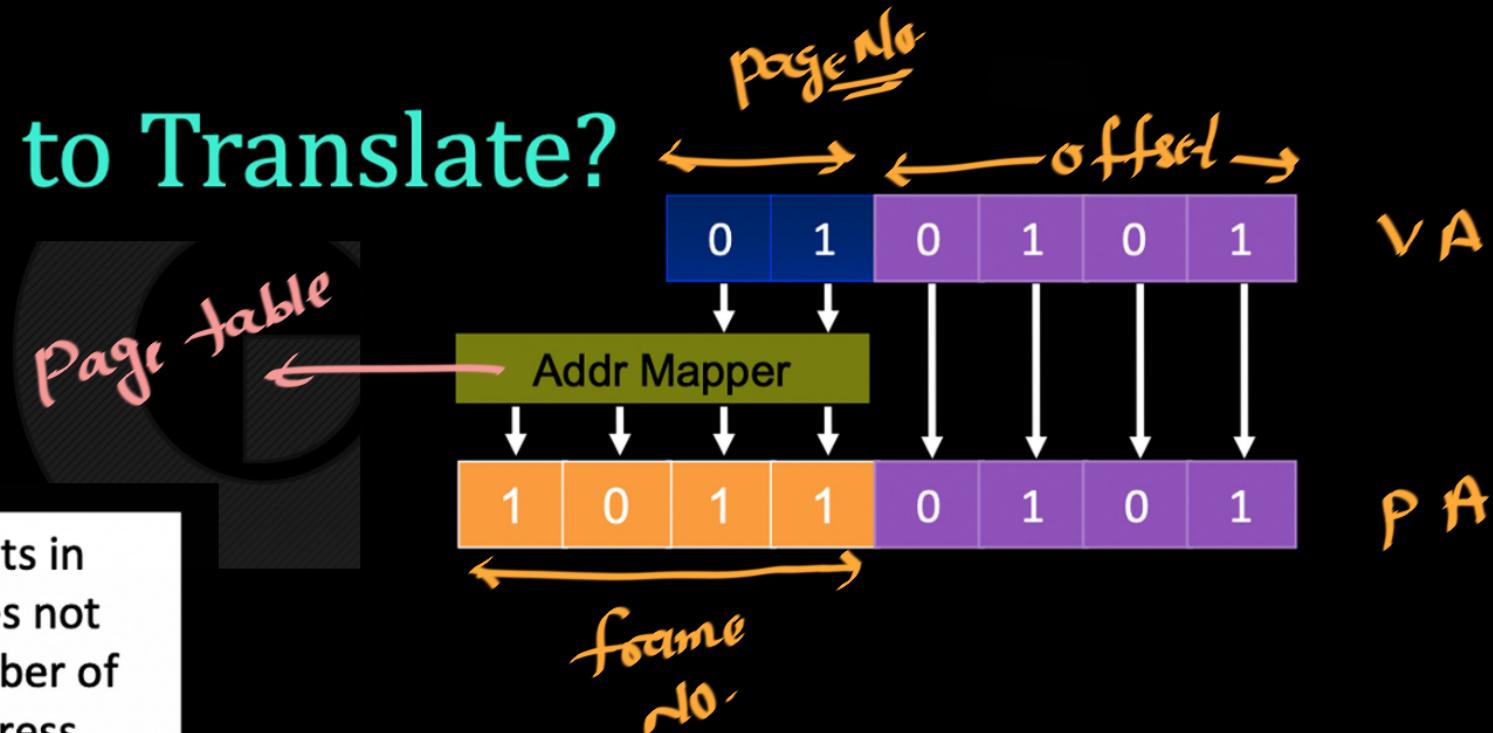
Memory management unit





Optional Image

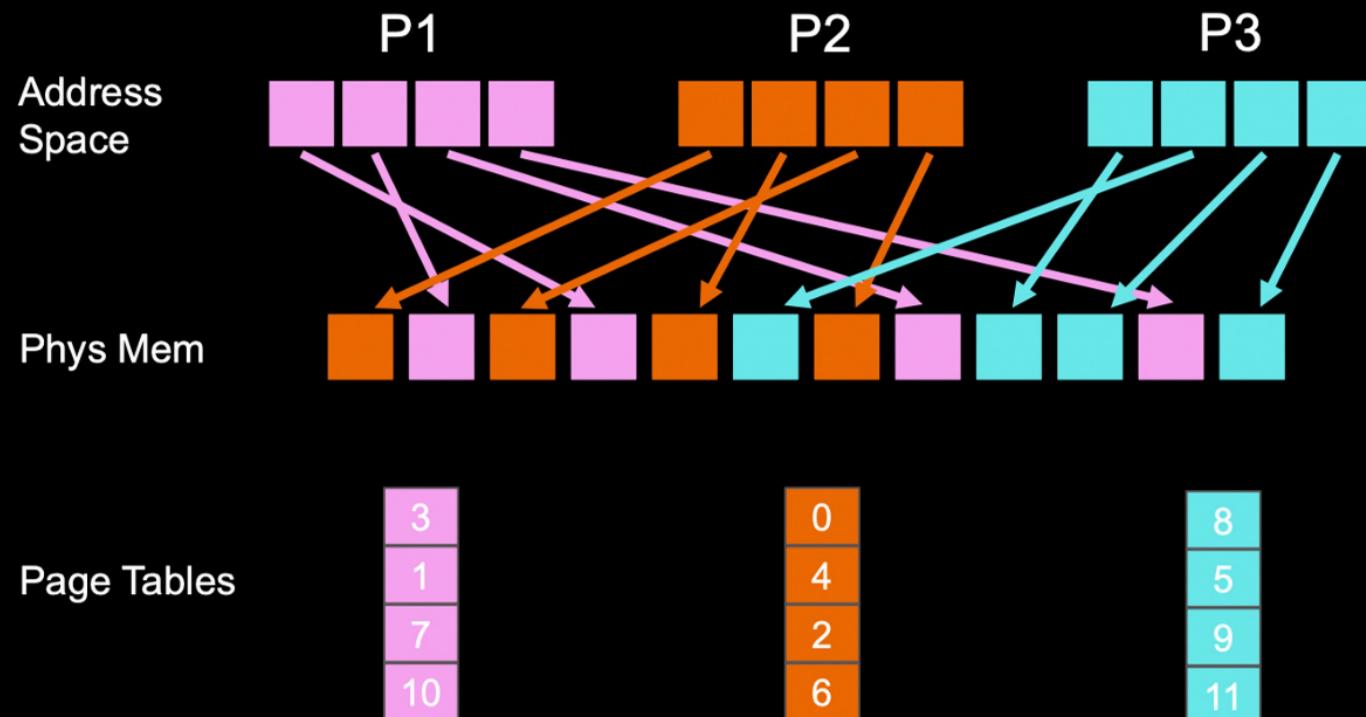
## How to Translate?

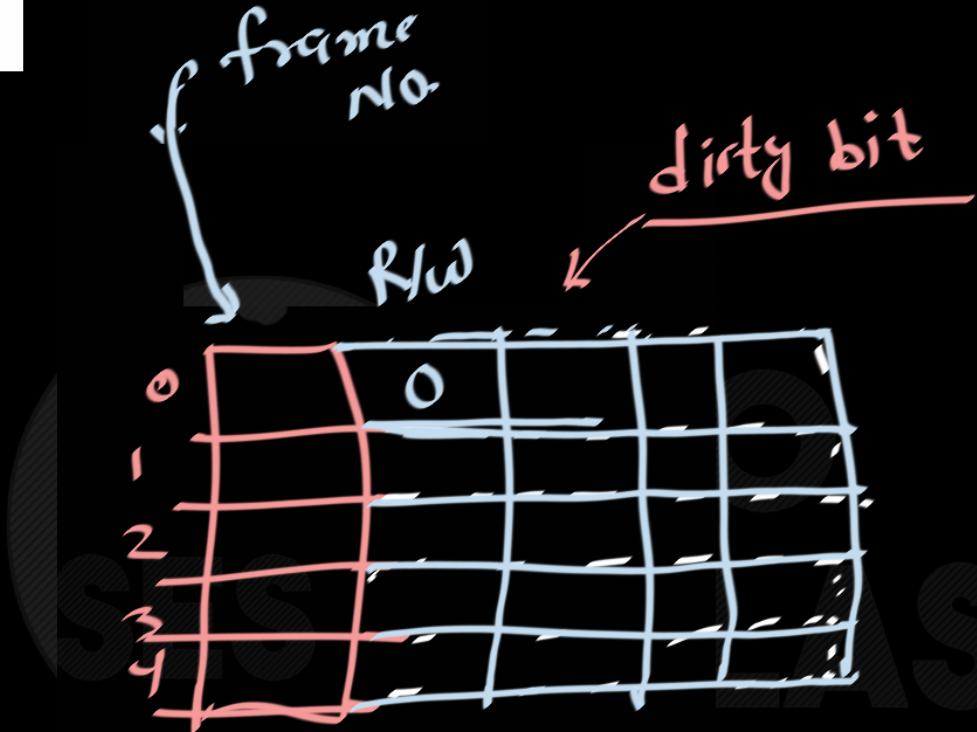




Optional Image

## Example: Fill in the Page Tables





Page Table

PA = 

LA = 

No. of entries in Page table =  $2^2 = 4$

Size of the page table :-

Size of one entry  $\times$  No. of entries



Example -1

$$PA = \boxed{4 \quad | \quad 6}$$

$$LA = \boxed{2 \quad | \quad 6}$$

No. of entries in page table =  $2^2 = 4$

Size of the page table :-

$$\frac{\text{Size of one entry} \times \text{No. of entries}}{}$$



$$4 \times 4 = 16 \text{ bits} = \textcircled{2B}$$

## Example - 2

$$PA = \boxed{4 \quad | \quad 6}$$

$$LA = \boxed{2 \quad | \quad 6}$$

No. of entries in page table  $\leftarrow 2^2 = 4$

size of the page table :-

$$\frac{\text{Size of one entry} \times \text{No. of entries}}{4 \times 4} = 16 \text{ bits} = 2B$$



(PTE)

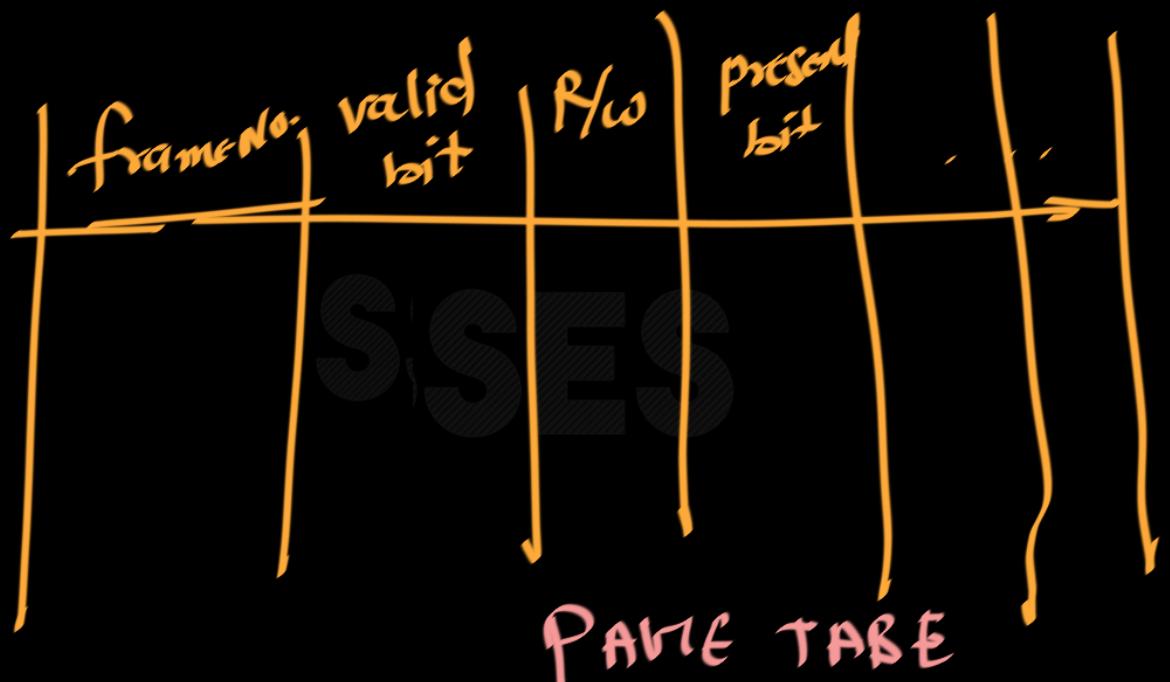
Page table entry size = 2 bytes (given)

page table size =  $2B \times 4 = 8 \text{ bytes}$

# Other PTE Info

- What other info is in PTE besides PFN?

- **Valid bit**
- **Protection bit**
- **Present bit** (needed later)
- **Referenced bit** (needed later)
- **Dirty bit** (needed later)





## Common Flags Of Page Table Entry

- **Valid Bit:** Indicating whether the particular translation is valid.
- **Protection Bit:** Indicating whether the page could be read from, written to, or executed from
- **Present Bit:** Indicating whether this page is in physical memory or on disk(swapped out)
- **Dirty Bit:** Indicating whether the page has been modified since it was brought into memory
- **Reference Bit(Accessed Bit):** Indicating that a page has been accessed



## Complete Page Table Entry (PTE)

Valid	Protection R/W/X	Ref	Dirty	Frame number
-------	------------------	-----	-------	--------------

### Synonyms:

- Valid bit == Present bit
- Dirty bit == Modified bit
- Referenced bit == Accessed bit



## Question

- Logical address = 256KB
- Physical address = 64KB
- Page size = 4 KB

Calculate:

- Number of pages
- Number of bits in page offset
- Number of frames
- If each page table entry size is  $2^p$  Bytes then size of page table



## Question

- Logical address = 256KB
- Physical address = 64KB
- Page size = 4 KB

$$\rightarrow 2^8 \times 2^{10} B = 2^{18} B$$

$$2^6 \times 2^{10} B = 2^{16} B$$

Calculate:

- Number of pages
- Number of bits in page offset
- Number of frames
- If each page table entry size is  $2^p$  Bytes then size of page table



$$2^6 \times 2^p = 2^{6+p}$$

No. of pages =  $2^6 = 64$



# Question

- Physical address = 128MB
- Page size = 4 KB
- Number of pages =  $2^6$

Calculate:

- Number of frames
- Page offset bits
- Logical address space size
- If each page table entry size is  $2^p$  Bytes then size of page table



## Question

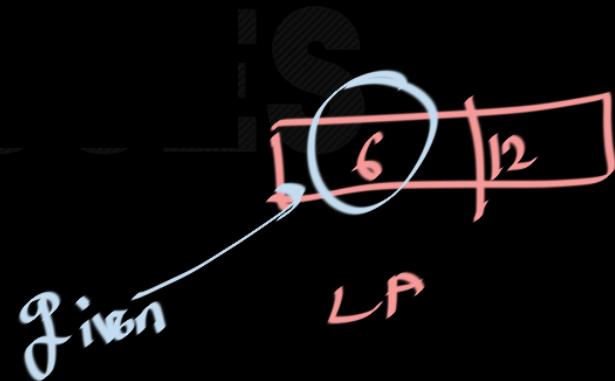
- Physical address = 128MB  $\rightarrow 2^{27}$  B
- Page size = 4 KB  $\rightarrow 2^{12}$  B
- Number of pages =  $2^6$



Calculate:

- Number of frames  $\rightarrow 2^{15}$
- Page offset bits  $\rightarrow 12$
- Logical address space size  $\rightarrow 2^{18}$
- If each page table entry size is  $2^p$  Bytes then size of page table

$$2^6 \times 2^p = 2^{18}$$





## Question

- Logical address = 1GB
- Page size = 4 MB
- Number of frames =  $2^{10}$

Calculate:

- Physical address space size
- Page offset bits
- Logical address space size
- If each page table entry size is  $2^p$  Bytes then size of page table



## Question

- Logical address = 1TB
- Number of pages =  $2^{12}$
- Number of frames =  $2^{10}$

Calculate:

- Physical address space size
- Page offset bits
- If each page table entry size is  $2^p$  Bytes then size of page table



# Question

## Problem 5

Consider the following page table used for address translations:  
(for this portion of the question, all numbers are expressed using base 10).

Virtual page	Frame
0	3
1	10
2	9
3	2
4	0

$2^{10} \text{ B}$

Assume the page size is 1024 bytes, convert the following virtual addresses into physical addresses  
(show your calculations):

- a. (2 marks) 697
- b. (2 marks) 1054
- c. (2 marks) 1024
- d. (4 marks) If possible, convert the **physical address** 2075 into a **virtual address**. If not explain why it can not be done.



Virtual page	Frame
0	3
1	10
2	9
3	2
4	0

Page size = 1024

$$697 = \text{Page No.} \times 1024 + \text{Page offset}$$

Page No. = 0

frame no. = 3

PA =  $3 \times 1024 + 697 = 3769$

- d. (4 marks) If possible, convert the physical address 2075 into a virtual address. If not explain why it can not be done.

Virtual page	Frame
0	3
1	10
2	9
3	2
4	0

frame size =  $1024$

$$2075_{PA} = \underbrace{3}_{\text{frame no.}} + \underbrace{27}_{\text{offset}}$$

Logical address

$$3 \times 1024 + 27 \\ = 3099$$

SrK to Everyone 8:38 PM

so most people don't know the connection between binary and decimal in addresses

AIMT-1

Virtual page numbers and physical frame numbers are represented in decimal. Consider a machine with 32-bit virtual addresses and a page size of 512 bytes. During a program execution, the TLB contains the following valid entries (all in decimal).

Virtual Page Num	Physical Frame Num
591	100
5912	200
11548	300
2589	400
59125	500

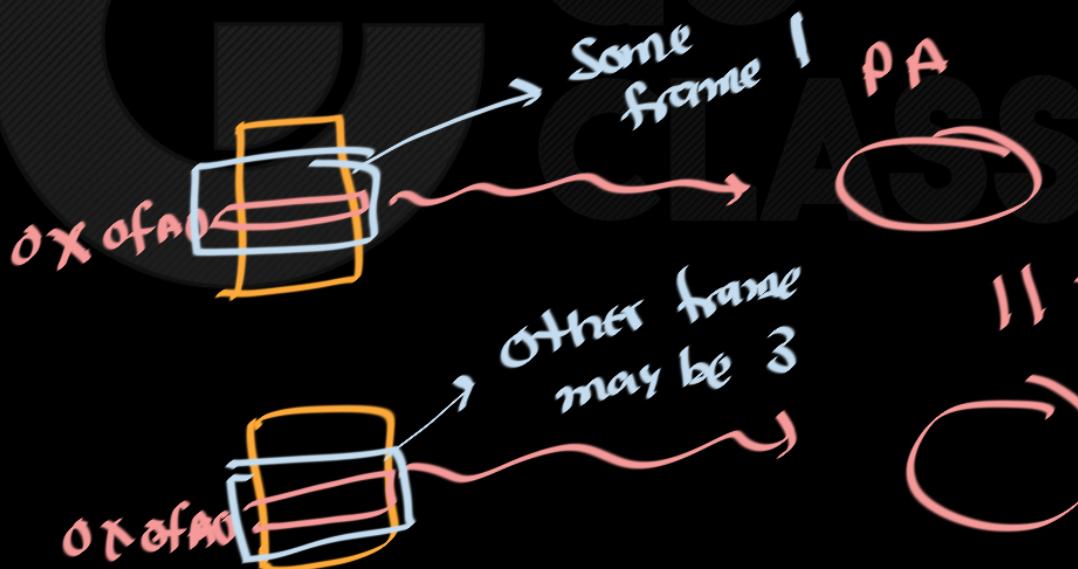
Translate the virtual address = 5912589 (in decimal) into a physical address (in decimal).

Suppose there are multiple processes in Paging system. Suppose no process is sharing any data with other process. Is it possible for same VA of two different processes we get same frame No?

Suppose there are multiple processes in Paging system. Suppose no process is sharing any data with other process.

is it possible for same VA of two different

processes we get same frame No?



Can these  
too be same?

Suppose there are multiple processes in paging system. Suppose no process is sharing any data with other process.

is it possible for same VA of two different processes we get same frame No?



even though LA is same still PA is different because pages will be mapped to different frames.

a. (2 marks) 697

begin solution

$697 = 0 * 1024 + 697$  and so vpn = 3.

The real Address is  $3 * 1024 + 697 = 3769$

b. (2 marks) 1054

begin solution

$1054 = 1024 + 30$  and so is on page 1.

The real Address is  $10 * 1024 + 30 = 10270$ ;

end solution

c. (2 marks) 1024

begin solution

page 0 is virtual address 0 .. 1023.

So  $1024 =$  is vpn = 1 offset = 0

The real Address is  $10 * 1024 = 10240$

end solution

d. (4 marks) If possible, convert the physical address 2075 into a virtual address. If not explain why it can not be done.

begin solution

2075 is on physical frame 2 with offset 27.

From the page table we see that frame 2 is virtual page 3.

Virtual page 3 is  $3 * 1024 = 3072 +$  the offset (27)

So the virtual address = 3099.

end solution

  
ASSES



## Question

Consider a single-level paging system with 12-bit virtual addresses, 24-bit physical addresses, and a 256 ( $2^8$ ) byte page size.

What is the maximum number of entries in a page table in this system?





## Solution

Consider a single-level paging system with 12-bit virtual addresses, 24-bit physical addresses, and a 256 ( $2^8$ ) byte page size.

What is the maximum number of entries in a page table in this system?

$$2^4 = 16$$



## Question

Consider a virtual memory system that uses paging. Virtual and physical addresses are both 32 bits long, and the page size is  $4\text{KB} = 2^{12}$  bytes. A process  $P_1$  has the following page table. Frame numbers are given in notation (recall that each hexadecimal digit represents 4 bits).

	Frame Number
0	0x00788
1	0x00249
2	0x0023f
3	0x00ace
4	0x00bcd

For each of the following virtual addresses, indicate the physical address to which it maps. If the virtual address is not part of the address space of  $P_1$ , write NO TRANSLATION instead. Use hexadecimal notation for the physical addresses.

- 0x00001a60
- 0x000051ff
- 0x00000fb5

<https://student.cs.uwaterloo.ca/~cs350/common/old-exams/W11-midterm-sol.pdf>



## Question

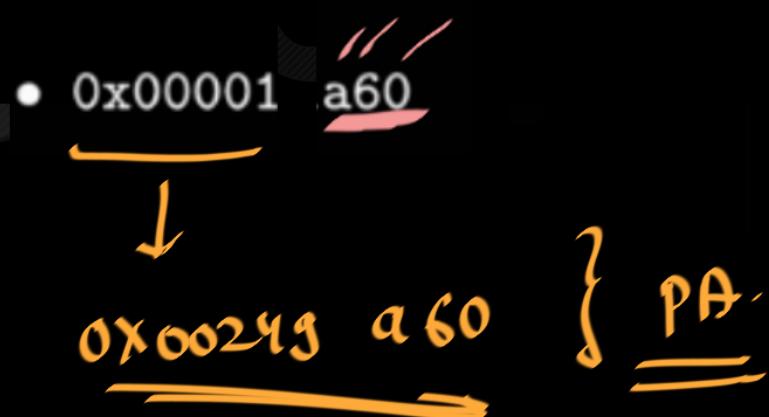
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- 0x00001a60
- 0x000051ff
- 0x00000fb5
- 0x000051ff





# Operating Systems

- 0x00001a60

0x00249a60

- 0x000051ff

NO TRANSLATION

- 0x00000fb5

0x00788fb5

iO  
CLASSES



# Question

Consider a virtual memory system that uses paging. Virtual and physical addresses are both 32 bits long, and the page size is  $4\text{KB} = 2^{12}$  bytes. A process  $P_1$  has the following page table. Frame numbers are given in notation (recall that each hexadecimal digit represents 4 bits).

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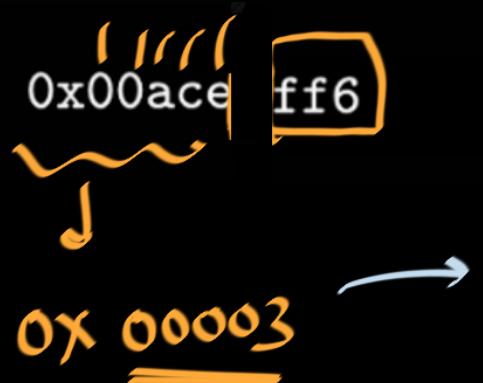
For each of the following physical addresses, indicate the virtual address that maps to it. If the physical address is not part of the physical memory assigned to  $P_1$ , write NO TRANSLATION instead. Use hexadecimal notation for the virtual addresses.

- 0x00aceff6
- 0x00249000
- 0x00000887

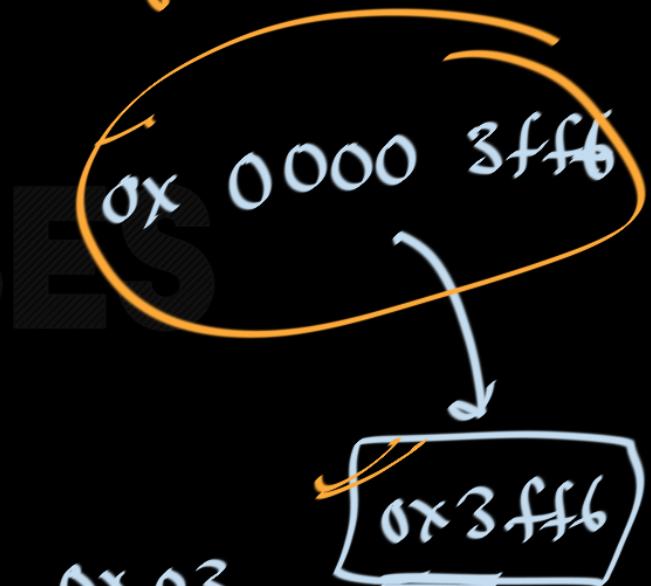
	Frame Number
0	0x00788
1	0x00249
2	0x0023f
3	0x00ace
4	0x00bcd

For each of the following physical addresses, indicate the virtual address that maps to it. If a physical address is not part of the physical memory assigned to  $P_1$ , write NO TRANSLATION. Use hexadecimal notation for the virtual addresses.

- 0x00aceff6
- 0x00249000
- 0x00000887



Given that VA is 32 bits





# Operating Systems

- 0x00aceff6

0x00003ff6

- 0x00249000

0x00001000

- 0x00000887

NO TRANSLATION



# Question

4.

Virtual Address	Physical Address
0x0008 0AF0	0x0010 1AF0
0x0000 2224	0x0008 3224
0x0007 3234	0x0018 3234

Suppose that while a process  $P$  is running, it uses the virtual addresses shown in the left column of the table above. For each of these virtual addresses, the corresponding physical address (after address translation) is also shown in the table. On the machine on which  $P$  is running, both virtual addresses and physical addresses are 32 bits long.

a. (3 marks)

Is it possible that the MMU used *paging*, with a page size of 64 KB ( $2^{16}$  bytes), to translate  $P$ 's virtual addresses to physical addresses? If so, write "YES". If not, write "NO" and explain, briefly and clearly, how you know that paging with this page size was not used.

b. (2 marks)

Is it possible that the MMU used paging, with a page size of 4 KB ( $2^{12}$  bytes), to translate  $P$ 's virtual addresses to physical addresses? If so, write "YES". If not, write "NO" and explain, briefly and clearly, how you know that paging with this page size was not used.



# Operating Systems

a. (3 marks)

Is it possible that the MMU used *paging*, with a page size of 64 KB ( $2^{16}$  bytes), to translate  $P$ 's virtual addresses to physical addresses? If so, write “YES”. If not, write “NO” and explain, briefly and clearly, how you know that paging with this page size was not used.

NO. If page sizes of 64 KB are used, virtual and physical addresses should have the same 16-bit offset. However, 0x0000 2224 and 0x0008 3224 have different offsets.

b. (2 marks)

Is it possible that the MMU used paging, with a page size of 4 KB ( $2^{12}$  bytes), to translate  $P$ 's virtual addresses to physical addresses? If so, write “YES”. If not, write “NO” and explain, briefly and clearly, how you know that paging with this page size was not used.

YES.



# Question

14. A system uses paging to implement virtual memory. Specifically, it uses a simple linear page table. The virtual address space is of size 1 GB (30 bits); the page size is 1 KB; each page table entry holds only a valid bit and the resulting page-frame number (PFN); the system has a maximum of  $2^{15}$  physical pages (32 MB of physical memory can be addressed at most).

How much memory is used for page tables, when there are 100 processes running in the system?

<https://pages.cs.wisc.edu/~remzi/Classes/537/Fall2013/OldExams/11-fall-mid.pdf>



## Question

14. A system uses paging to implement virtual memory. Specifically, it uses a simple linear page table. The virtual address space is of size 1 GB (30 bits); the page size is 1 KB; each page table entry holds only a valid bit and the resulting page-frame number (PFN); the system has a maximum of  $2^{15}$  physical pages (32 MB of physical memory can be addressed at most).

How much memory is used for page tables, when there are 100 processes running in the system?

$$\text{page bits} = 2^{10}$$

$$VA =$$



$$PTE =$$

frame no  
valid bit



PA:



$2^{20} \times (15+1)$

=

$$2^{20} \times 2^3$$

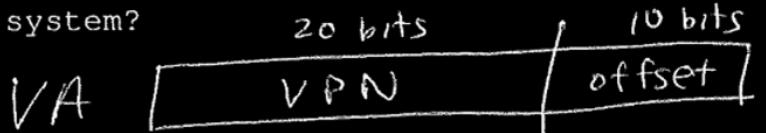
size of page table



# Operating Systems

14. A system uses paging to implement virtual memory. Specifically, it uses a simple linear page table. The virtual address space is of size 1 GB (30 bits); the page size is 1 KB; each page table entry holds only a valid bit and the resulting page-frame number (PFN); the system has a maximum of  $2^{15}$  physical pages (32 MB of physical memory can be addressed at most).

How much memory is used for page tables, when there are 100 processes running in the system?



$$\frac{2^{20} \times 2B}{\underline{\underline{\leq 2MB}}} \times 100 = \underline{\underline{200MB}}$$

# GATE CSE 2001 | Question: 2.21

asked in Operating System Sep 15, 2014 • edited Jun 23, 2018 by Pooja Khatri

47,556 views



40

Consider a machine with 64 MB physical memory and a 32-bit virtual address space. If the page size is 4 KB, what is the approximate size of the page table?



- A. 16 MB
- B. 8 MB
- C. 2 MB
- D. 24 MB

H.W.

<https://gateoverflow.in/739/gate-cse-2001-question-2-21>



Number of pages =  $2^{32}/4KB = 2^{20}$  as we need to map every possible virtual address.

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So, we need  $2^{20}$  entries in the page table. Physical memory being  $64 MB$ , a physical address must be 26 bits and a page (of size  $4KB$ ) address needs  $26 - 12 = 14$  address bits. So, each page table entry must be at least 14 bits.



So, total size of page table =  $2^{20} \times 14 \text{ bits} \approx 2 MB$  (assuming PTE is 2 bytes)

Best answer

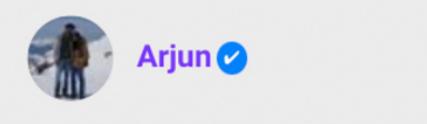
Correct Answer: C

answered Jan 2, 2015 • edited May 22, 2019 by Naveen Kumar 3

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## GATE CSE 2016 Set 1 | Question: 47

asked in Operating System Feb 12, 2016 • edited Feb 17, 2016 by makhdoom ghaya

9,802 views

27  
27  
27

Consider a computer system with 40-bit virtual addressing and page size of sixteen kilobytes. If the computer system has a one-level page table per process and each page table entry requires 48 bits, then the size of the per-process page table is \_\_\_\_\_ megabytes.

gatecse-2016-set1 operating-system virtual-memory easy numerical-answers

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<https://gateoverflow.in/39690/gate-cse-2016-set-1-question-47>



No. of pages( $N$ ) =  $2^{26}$  = No. of entries in Page Table

47

Page Table Entry Size( $E$ ) = 6 bytes



So, Page Table Size =  $n \times e = 2^{26} \times 6$  bytes = 384 MB



answered Feb 19, 2016 • edited Jun 26, 2018 by Arjun

Best answer

edit flag hide comment Follow Pip Box

Delete with Reason Wrong Useful

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## GATE CSE 2015 Set 2 | Question: 47

asked in Operating System Feb 13, 2015 • edited Jun 19, 2021 by Lakshman Patel RJIT

13,805 views

- 56 A computer system implements 8 kilobyte pages and a 32-bit physical address space. Each page table entry contains a valid bit, a dirty bit, three permission bits, and the translation. If the maximum size of the page table of a process is 24 megabytes, the length of the virtual address supported by the system is \_\_\_\_\_ bits.



gatecse-2015-set2

operating-system

virtual-memory

normal

numerical-answers

<https://gateoverflow.in/8247/gate-cse-2015-set-2-question-47>



# Operating Systems



8 KB pages means 13 offset bits.



We also have 1 valid bit, 1 dirty bit and 3 permission bits.



So, total size of a PTE (Page Table Entry) =  $19 + 5 = 24$  bits = 3 bytes.

Best answer

Given in question, maximum page table size = 24 MB

Page table size = No. of PTEs × size of an entry

So, no. of PTEs =  $24 \text{ MB} / 3 \text{ B} = 8 \text{ M}$

Virtual address supported = No. of PTEs \* Page size (As we need a PTE for each page and assuming single-level paging)

=  $8 \text{ M} * 8 \text{ KB}$

=  $64 \text{ GB} = 2^{36}$  Bytes

So, length of virtual address supported = 36 bits (assuming byte addressing)

answered Feb 14, 2015 • edited Jun 19, 2021 by Lakshman Patel RJIT

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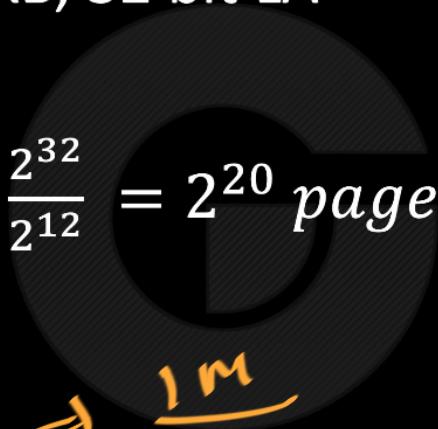
Arjun



A Typical System has:

Page size = 4 KB, 32 bit LA

$$\text{So, } \frac{2^{32}}{4 \text{ KB}} = \frac{2^{32}}{2^{12}} = 2^{20} \text{ pages}$$



There will be  $2^{20}$  page table entries,  
if each page table entry is 4B then page ~~table~~ size = 4MB

- One such page table for each process.

Size of page table

for 1 process



example :

```
main() {  
    int a, *p;  
    a = 5;  
    p = malloc(4);  
    *p = a;  
}
```

in it's entire life  
this process might  
be using

10B + 1B + 20B  
Code      static      Stack

4B  
heap

$\approx 35B$

Page size = 4 KB, 32 bit LA

$$\text{So, } \frac{2^{32}}{4 \text{ KB}} = \frac{2^{32}}{2^{12}} = 2^{20} \text{ pages}$$

Page table size = 4 MB

Process : 35 B

Satyam Naik to Everyone 9:05 PM

index page is bigger than the entire book.



## A Typical System has:

Page size = 4 KB, 32 bit LA

$$\text{So, } \frac{2^{32}}{4 \text{ KB}} = \frac{2^{32}}{2^{12}} = 2^{20} \text{ pages}$$

### Observation

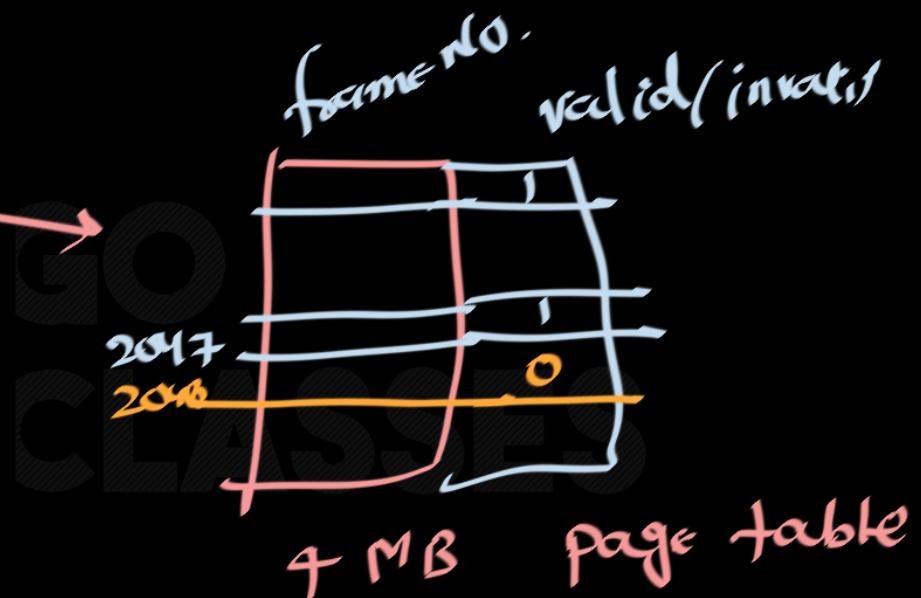
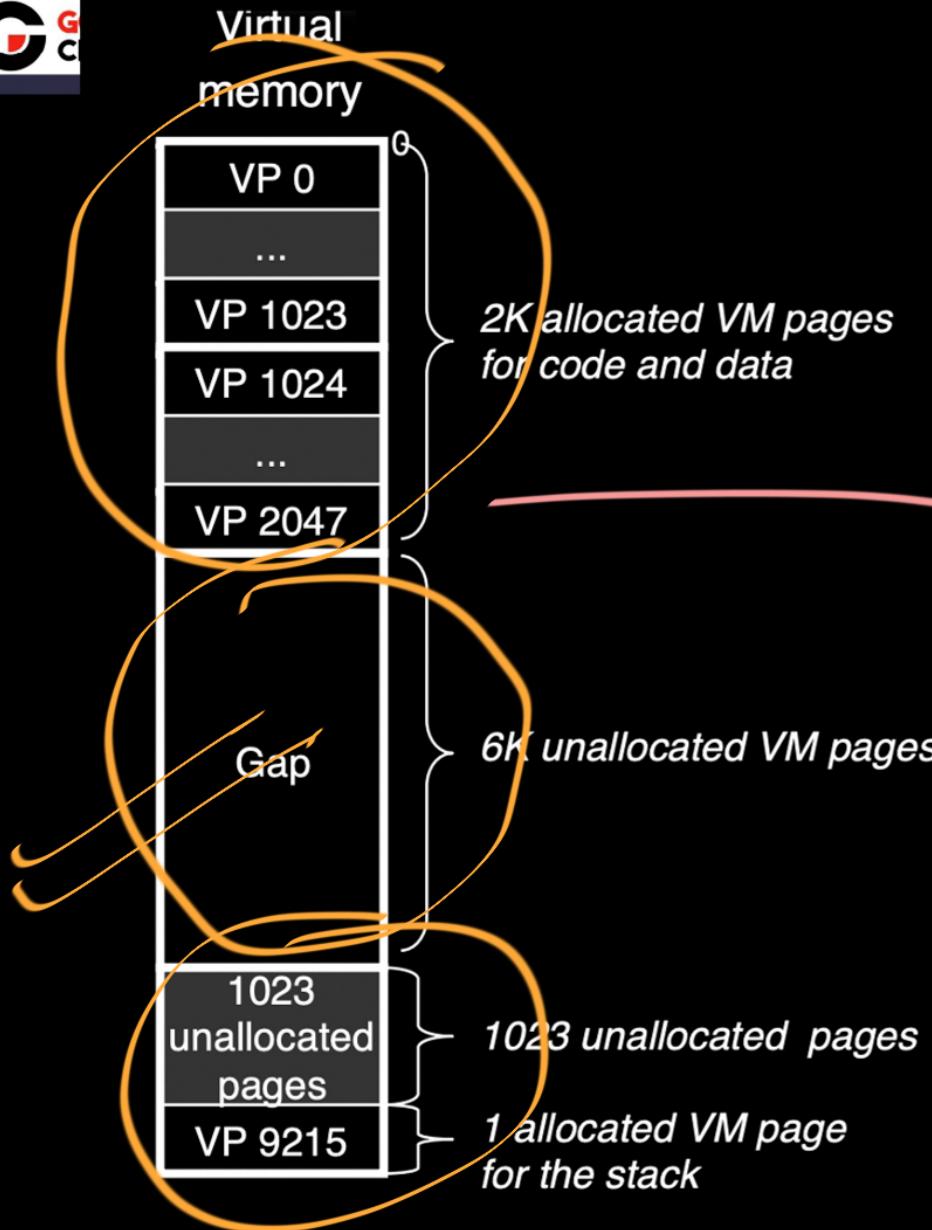
Usually there is lot of unallocated pages for a process in between heap and stack.

Process just usages few pages.

We can not afford this huge page table for every process

There will be  $2^{20}$  page table entries,  
if each page table entry is 4B then page size = 4MB

- One such page table for each process.



35B



Why are page tables so large? ( even though the

process is small)



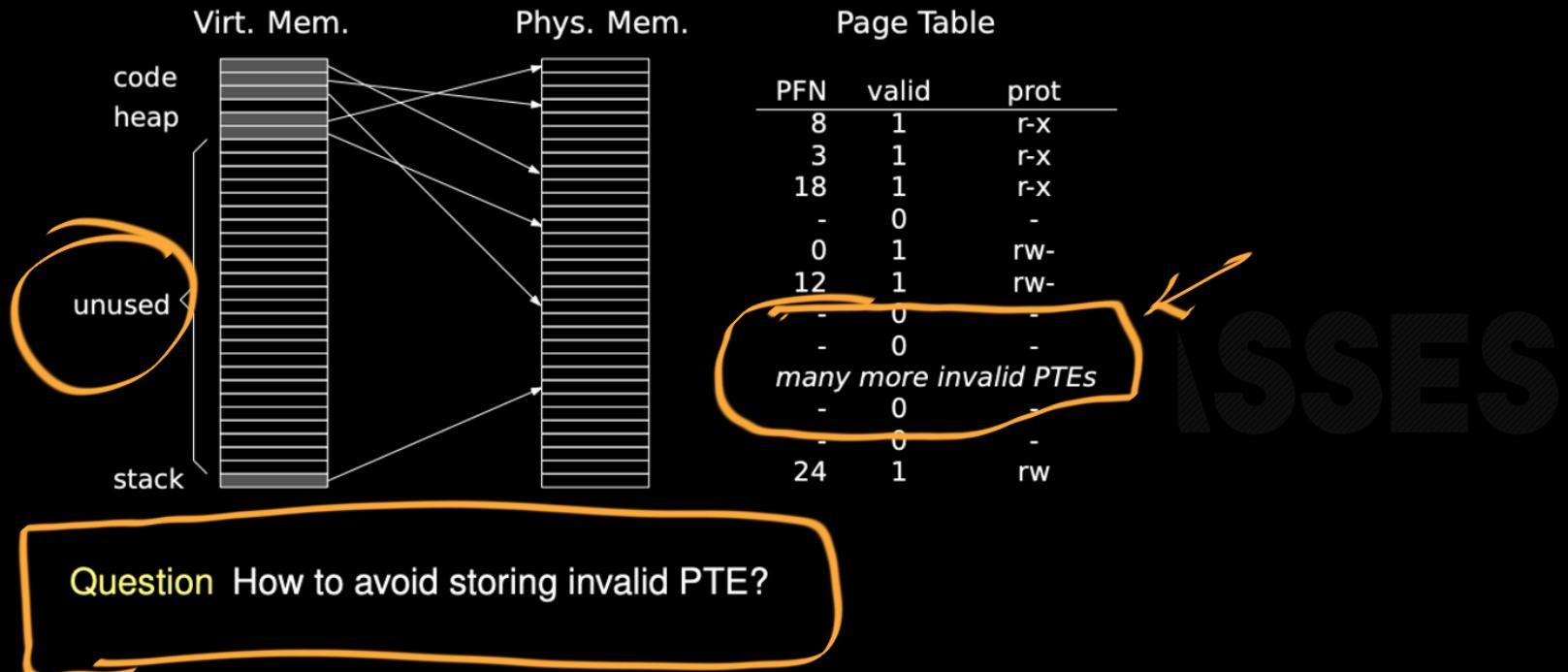
↓  
35B

Because we have too many  
invalid pages to maintain.

Page table = 4MB  
size

## Why are page tables so large?

Answer Page tables are full of invalid PTEs



[https://web.fe.up.pt/~pfs/aulas/so2021/at/24vm\\_tables.pdf](https://web.fe.up.pt/~pfs/aulas/so2021/at/24vm_tables.pdf)



# Operating Systems

now we have a challenge:  
how to reduce the size of page tables?



the smart solution:  
**Multi-Level Page Tables**



# Multi Level Paging



## Why Multi-level paging ?

- We may not find continuous space to store page table in main memory.

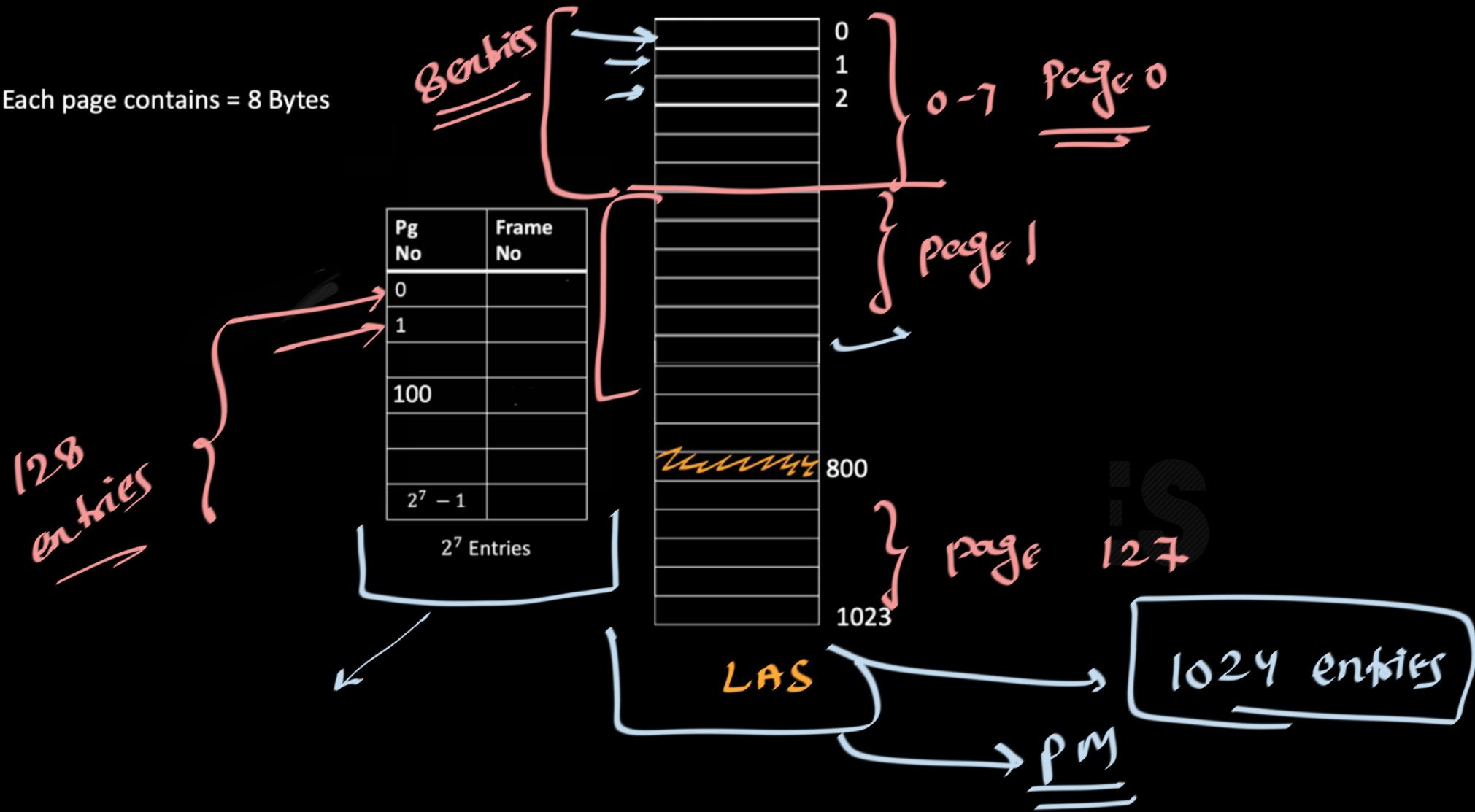
- We have seen most of the pages are empty.

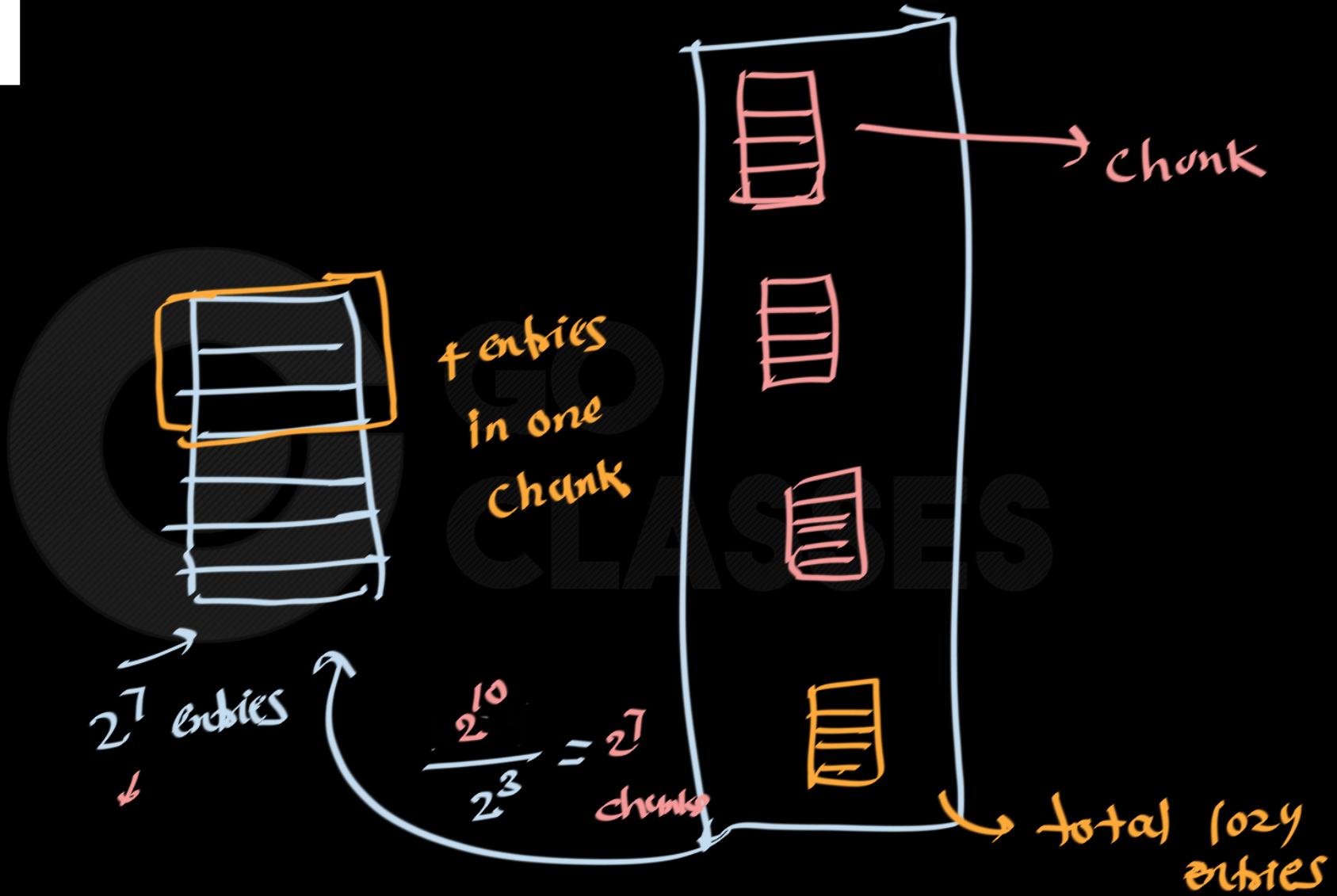
Why to store entries corresponding to the pages which are empty ?

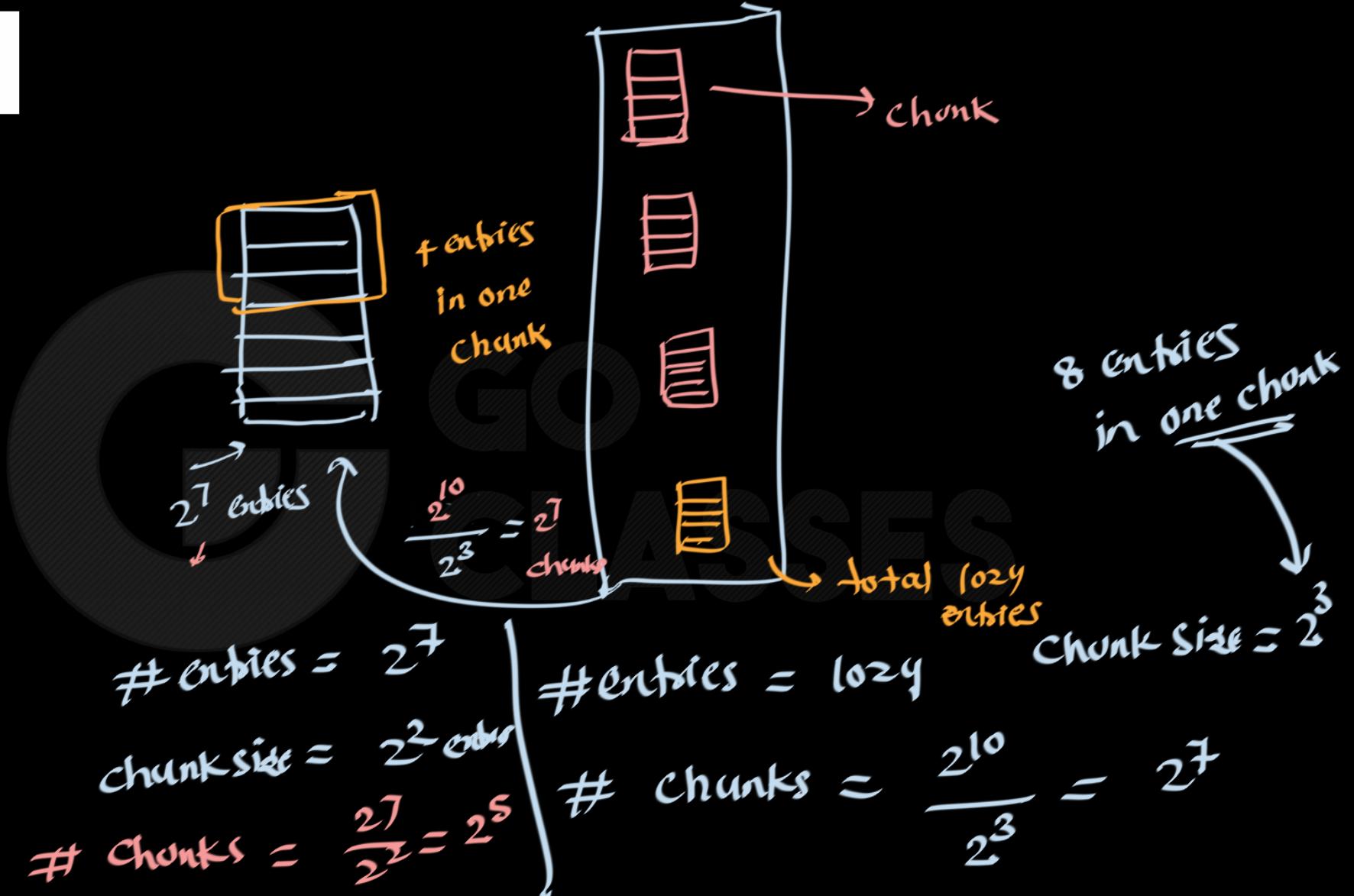


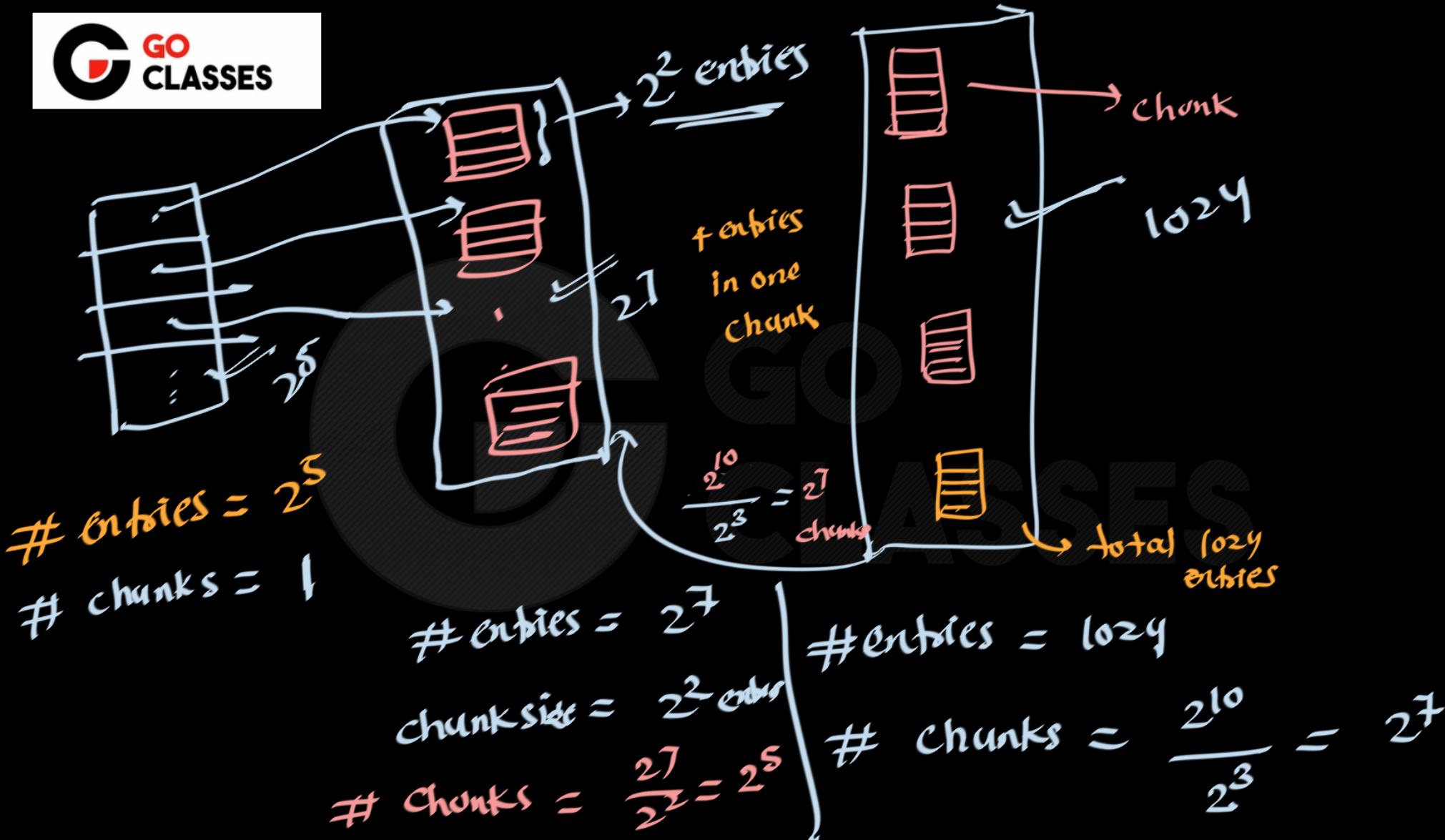
GO  
CLASSES

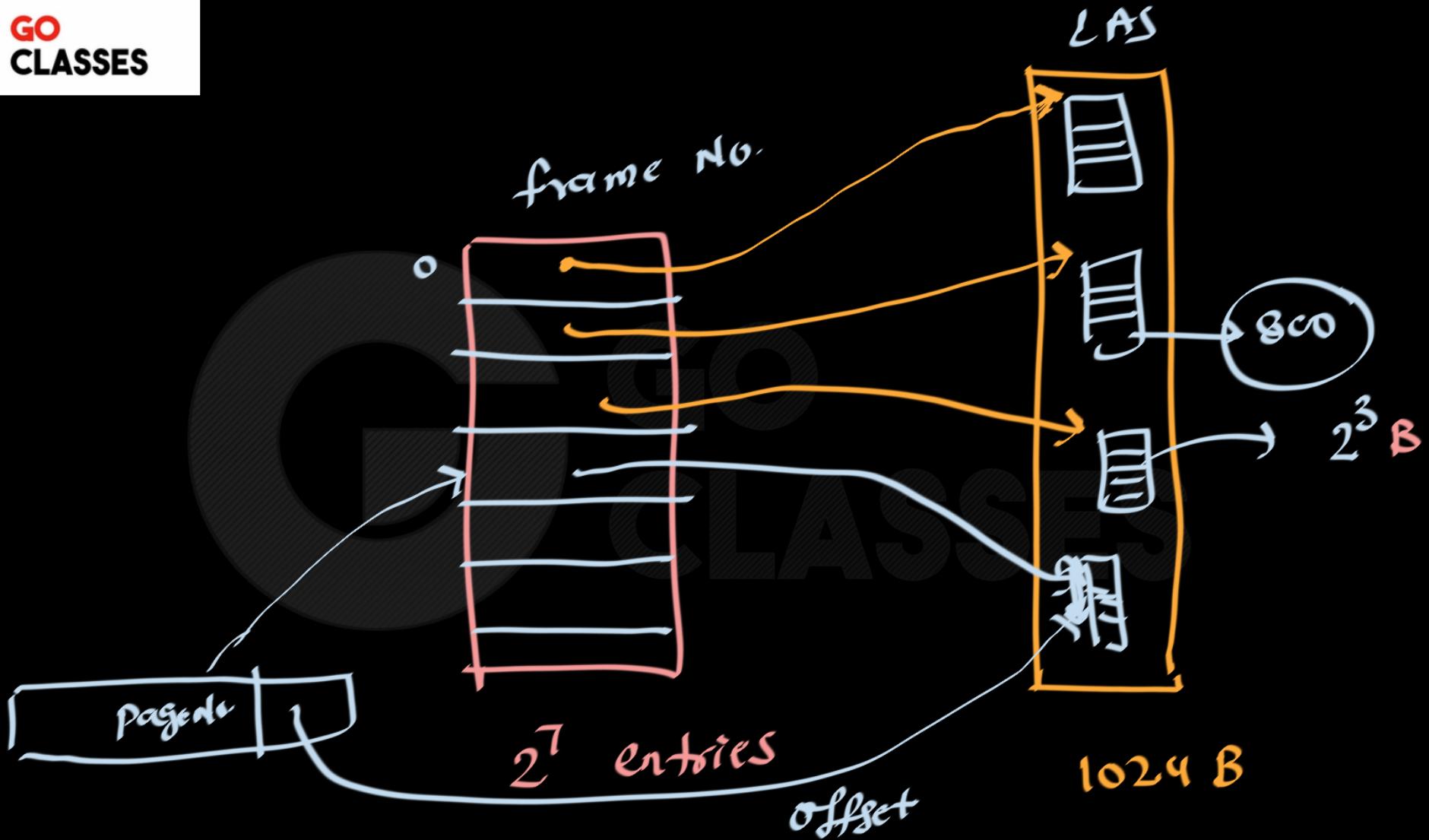
Each page contains = 8 Bytes

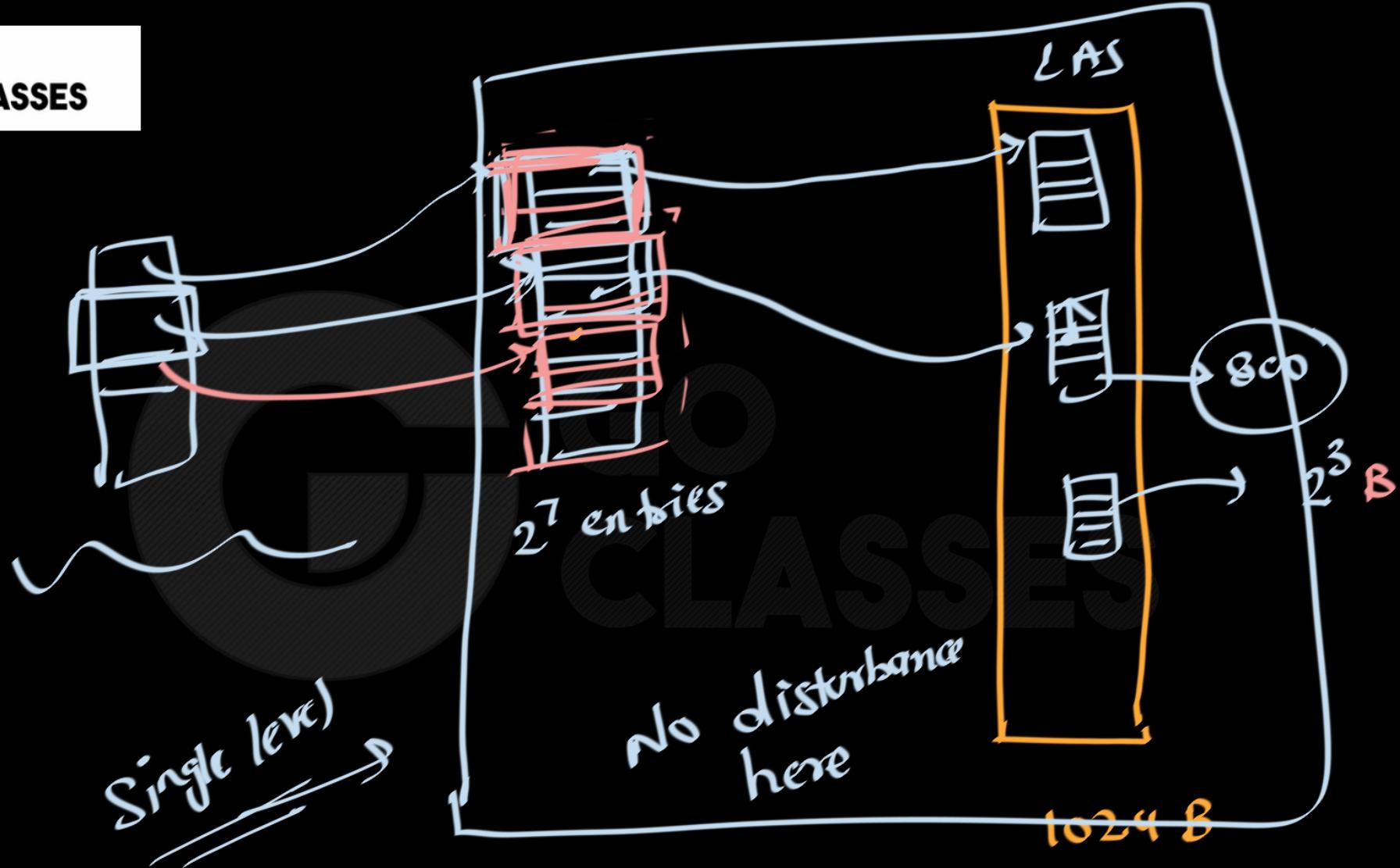














$= 2 \text{ entries}$



$= 2^2 \text{ entries}$

chunk size =  $2^1$

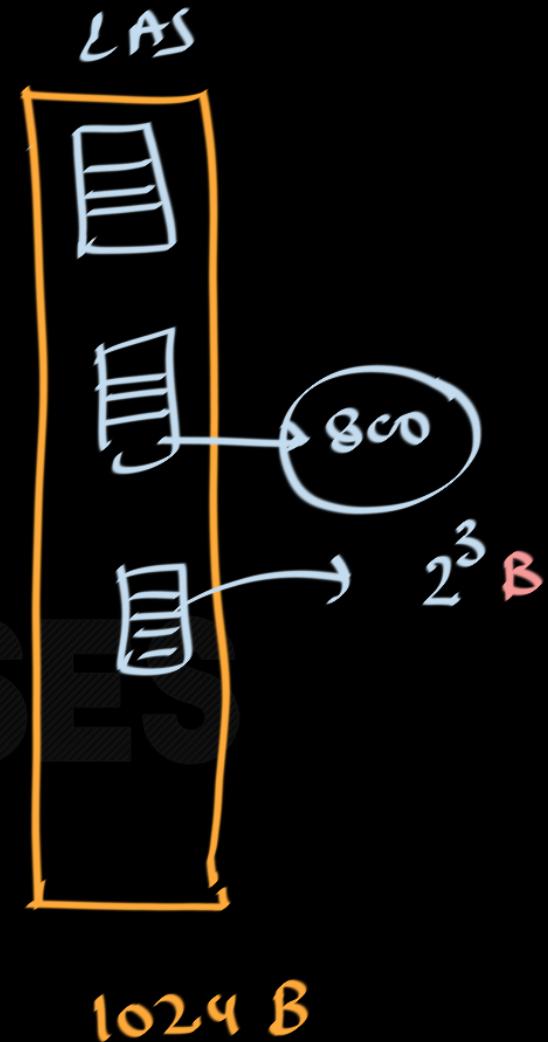
$$\# \text{ chunks} = \frac{2^2}{2^1} = 2$$

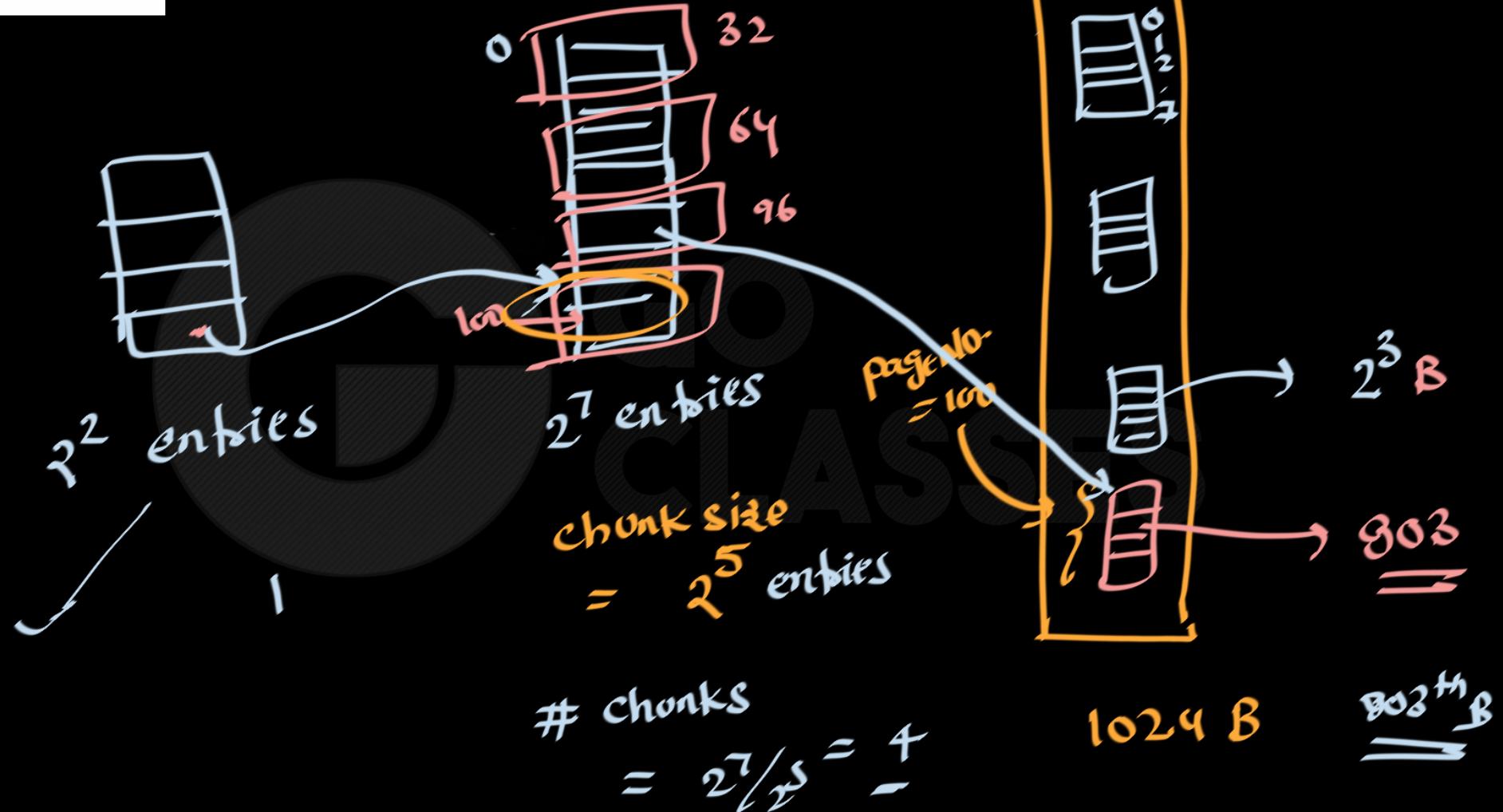


$= 2^3 \text{ entries}$

chunk size  
=  $2^5 \text{ entries}$

$$\# \text{ chunks} = 2^7 / 2^5 = 4$$

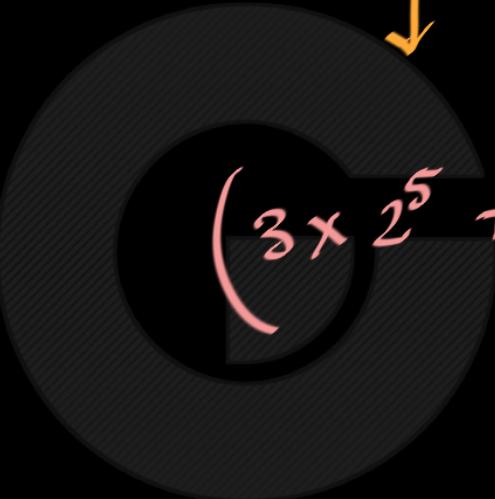




$$g_{03} = \left( 3 \times 2^5 + 4 \right) \times 2^3 + 3$$

$100 \times 2^3 + 3$

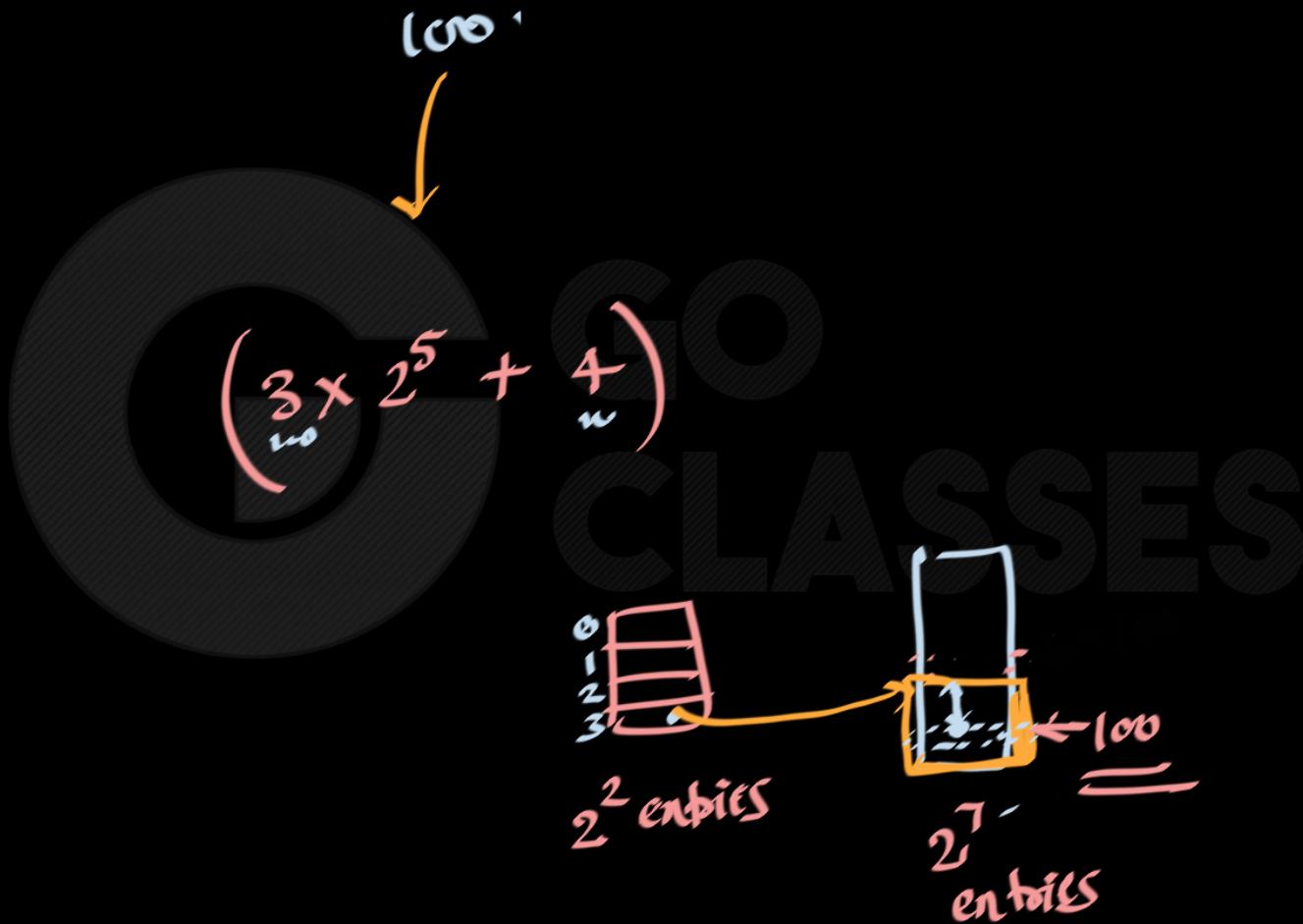
$g_{03} =$



$2^2$  entries

$2^7$  entries

$2^{10}$  entries



$$g_{03} = \left( 3 \times 2^5 + 4 \right) \times 2^3 + 3$$

Diagram illustrating the calculation of  $g_{03}$  through three stages:

- Initial Stage:** A stack of 4 boxes, each containing 3 entries. This is labeled  $3 \times 2^5 + 4$ .
- Intermediate Stage:** The stack is shown multiplied by  $2^3$ , resulting in a stack of 64 boxes, each containing 3 entries. This is labeled  $\left( 3 \times 2^5 + 4 \right) \times 2^3$ .
- Final Stage:** The stack is shown multiplied by  $2^3 + 3$ , resulting in a final stack of 100 boxes, each containing 1 entry. This is labeled  $\left( 3 \times 2^5 + 4 \right) \times 2^3 + 3$ .

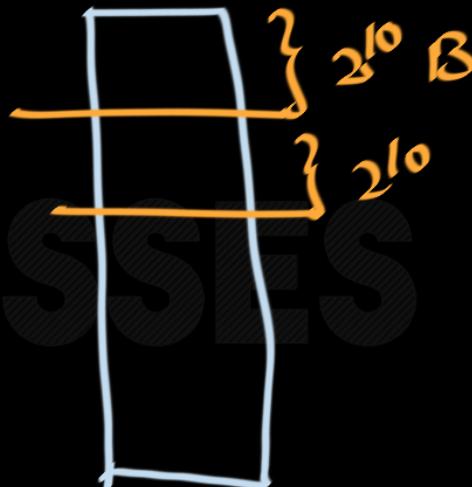
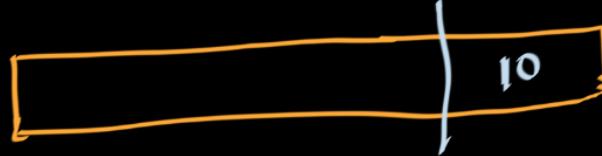
The term  $2^2$  entries is also present near the first stage.

32 bit logical address



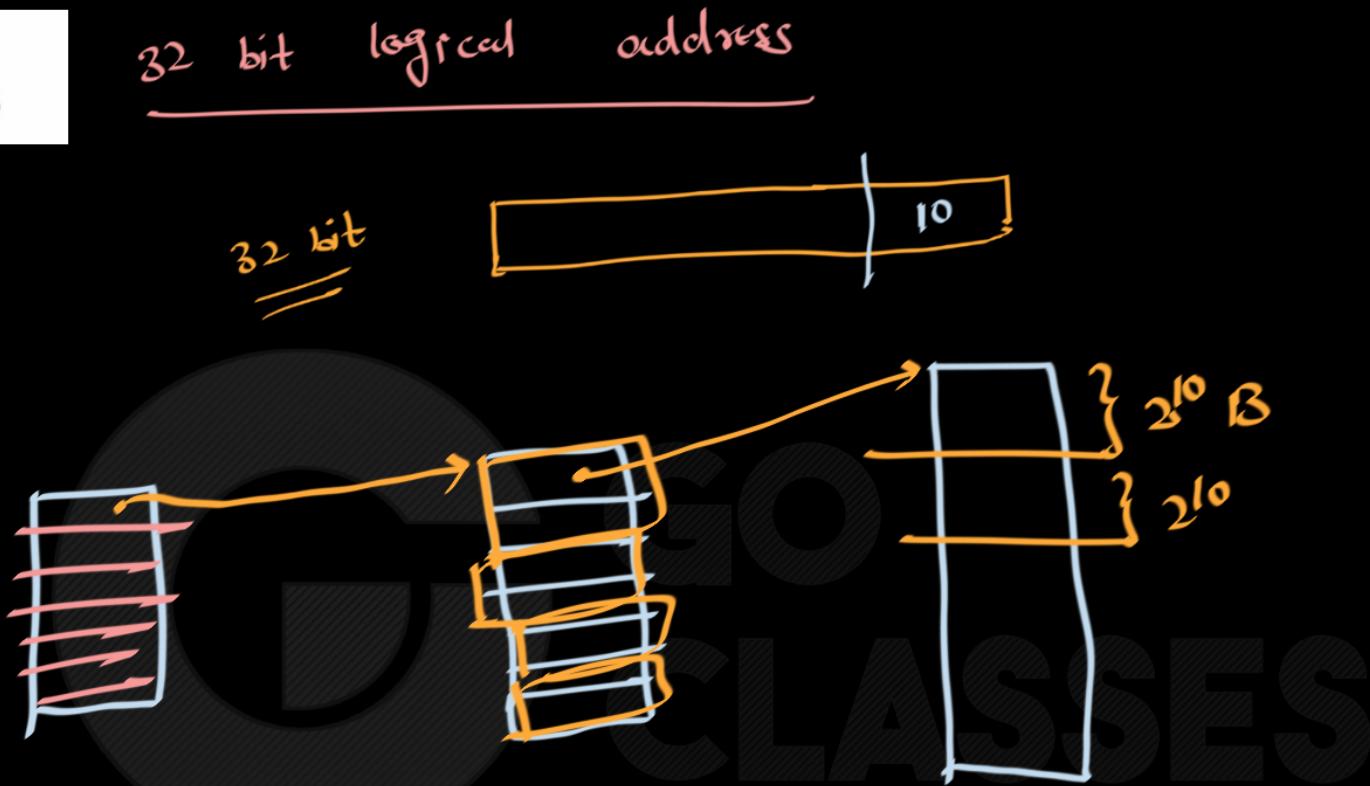
32 bit logical address

32 bit



$$\frac{2^{32}}{2^{10}} = 2^{22} \text{ entries}$$

2<sup>32</sup> B LAS



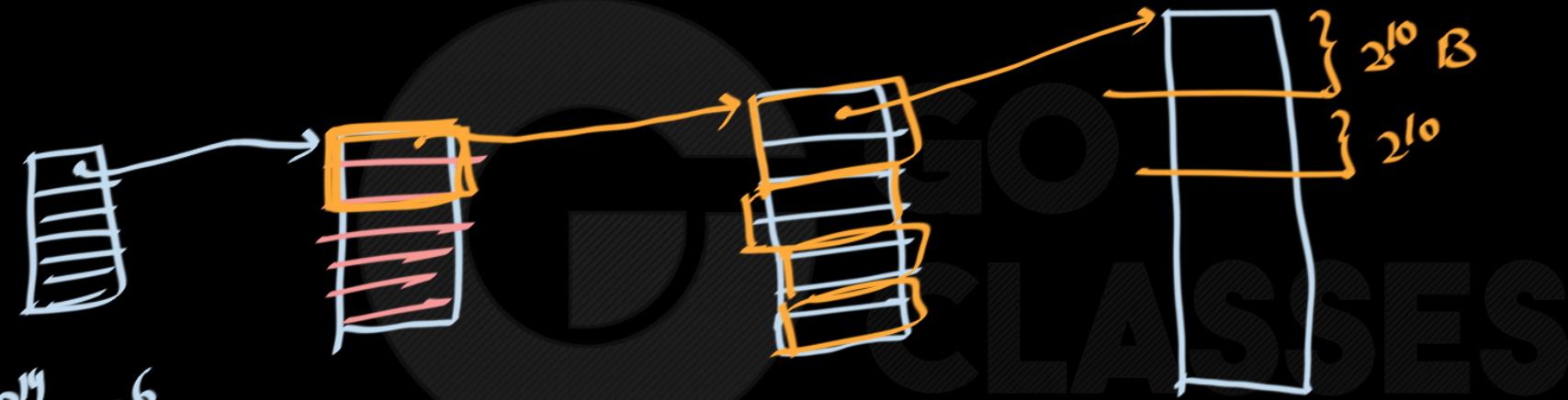
$$\frac{2^{32}}{2^8} = 2^{14} \text{ entries}$$

$$\frac{2^{32}}{2^{10}} = 2^{22} \text{ entries}$$

$2^{32} B$  LAS

chunk size  
 $= 2^8$  entries

32 bit logical address



$$\frac{2^{11}}{2^8} = 2^6$$

$$\frac{2^{22}}{2^8} = 2^{14} \text{ entries}$$

Chunk size =

$2^8$  entries

$$\frac{2^{32}}{2^{10}} = 2^{22} \text{ entries}$$

Chunk size  
=  $2^8$  entries

$2^{32}$  B LAS



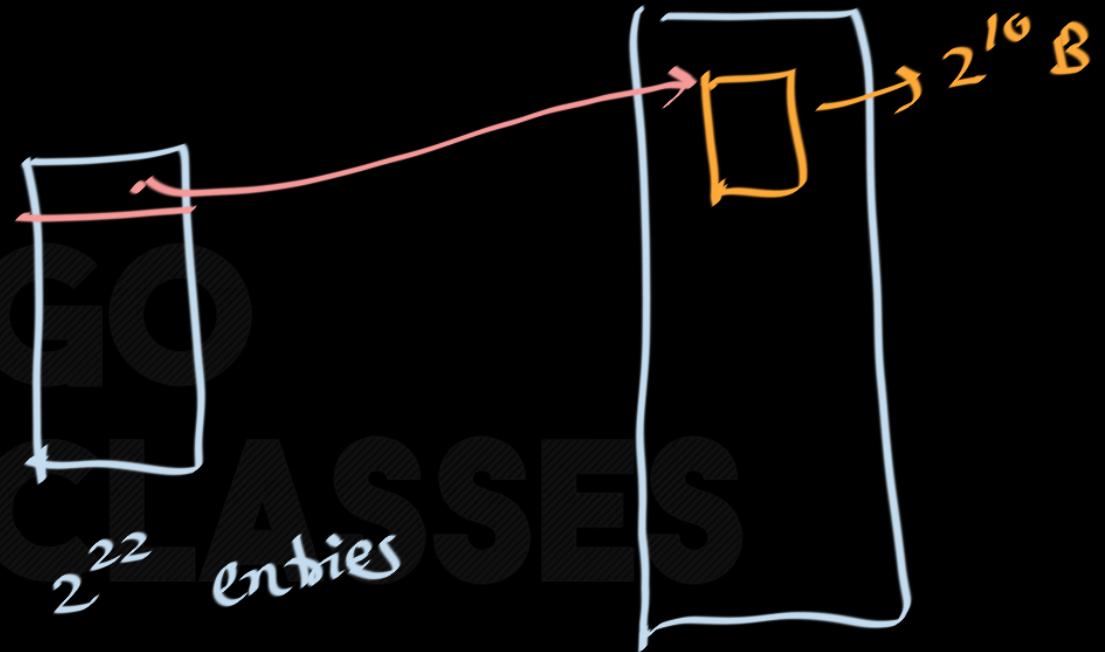
, every entry points to one chunk of  
size  $2^8$ .

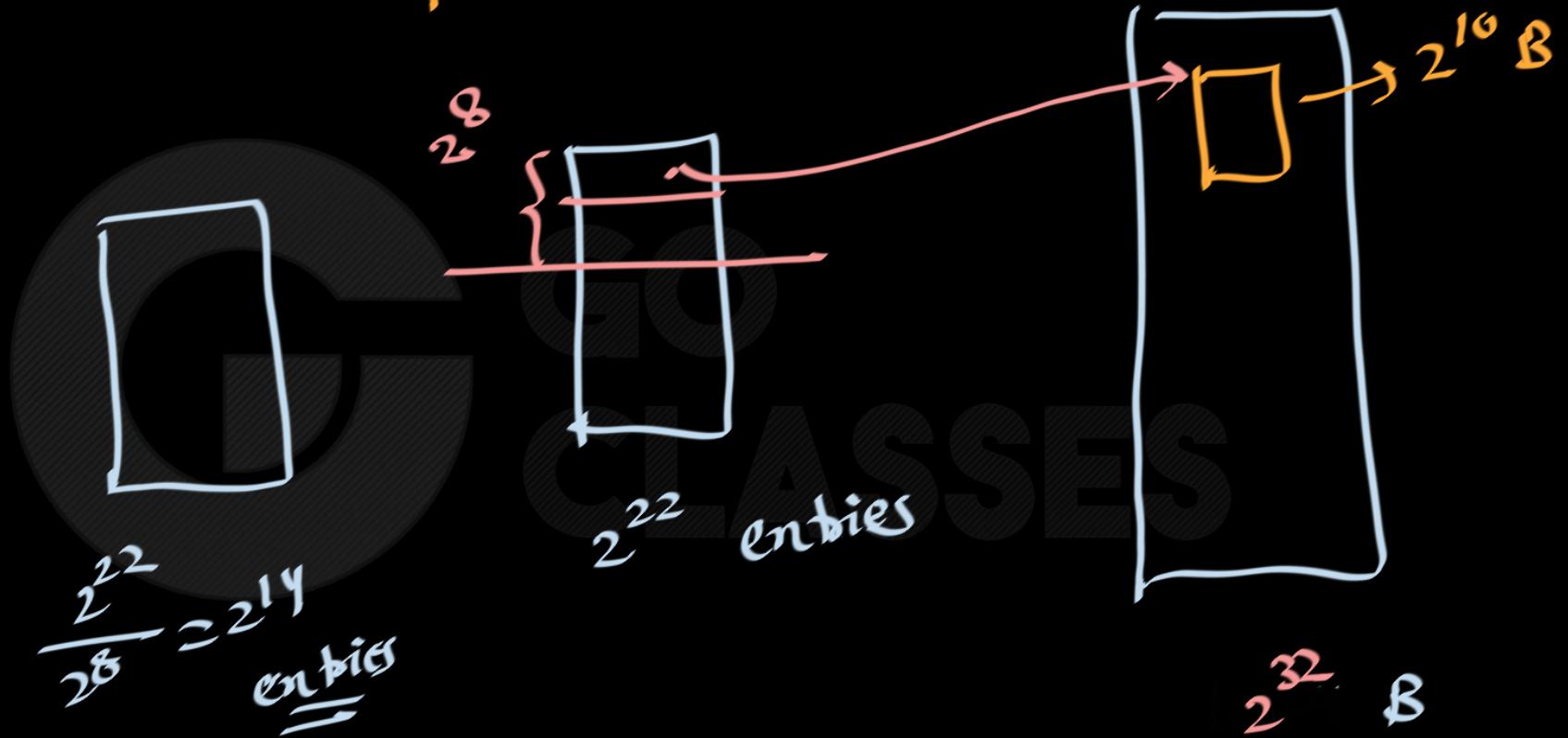
how many # chunks in level 2

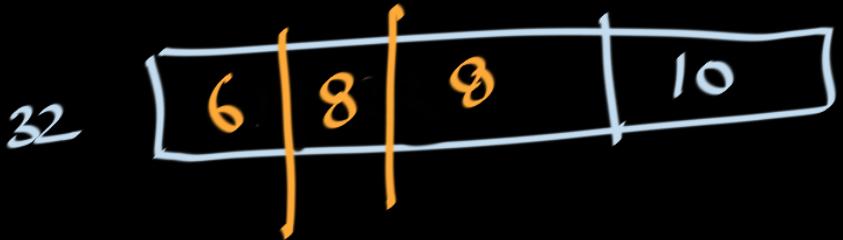
$$2^6 \times 1 = 2^6 \text{ chunks}$$

$$\text{how many } \# \text{ } \underline{\text{entries}} \text{ in level 2} : 2^6 \times 2^8 = 2^{14} \text{ entries}$$

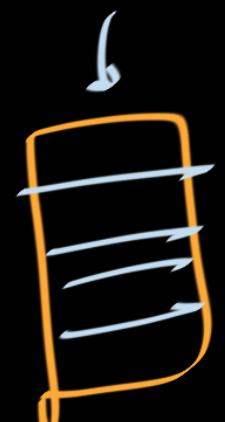
32



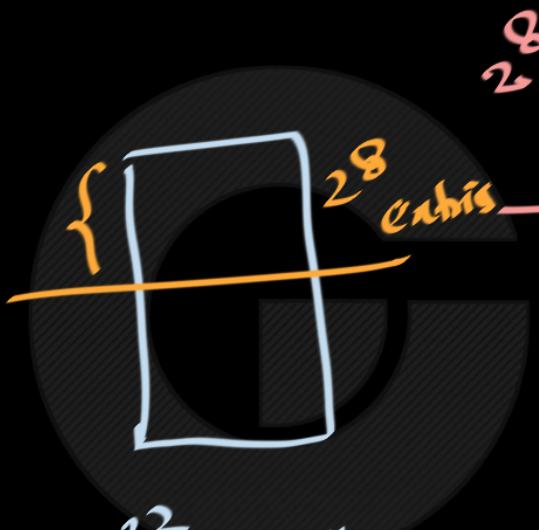




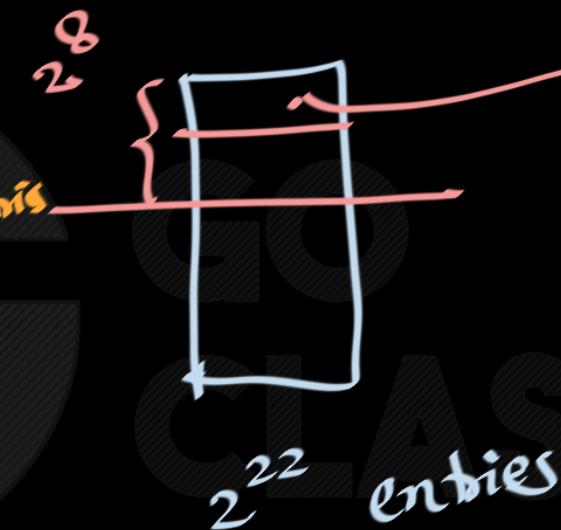
64 entries



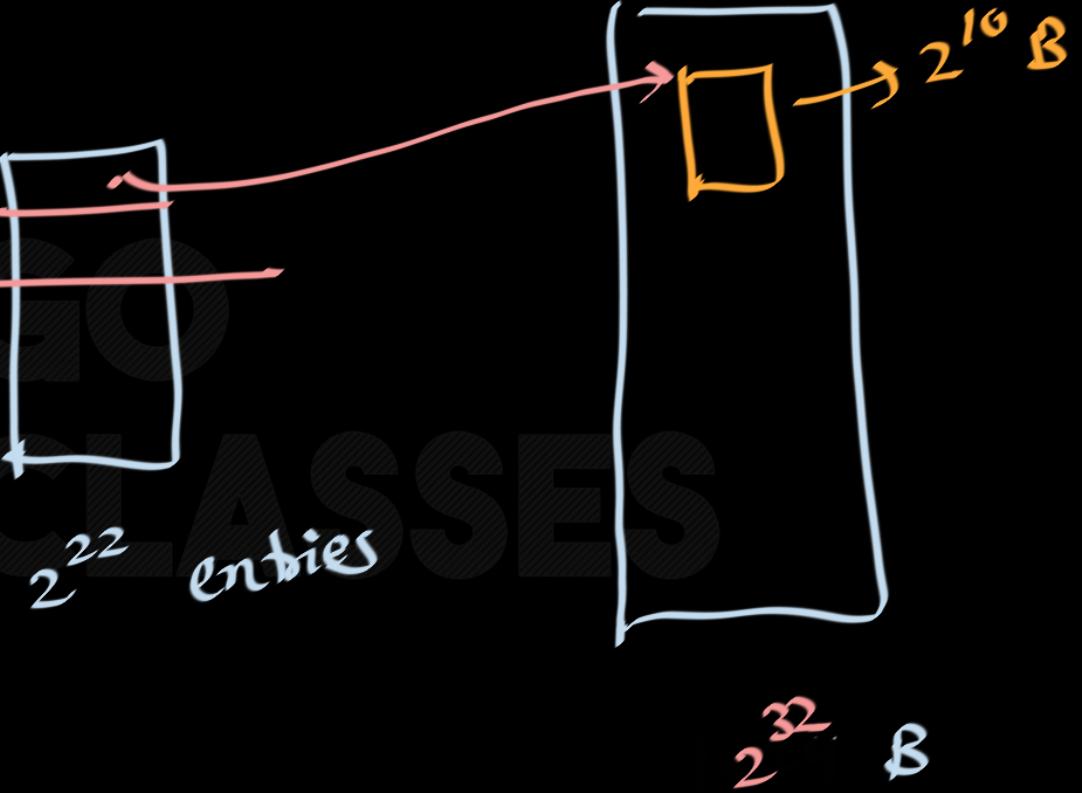
$$\frac{2^{14}}{2^8} = 2^6$$



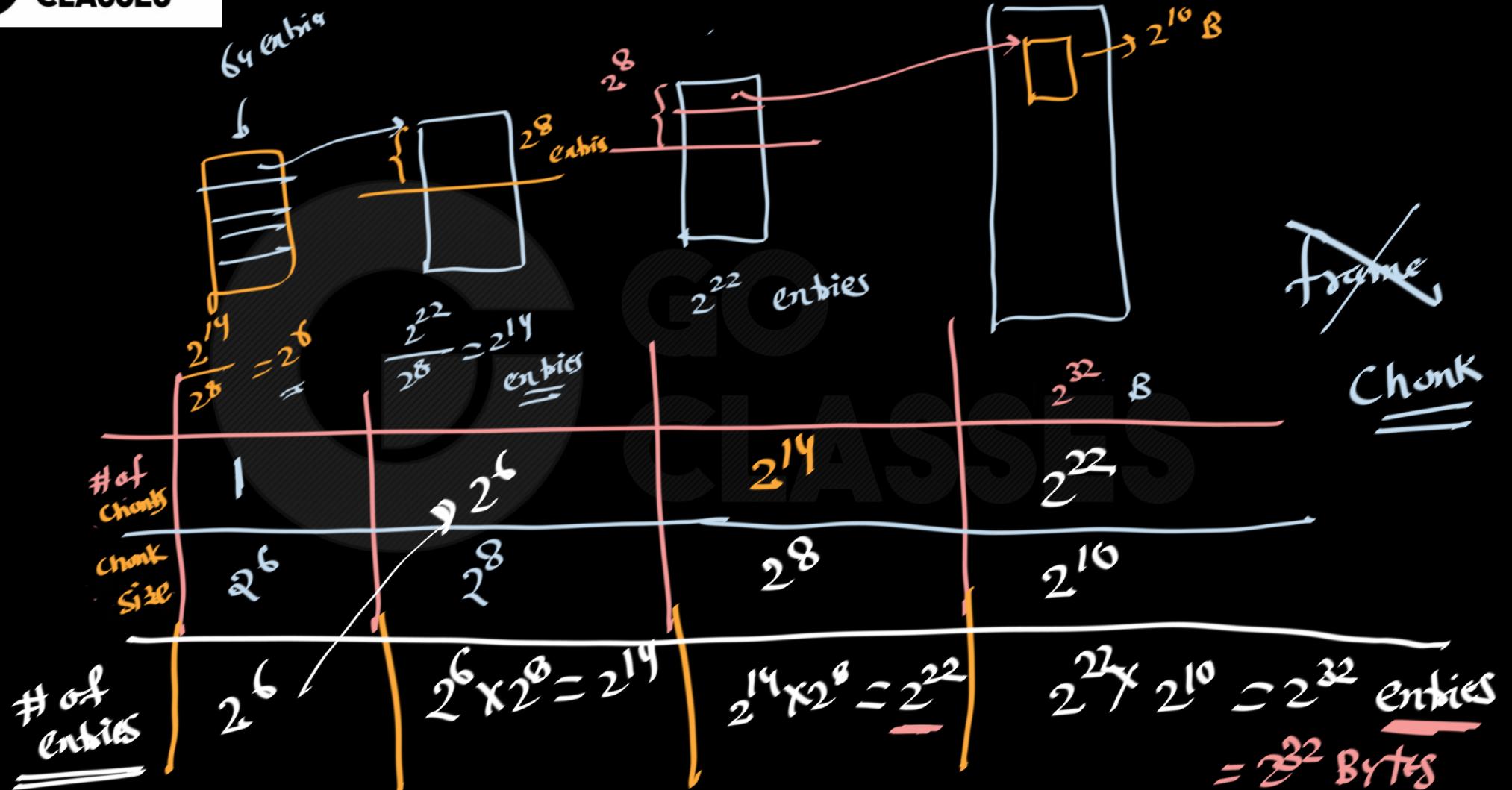
$$\frac{2^{22}}{2^8} = 2^{14}$$



$2^{22}$  entries



$2^{32}$  B





# Operating Systems

6 bits

8 bits

8 bits

10 bits





# Operating Systems

