Aravind Sridhar

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PERSONAL PROFILE

I am currently a computer engineering major graduating in June 2017. I plan to use my knowledge of software, hardware, and electrical engineering in order solve a wide range of problems. I am seeking a full time position after graduation.

EDUCATION

University of California at San Diego - San Diego, California June 2017
Major: Computer Engineering, *Jacobs School of Engineering*
Cumulative GPA: 3.202

PROFESSIONAL EXPERIENCE

UCSD Radiology - Position: **RF Engineering Research Intern** - San Diego June – Present

- Researching coil needs for human body MRI scans at UCSD musculoskeletal imaging group under PHD advisor Won Bae
- Also working with GE senior RF engineer, Eddy Boskamp, learning about designing, modeling, fabricating, and analyzing different RF coils

Quantum Labs - Position: **Intern** – San Jose Aug-Sept 2014

- Developed GUI for a newly acquired laser trimmer that used PASCAL for semiconductor test/trim purposes
- Set up a programmable high power pulsed power supply for future use in a sputtering system.

Nanoshift - Position: **Intern** - Emeryville Aug-Sept 2013

- Undertook research on MEMS, Microfluidics, Nanotechnology, Clean technology, and Process technology, while shadowing laboratory personnel in the UC Berkeley Nano lab
- Compiled report on top companies, emerging technologies, and up-to-date processes in related fields

Academic Connection of Engineers - Position: **Web Developer** – San Diego June -Present

- Website Developer for ACE at UCSD - website: ace.ucsd.edu

Software

Object Oriented Design
- Data structures, memory management

Algorithm Design
- Sorting, searching, pattern matching, graph/network

Agile Methods
- IDEs, version control, team development, testing

Operating Systems
- Kernel structure, concurrency, virtual memory, scheduling, security

Functional Programming
- Ocaml, Scala

Website Development
- ace.ucsd.edu, FTP

Languages
- Java, C, C++, Python, Ocaml, Scala, Python, Verilog, JavaScript, HTML, VHDL
- Eager to learn new languages

Hardware and Electrical

Advanced Digital Design
- CPU architecture
- Design of system, processor, control, memory
- FPGA, ASIC, ISA, pipelining, hazard elimination, synchronous/asynchronous FSM synthesis, large scale design (RLE, SHA1)

Circuit Design
- Basic analog design, first and second order systems, diode and transistor circuits

Linear Electronic Systems
- Frequency response, Laplace transforms, op-amp filter design, transfer functions
- Signals and Systems, impulse response, FFT, transfer functions, stability, convolution, sampling

Power Systems
- Power and energy conversion, 3-phase circuits, transformers, transmission lines

Additional Skills

- Linux environment, PSpice, OrCAD, PSim, ADS, ModelSim, Quartus, HFSS, Android Studio, GIT, Soldering, Oscilloscope, Network Analyzer, Smith Charts, Impedance Matching, MATLAB
- Languages Spoken: English, Tamil, and basic Spanish
- Strong written and verbal communication skills
- Excellent teamwork, multitasking, and prioritizing abilities