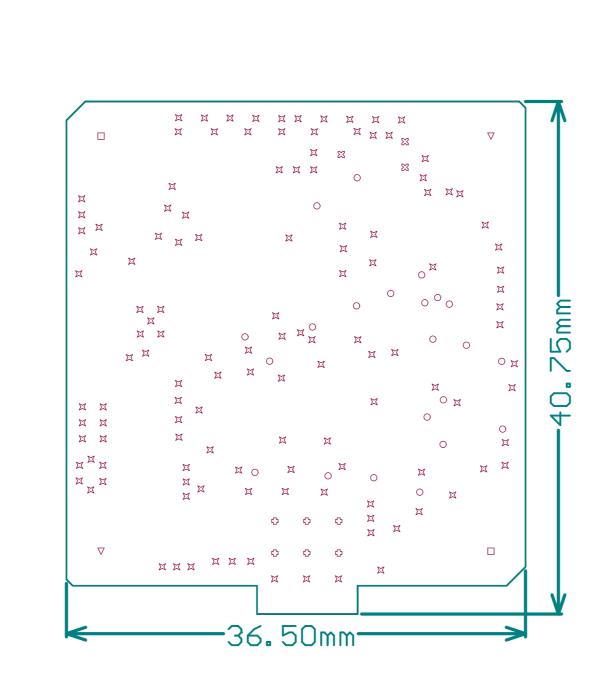
Symbol	Count	Hole Size	Plated	Hole Type	Drill Layer Pair	Via/Pad	Pad Shape	Template
	2	2.400mm (94.49mil)	NPTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c10hn240
∇	2	3.000mm (118.11mil)	NPTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c10hn300
×	3	1.000mm (39.37mil)	NPTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c100hn100
0	6	0.860mm (33.86mil)	PTH	Round	Top Layer - Bottom Layer	Pad	(Mixed)	(Mixed)
0	22	0.254mm (10.00mil)	PTH	Round	Top Layer - Bottom Layer	Via	Rounded	v51h25m0m×0
×	128	0.305mm (12.00mil)	PTH	Round	Top Layer - Bottom Layer	Via	Rounded	(Mixed)
	163 Total							



- 1. LATEST VERSION OF ALL REFERENCED IPC SPECIFICATIONS TO BE USED.
- 2. ALL MULTILAYER PC BOARDS MUST BE PRODUCED IN ACCORDANCE IPC-A-600, CLASS 2.
- 3. INSPECT ALL MULTILAYER PC BOARDS IN ACCORDANCE WITH IPC-6012, CLASS 2.
- 4. MATERIAL: THIN LAMINATE, FR4 COPPER CLAD 1 OZ PER LAYER 1.6 MM +/-10% THICK, COLOR GREEN, MINIMUM CTI (PER IEC 60112 USING SOLUTION A) = 175.
- 5. FABRICATE BOARDS USING THE GERBER FILES CONTAINED IN FABRICATION FOLDER.
- 6. DRILL DATA ARE CONTAINED IN THE FABRICATION FOLDER.
- 7. ALL HOLE DIAMETERS MUST BE PER DRILL CHART +/-0.003 INCHES TOLERANCE, UNLESS OTHERWISE SPECIFIED.
- 8. PLATED-THRU HOLES TO HAVE COPPER WALL THICKNESS NOT LESS THAN 0.001 INCHES. THICKNESS TO BE DETERMINED BY IPC-6012, CLASS 2.
- 9. SURFACE FINISH PROCESS MUST BE HAL LEAD-FREE
- 10. SILKSCREEN BOTH SIDES WITH WHITE EPOXY NON-CONDUCTIVE INK.
- SILKSCREEN MAY BE TRIMMED OFF ANY SOLDERED ENTITY.
- 11. SOLDER MASK BOTH SIDES WITH LIQUID PHOTO IMAGEABLE SOLDER MASK (LPI), MEETING IPC-SM-840, CLASS 2, HIGH PERFORMANCE SPECIFICATIONS. FINISH: SOLDER MASK OVER BARE COPPER (SMOBC).
- 12. WARP AND TWIST SHALL BE LESS THAN 0.0075 INCH PER INCH. INSPECT PER IPC-TM-650, 2.4.22.
- 13. ALL BOARDS MUST BE DATE CODED USING A FOUR DIGIT CODE, YEAR FOLLOWED BY WEEK (e.g. 0449 = 49TH WEEK OF 2004).
- 14. ALL DIMENSIONS AND HOLE DIAMETERS APPLY TO THE FINISHED BOARD.
- 15. PADS MUST BE FINISHED TO WITHIN +/-0.0015 INCHES OF THE MINOR DIMENSION (PAD WIDTH)
- AND +/-0.002 INCHES OF THE MAJOR DIMENSION (PAD LENGTH).

 16. FIDUCIALS MUST BE FREE OF ANY MARKINGS.
- 17. BARE BOARD TO BE ELECTRICALLY TESTED IN ACCORDANCE WITH IPC-ET-652 AND MARKED AS TESTED.
- 18. BOARDS SHALL BE UL-RECOGNIZED AND UL-MARKED. UL FIRE RETARDANT RATING 94V-0 OR HIGHER.
- 19. ROHS: ALL MATERIALS MUST BE COMPLIANT WITH THE ROHS DIRECTIVE.
 20. EACH PCB MUST BE INDIVIDUALLY BAGGED.
- 21. ALL VIA MUST BE TENTED.



Layer	Name	Material	Thickness	Constant	Board Layer Stack
1	Top Overlay				
2	Top Solder	Solder Resist	0.020mm	3.5	
3	Top Layer	Copper	0.035mm		
4	Dielectric 1	PP-006	0.128mm	4.2	
5	Dielectric 2		0.088mm	4.2	
6	PWR	Copper	0.035mm		
7	Dielectric 3	FR-4	1.000mm	4.2	
8	GND	Copper	0.035mm		
9	Dielectric 4	PP-006	0.088mm	4.2	
10	Dielectric 5		0.128mm	4.2	
11	Bottom Layer	Copper	0.035mm		
12	Bottom solder	Solder Resist	0.020mm	3.5	
13	Bottom Overlay				

Total Pcb Thickness = 1.612MM +/- 10% Finished cu Thickness: 1 0Z

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CUSTOMER: LUMAX									
UNLESS OTHERWISE SPECIFIED:	Drawn By : Harshavardhan K		DATE 3/24/2021	Design Partner:					
ALL DIMENSIONS ARE IN INCHES	Designed By : Aravinth D		DATE 3/24/2021	Embdes Engineering Solutions Pvt Ltd					
TOLERANCES (DECIMAL):	Checked By : Uday Kumar K		DATE 3/24/2021	PROJECT NAME: LMAT_AMT_V4					
X +/-0.1 XX +/-0.01 XXX +/-0.005	REF ID		LAYER NAME: Biract Didt Presving						
ANGLES +/-0.5	Scale 1:1	•	NOT SCALE DRAWING	Sheet 1	of 1	Size	DWG NO	REV 1.0	

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ALL DIMENSIONS ARE IN INCHES

Drawn By: Harshavardhan K

DATE

3/24

Designed By: Aravinth D

DATE

3/24 Embdes Engineering Solutions Pvt Ltd TOLERANCES
(DECIMAL):

.X +/-0.1

.XX +/-0.01

.XXX +/-0.005

ANGLES +/-0.5

Checked By: Unit of the content of LAYER NAME: Board Outlingside DO NOT SCALE DRAWING