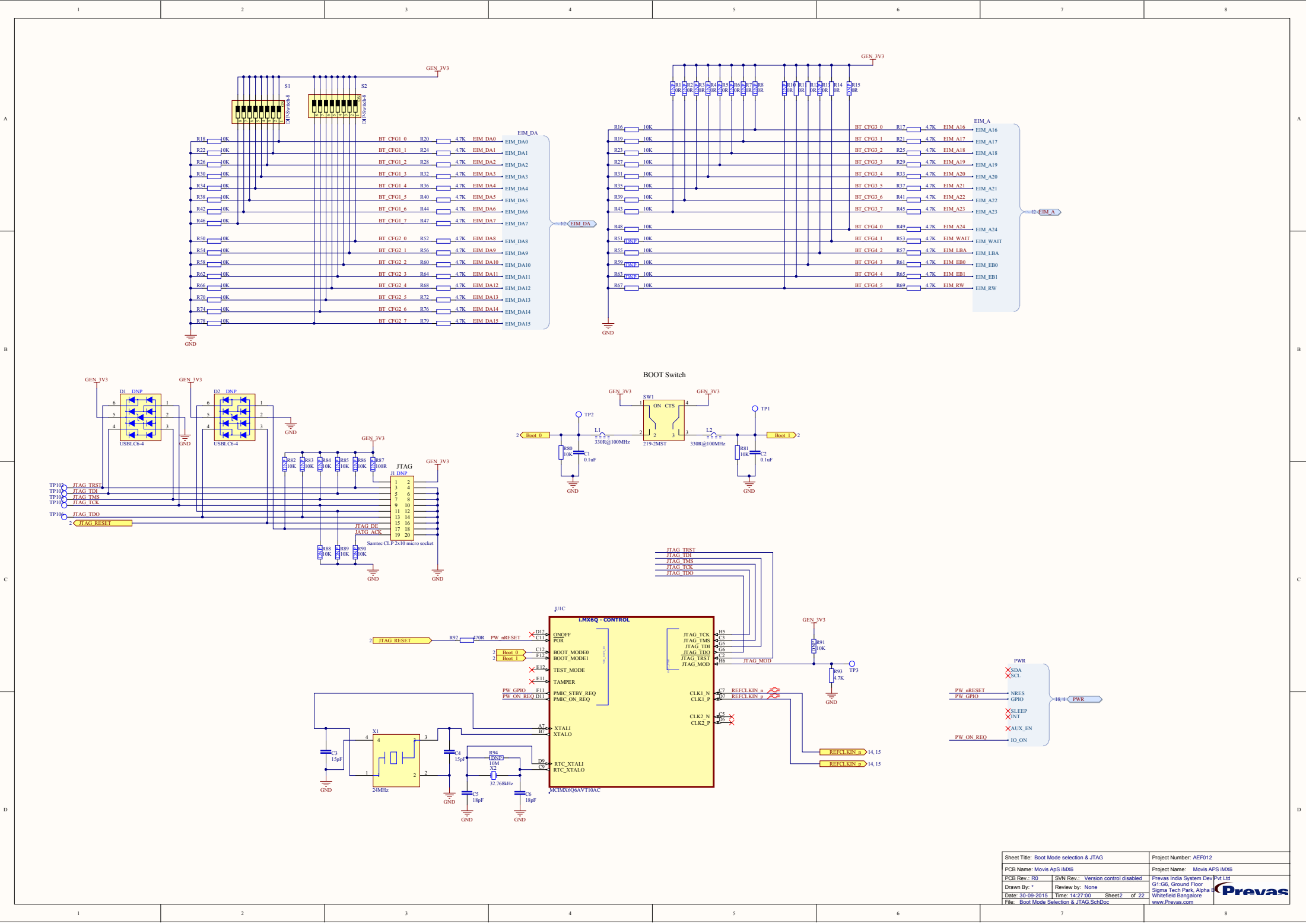
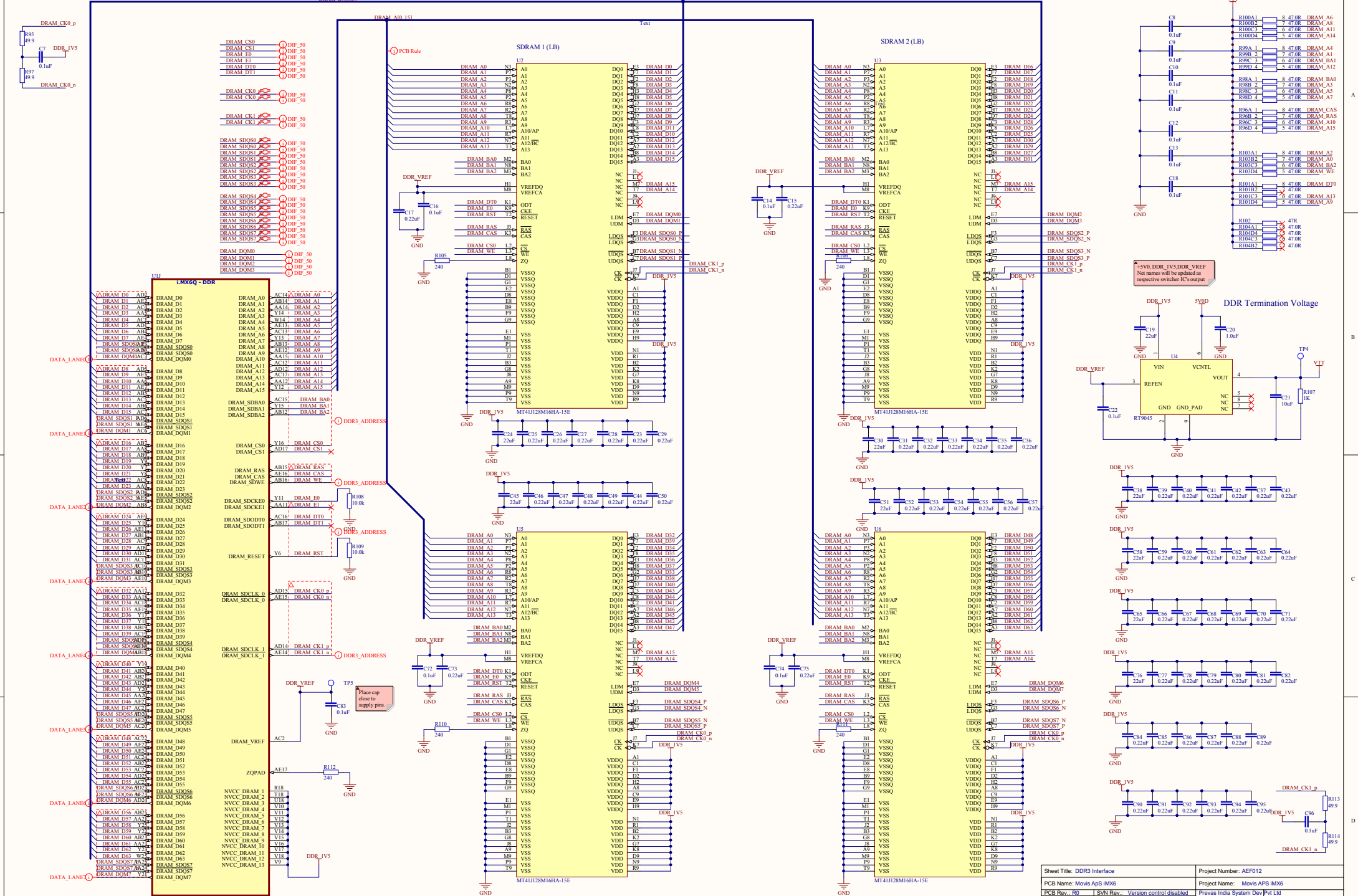




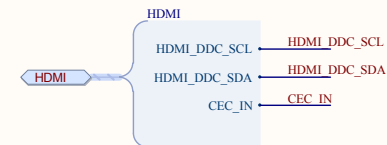
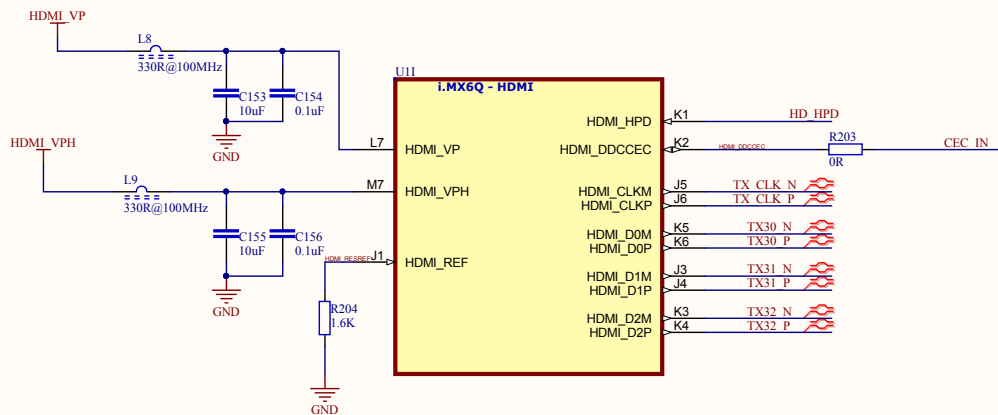
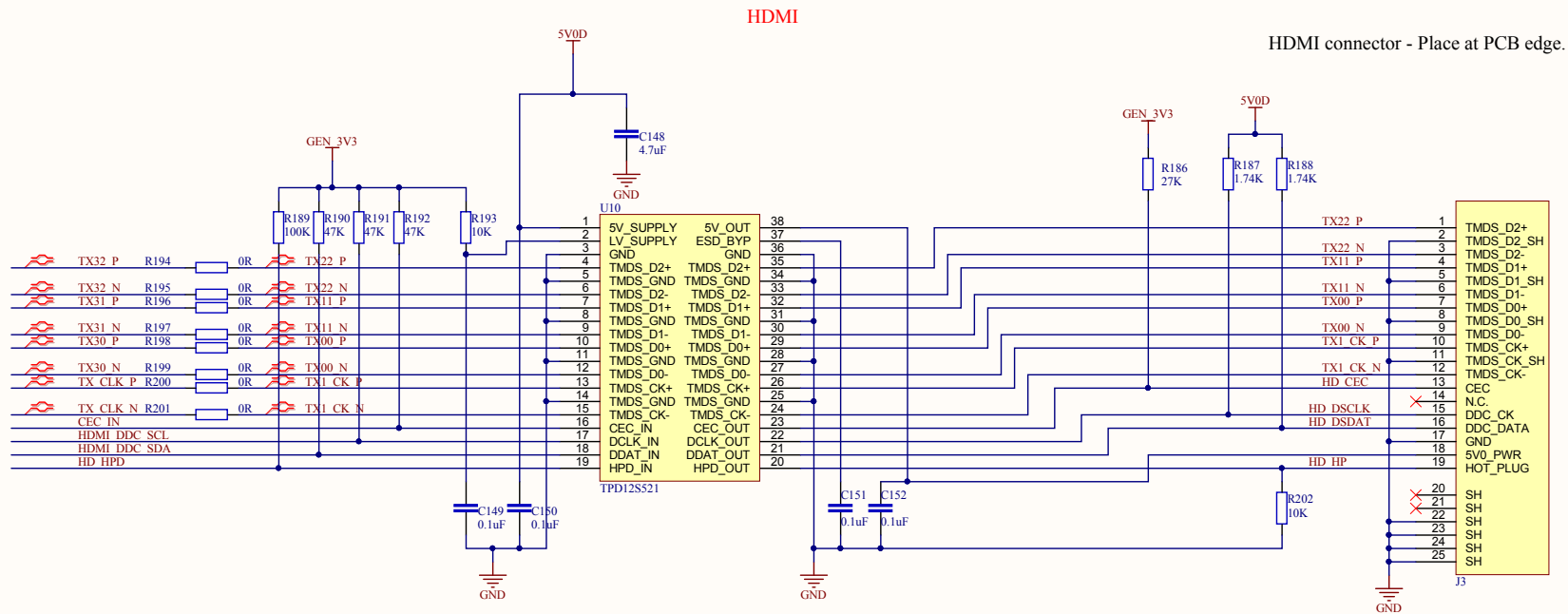
Boot Devices	SW2 (8 Position Switch)								SW3 (8 Position Switch)							
	POS1	POS2	POS3	POS4	POS5	POS6	POS7	POS8	POS1	POS2	POS3	POS4	POS5	POS6	POS7	POS8
<i>SD4 - eMMC</i>	OFF	ON	X	ON	OFF	ON	ON	OFF	OFF	ON	OFF	ON	ON	OFF	ON	OFF
<i>eSPI4- SPI Flash</i>	X	X	X	X	ON	ON	OFF	OFF	X	X	X	X	X	X	X	X
<i>SATA</i>	X	X	X	X	OFF	ON	OFF	OFF	OFF	OFF	OFF	OFF	ON	X	X	X
ON - High				OFF - Low						X - Don't Care						



Place Termination in Track END !!

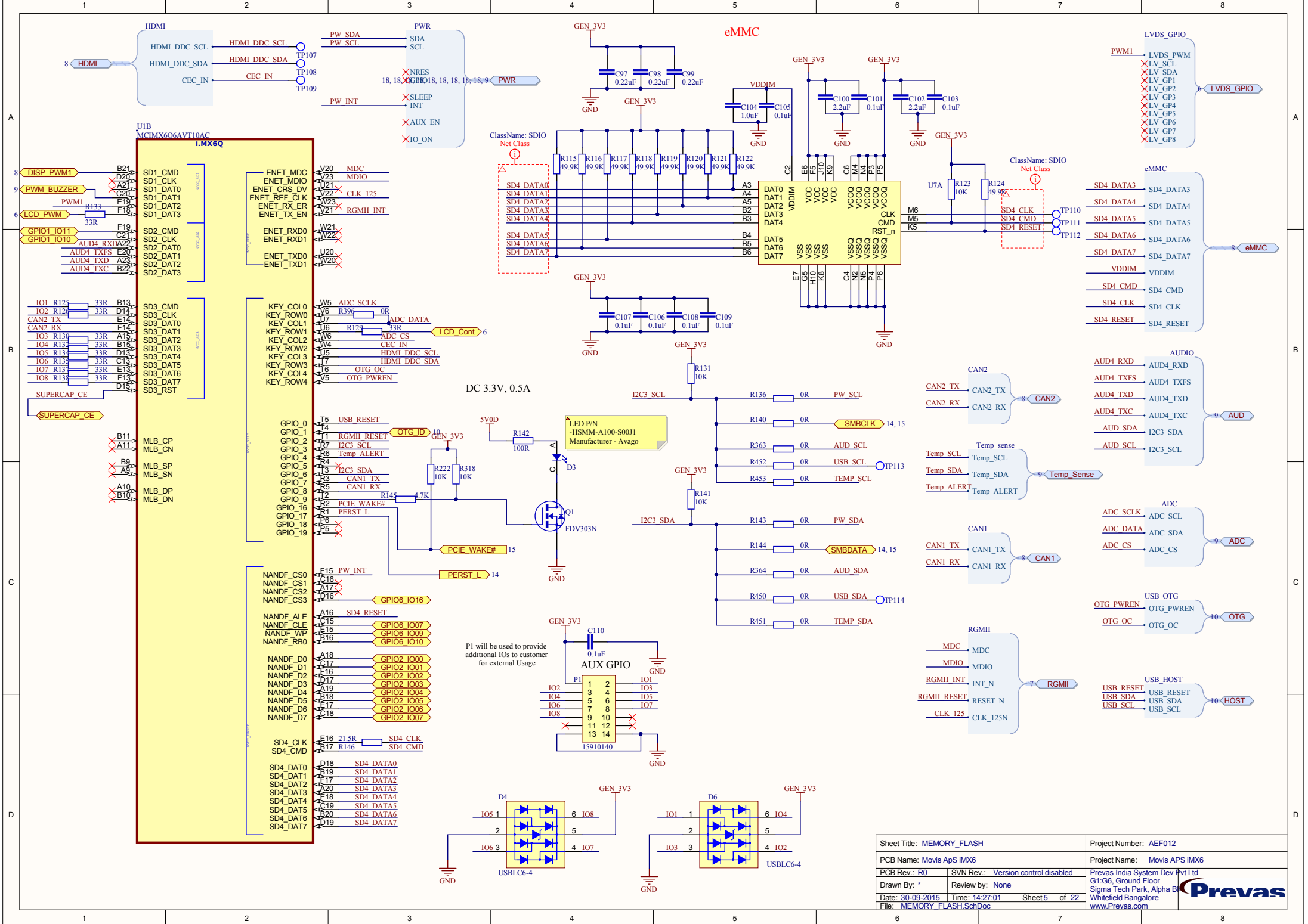


Place Termination in Track END !!

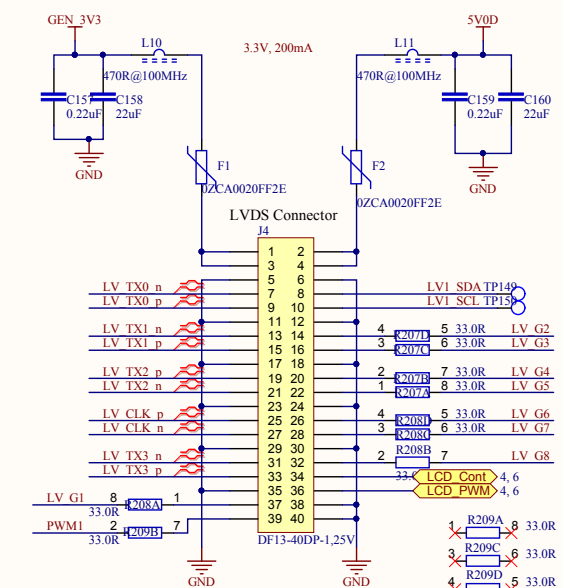
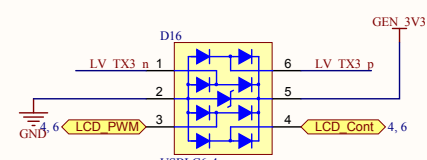
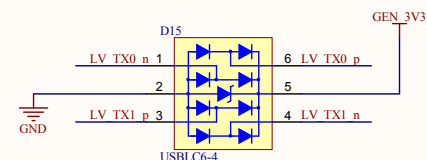
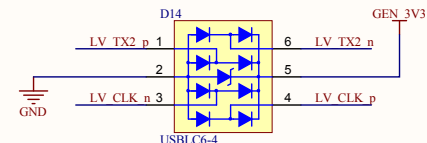
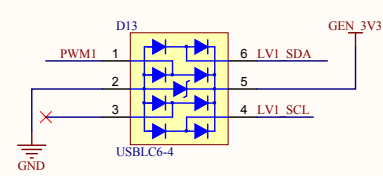
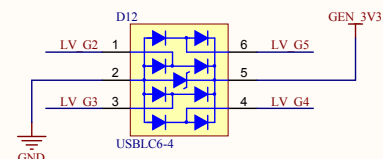
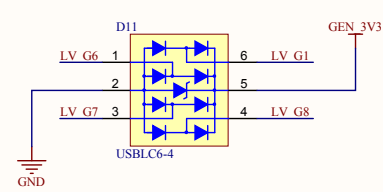
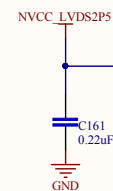
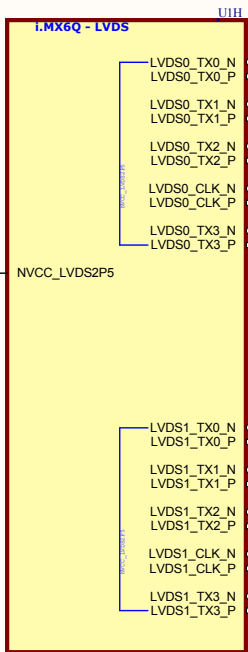


Sheet Title: HDMI Interface		Project Number: AEF012	
PCB Name: Movis ApS IMX6		Project Name: Movis APS IMX6	
PCB Rev.: R0	SVN Rev.: Version control disabled	Prevas India System Dev Pvt Ltd	
Drawn By: *	Review by: None	G1:G6, Ground Floor	
Date: 30-09-2015	Time: 14:27:01	Sigma Tech Park, Alpha B	
File: HDMI Interface SchDoc	Sheet 4 of 22	Whitefield Bangalore	
		www.Prevas.com	

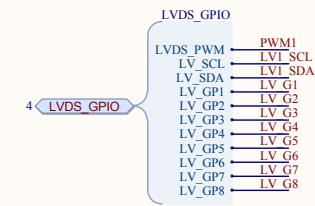
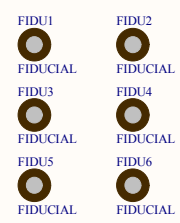




# LVDS

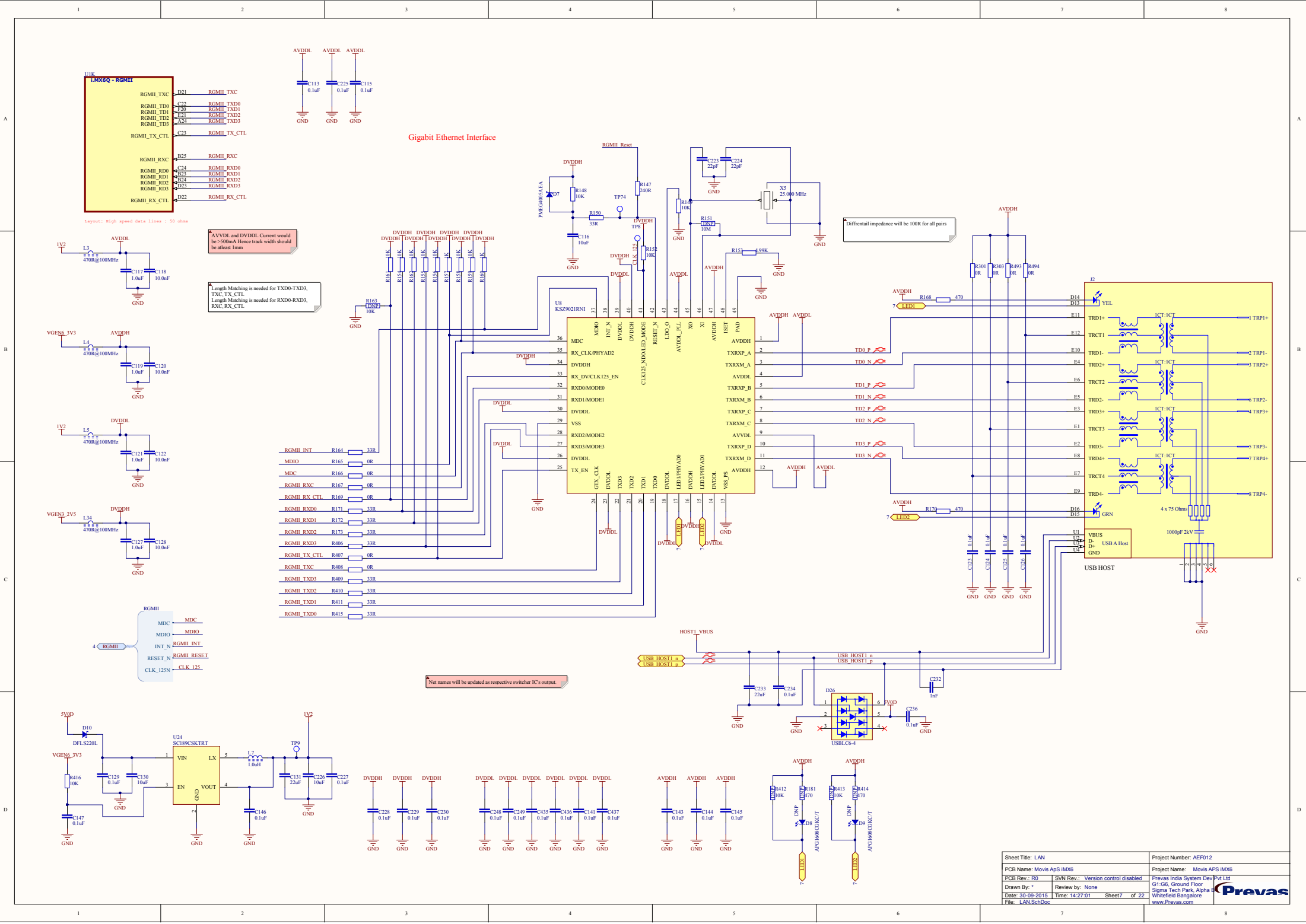


J9 will be for LVDS and other LCD control like Backlight ON/OFF, PWM etc

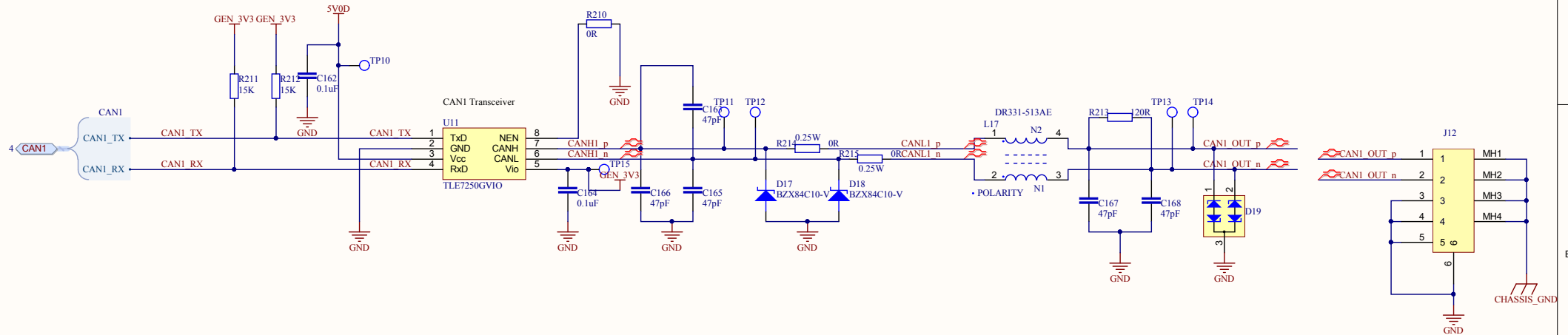


Sheet Title: LVDS Interface		Project Number: AEF012
PCB Name: Movis ApS IMX6		Project Name: Movis APS IMX6
PCB Rev.: R0	SVN Rev.: Version control disabled	Prevas India System Dev Pvt Ltd
Drawn By: *	Review by: None	G1:G6, Ground Floor
Date: 30-09-2015	Time: 14:27:01	Whitefield Bangalore
File: LVDS Interface.SchDoc	Sheet 6 of 22	www.Prevas.com

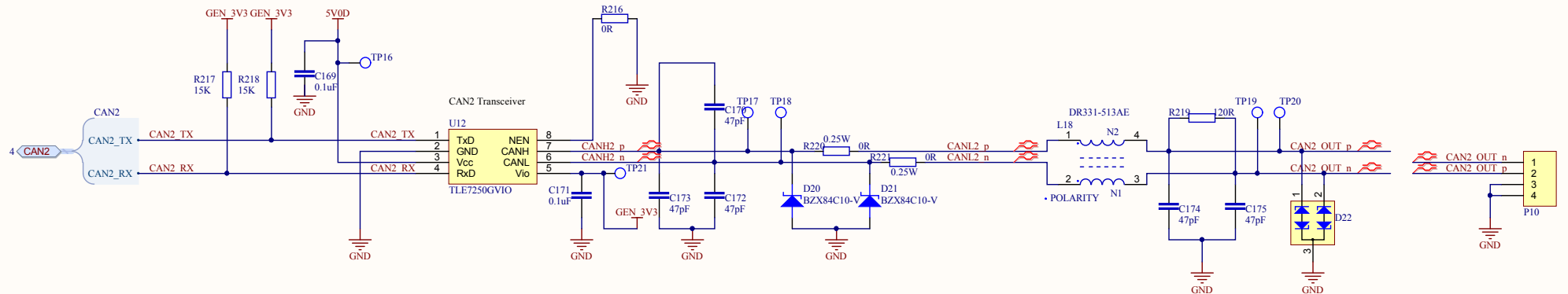




# CAN1 Interface



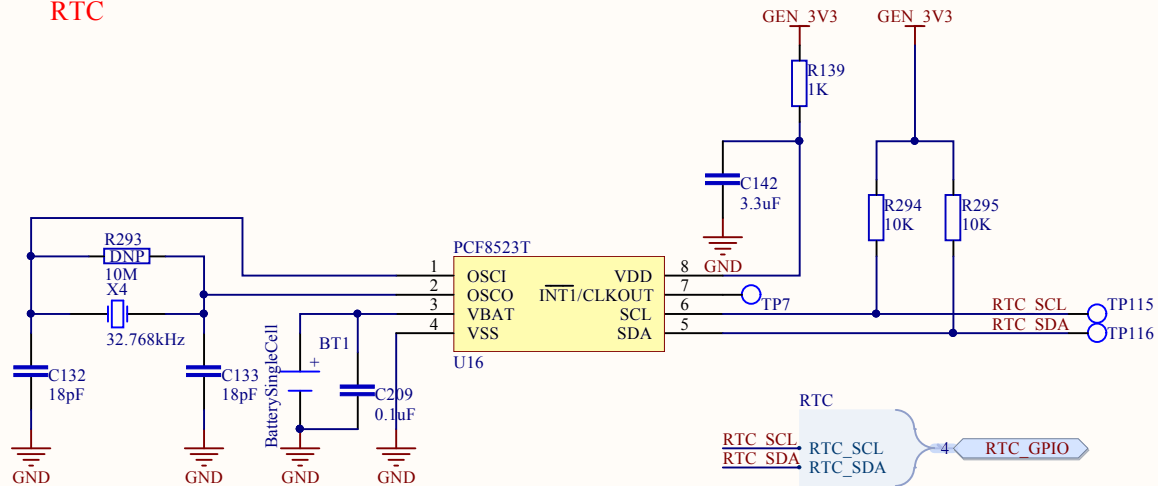
# CAN2 Interface



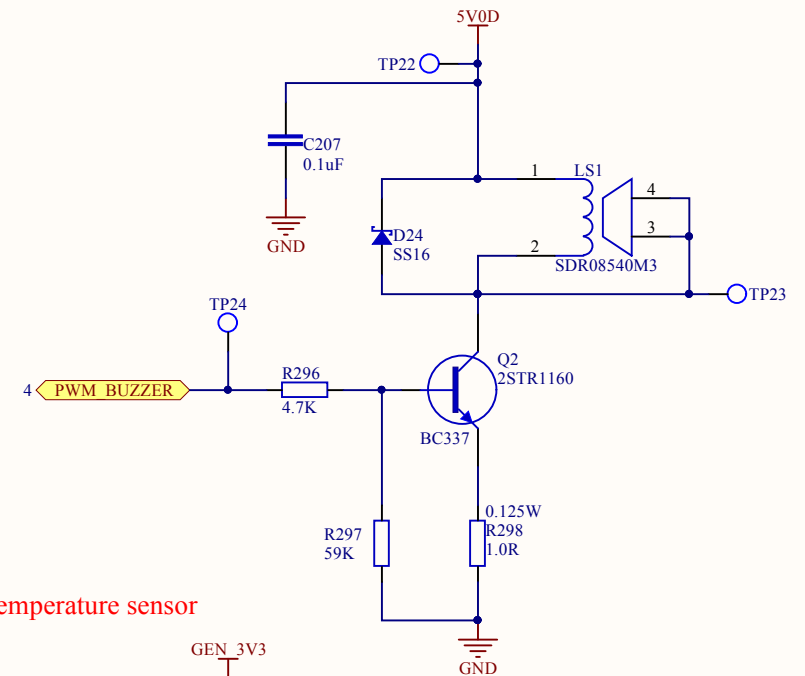
Sheet Title: CAN Interface		Project Number: AEF012	
PCB Name: Movis ApS IMX6		Project Name: Movis APS IMX6	
PCB Rev.: R0	SVN Rev.: Version control disabled	Prevas India System Dev Pvt Ltd	
Drawn By: INVIHA	Review by: None	G1:G6, Ground Floor	
Date: 30-09-2015	Time: 14:27:01	Sigma Tech Park, Alpha B	
File: CAN Interface.SchDoc	Sheet 8 of 22	Whitefield Bangalore	
		www.Prevas.com	



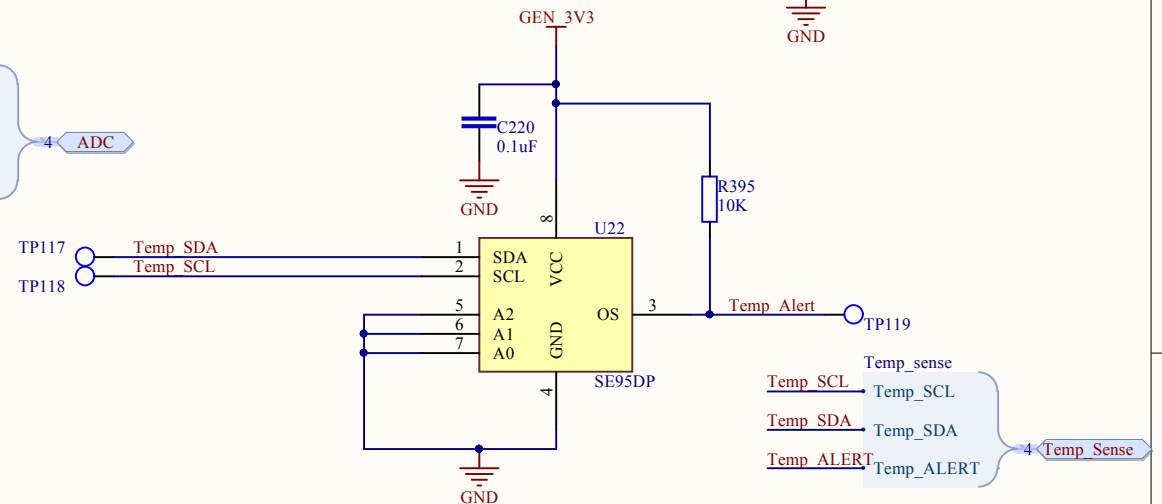
## RTC



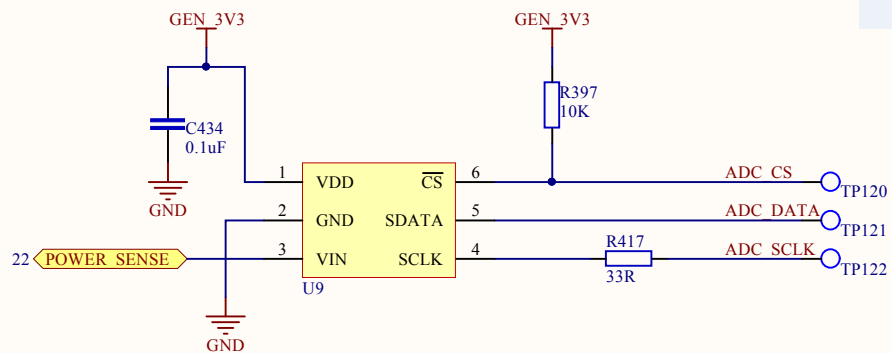
## BUZZER



## Temperature sensor



## ADC

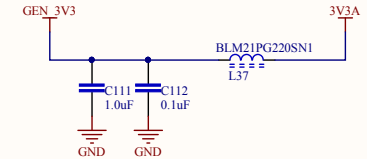
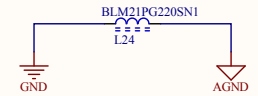
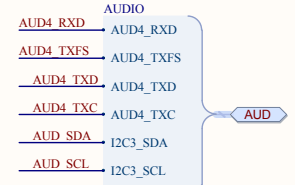
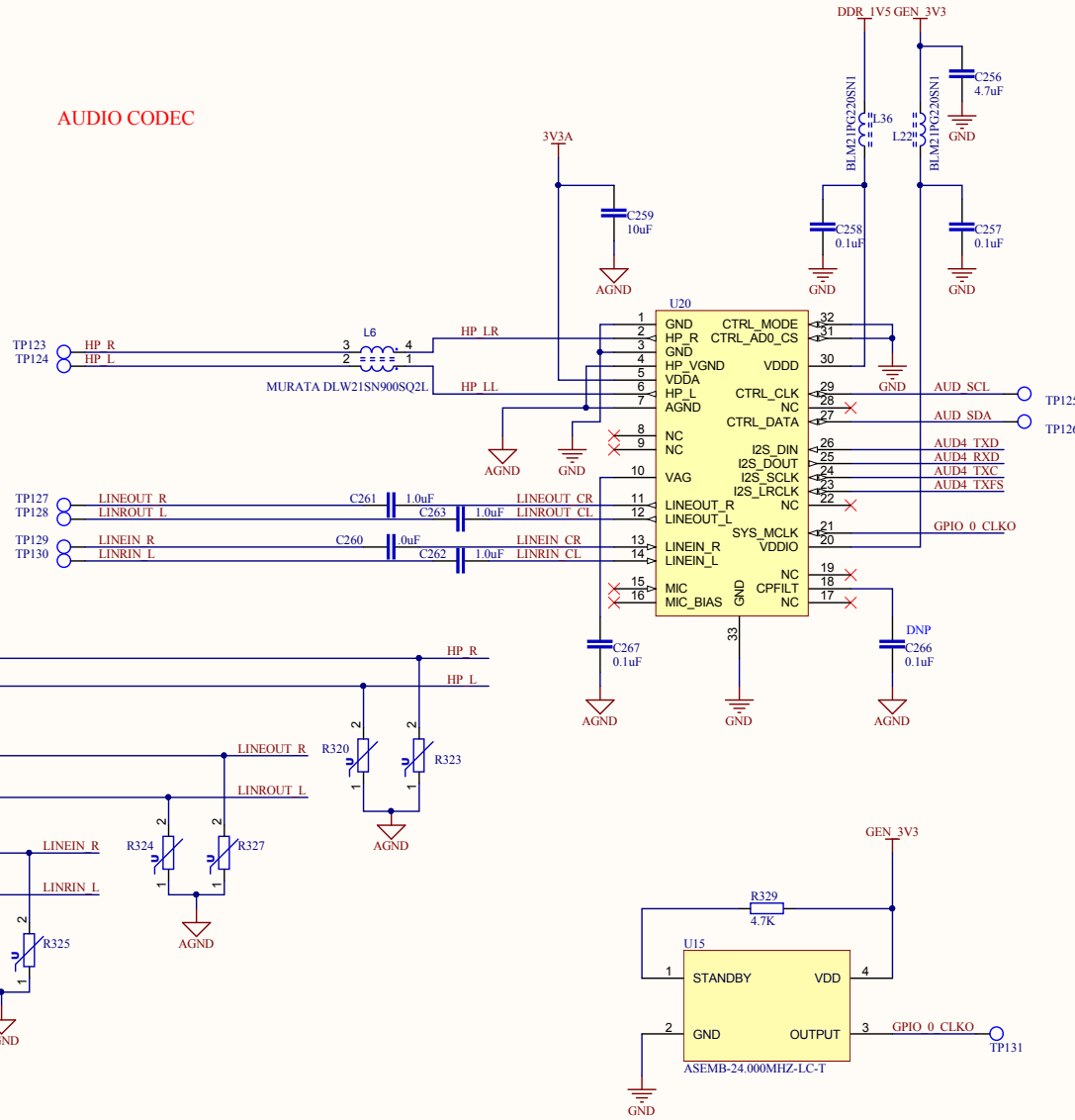


Sheet Title: RTC & Buzzer		Project Number: AEF012	
PCB Name: Movis ApS iMX6		Project Name: Movis APS iMX6	
PCB Rev.: R0	SVN Rev.: Version control disabled	Prevas India System Dev Pvt Ltd	
Drawn By: INVIHA	Review by: None	G1:G6, Ground Floor	
Date: 30-09-2015	Time: 14:27:01	Sigma Tech Park, Alpha B	
File: RTC & Buzzer.SchDoc	Sheet 9 of 22	Whitefield Bangalore	
		www.Prevas.com	



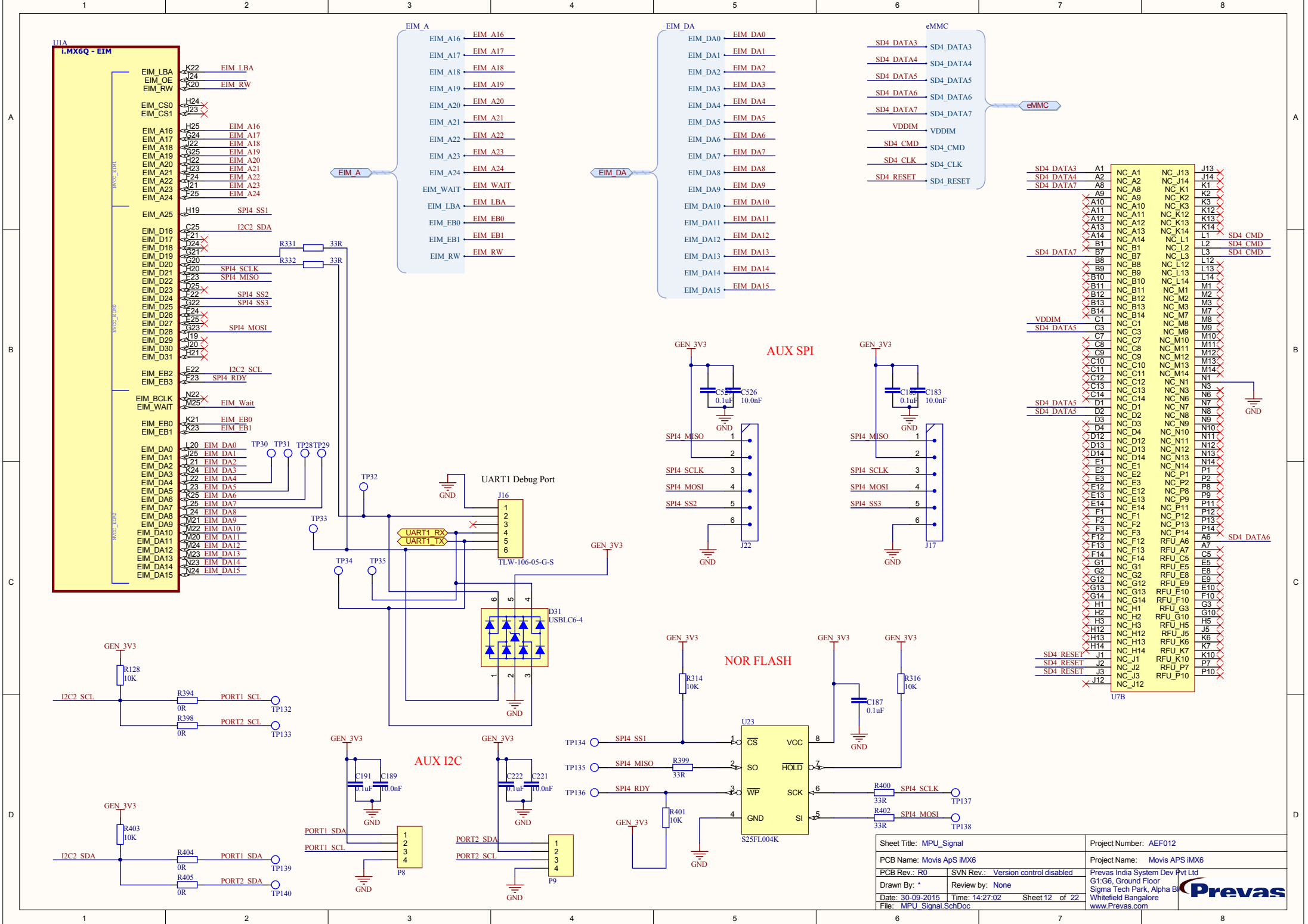


# AUDIO CODEC



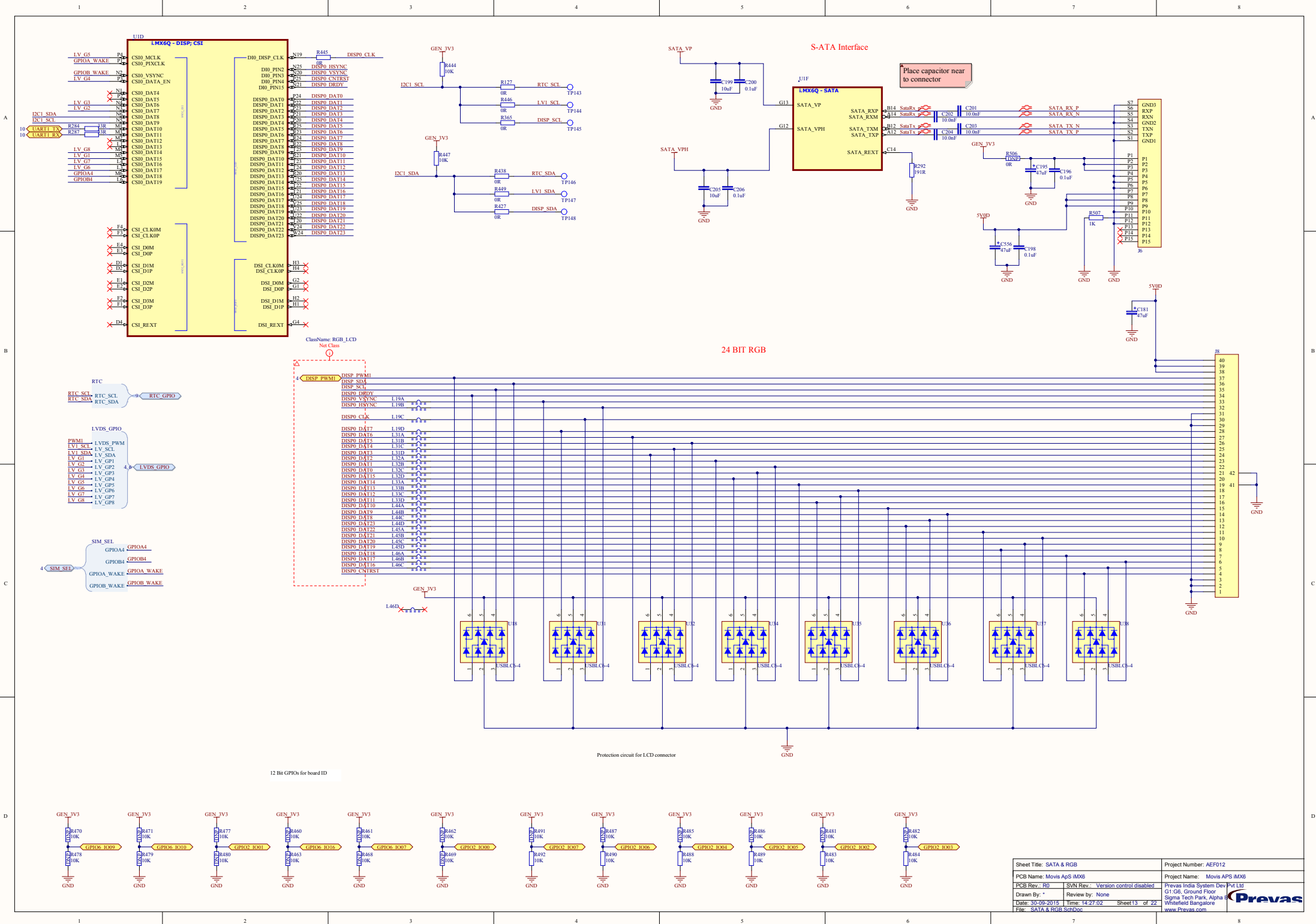
Sheet Title: Audio Codec		Project Number: AEF012	
PCB Name: Movis ApS IMX6		Project Name: Movis APS IMX6	
PCB Rev.: R0	SVN Rev.: Version control disabled	Prevas India System Dev Pvt Ltd	
Drawn By: *	Review by: None	G1:G6, Ground Floor	
Date: 30-09-2015	Time: 14:27:02	Sigma Tech Park, Alpha B	
File: Audio Codec.SchDoc	Sheet 11 of 22	Whitefield Bangalore	
		www.Prevas.com	

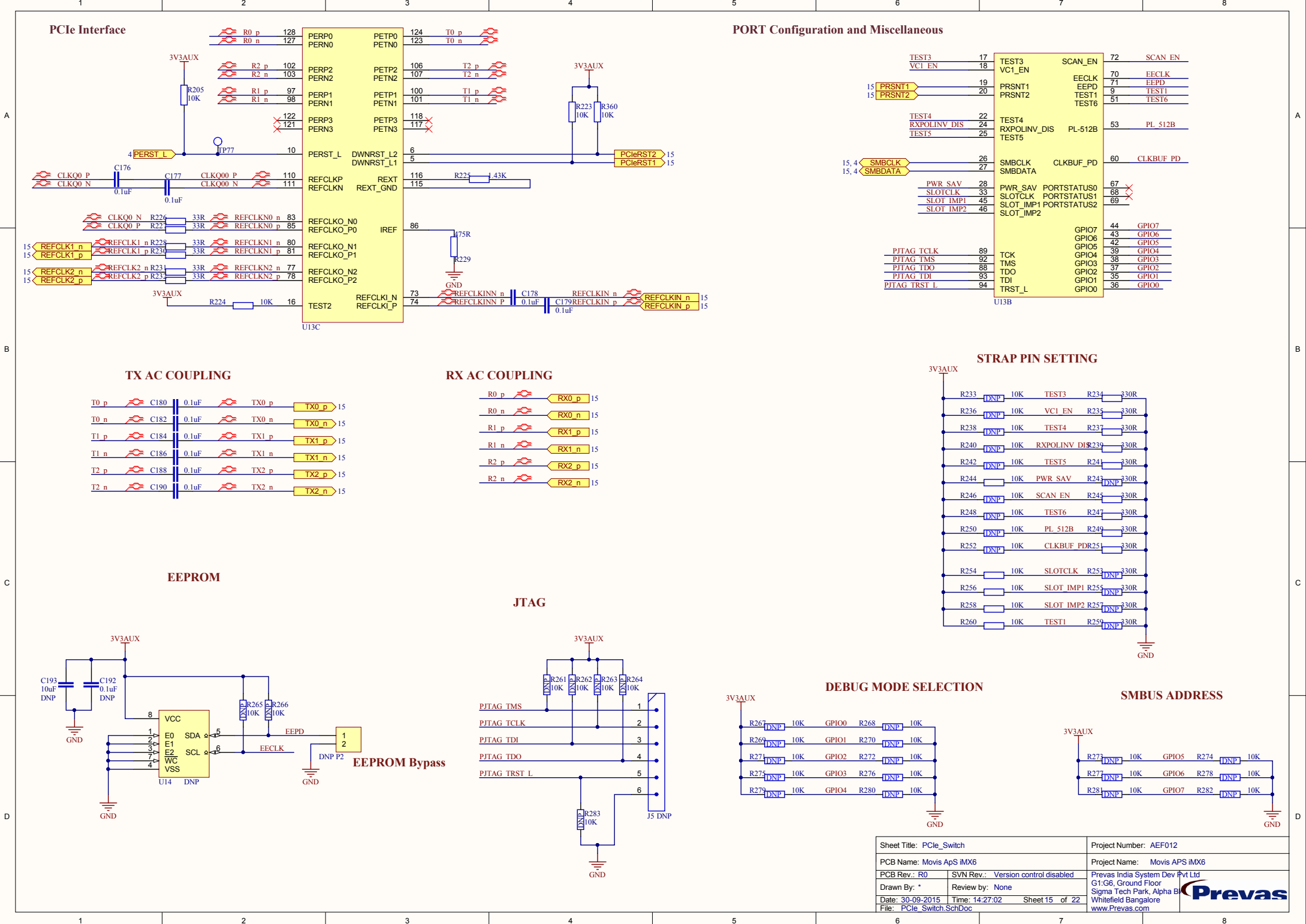




Sheet Title: MPU_Signal	Project Number: AEF012
PCB Name: Movis ApS IMX6	Project Name: Movis APS IMX6
PCB Rev.: R0	SVN Rev.: Version control disabled
Drawn By: *	Review by: None
Date: 30-09-2015	Time: 14:27:02
File: MPU_Signal.SchDoc	Sheet 12 of 22
	Prevas India System Dev Pvt Ltd G1:G6, Ground Floor Sigma Tech Park, Alpha B Whitefield Bangalore www.Prevas.com









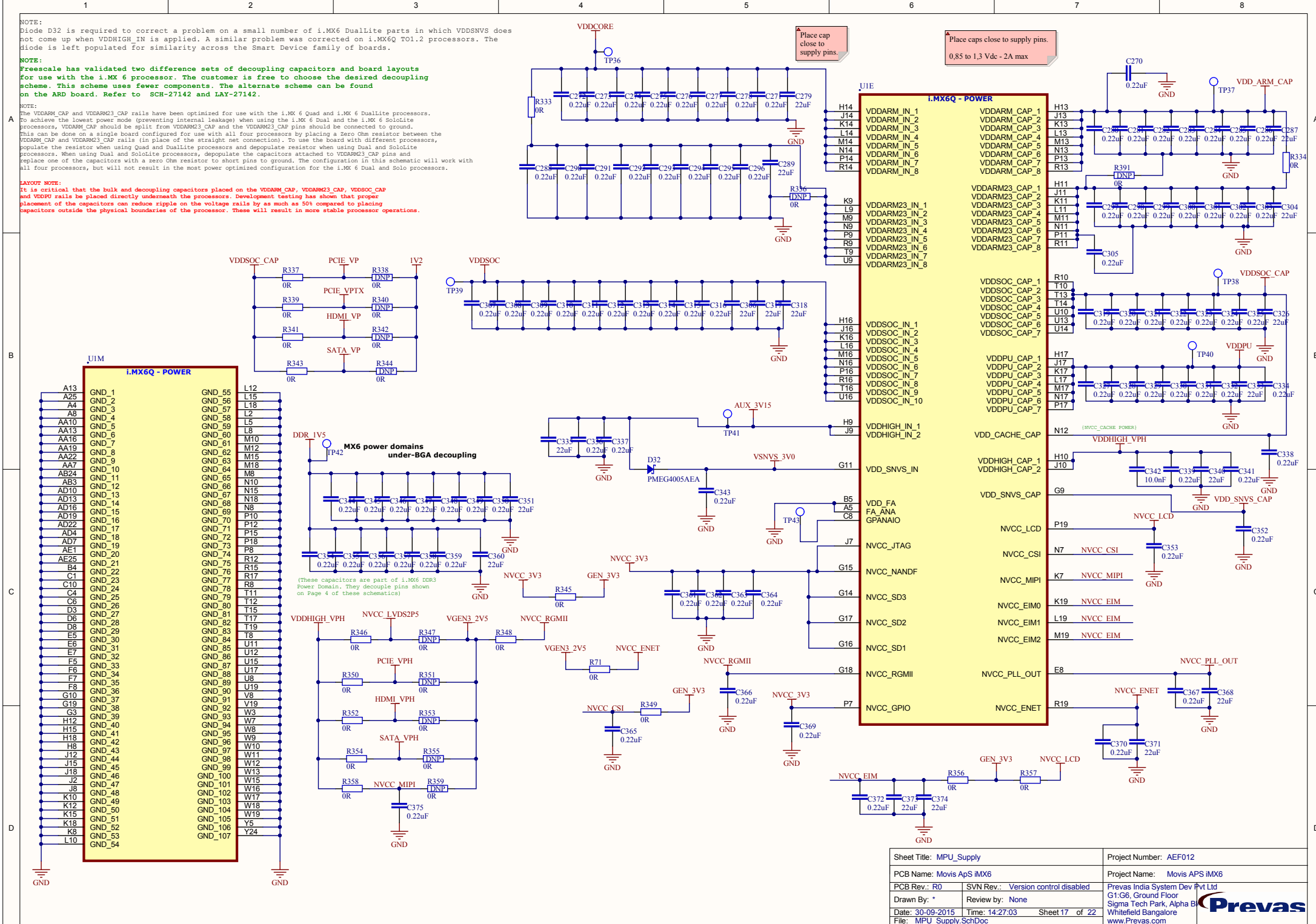




NOTE:  
 Freescale has validated two difference sets of decoupling capacitors and board layouts for use with the i.MX 6 processor. The customer is free to choose the desired decoupling scheme. This scheme uses fewer components. The alternate scheme can be found on the ARD board. Refer to SCH-27142 and LAY-27142.

NOTE:  
The VDDARM3\_CAP and VDDARM23\_CAP rails have been optimized for use with the 1.0M  $\Omega$  Quad and 1.0M  $\Omega$  DualLite processors.  
To achieve the lowest power mode (preventing internal leakage) when using the 1.0M  $\Omega$  Dual and the 1.0M  $\Omega$  SoloLite processors, the VDDARM3\_CAP and VDDARM23\_CAP resistors must be populated on the VDDARM3\_CAP and VDDARM23\_CAP pins.  
This can be done on a single board configured for use with all four processors by placing a Zero Ohm resistor between the VDDARM3\_CAP and VDDARM23\_CAP rails (in place of the straight net connection). To use the board with different processors, populate the resistor when using Quad and DualLite processors and depopulate resistor when using Dual and SoloLite processors. When using Dual and SoloLite processors, depopulate the capacitors attached to VDDARM3\_CAP pins and one of the capacitors with a zero Ohm resistor to short to ground. The configuration in this schematic will work with all four processors, but will not result in the most power optimized configuration for the 1.0M  $\Omega$  Dual and Solo processors.

**LAYOUT NOTE:**  
It is critical that the bulk and decoupling capacitors placed on the VDDARM\_CAP, VDDARM23\_CAP, VDDSOC\_CAP and VDDPU rails be placed directly underneath the processors. Development testing has shown that proper placement of the capacitors can reduce ripple on the voltage rails by as much as 50% compared to placing capacitors outside the physical boundaries of the processor. This will result in more stable processor operations.





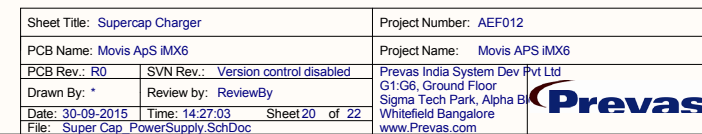
Layout:  
Avoid using vias on nets with high currents.

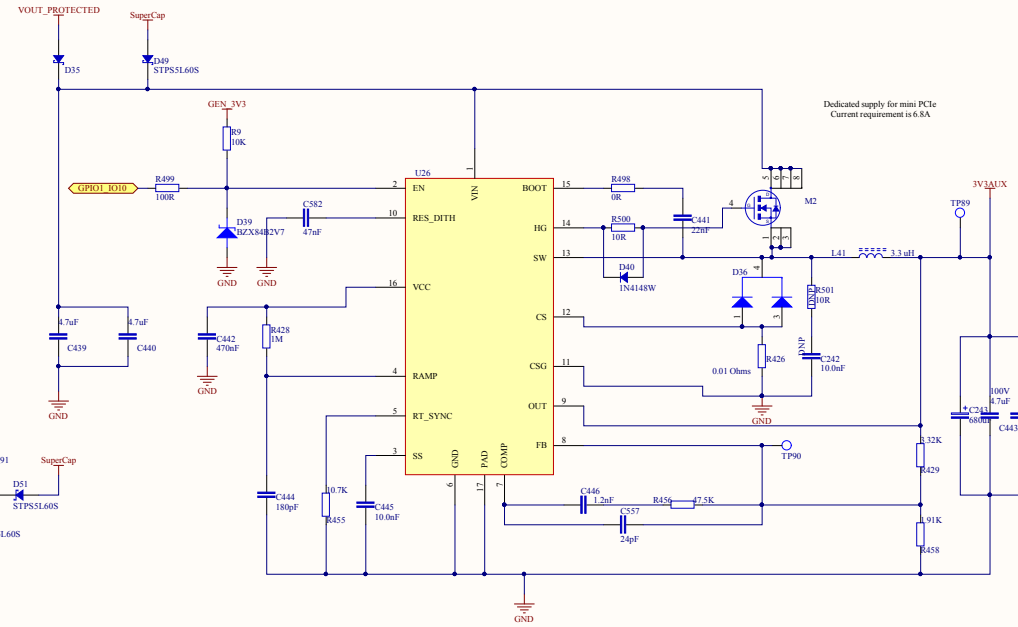
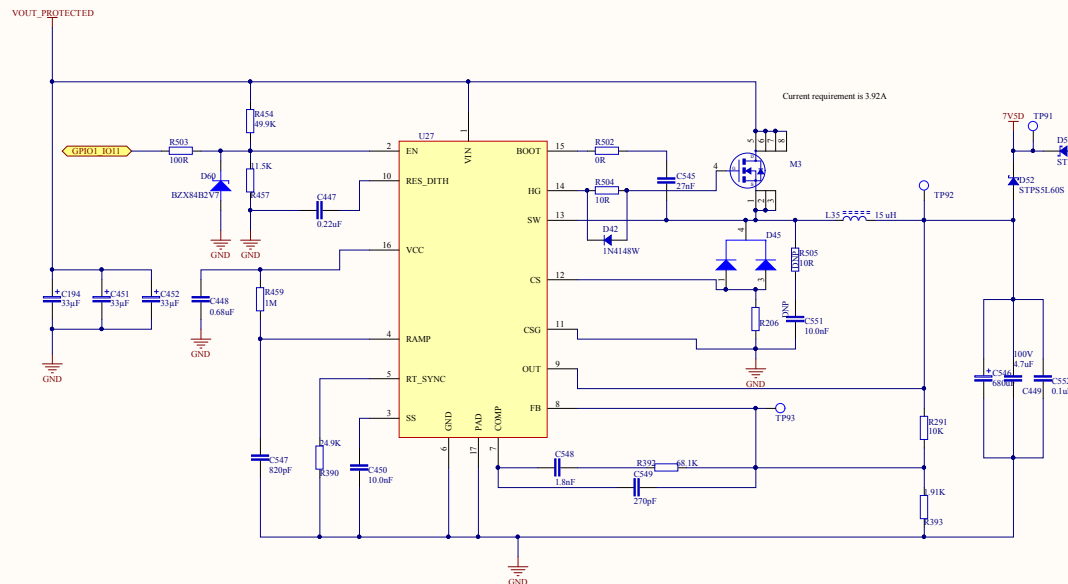
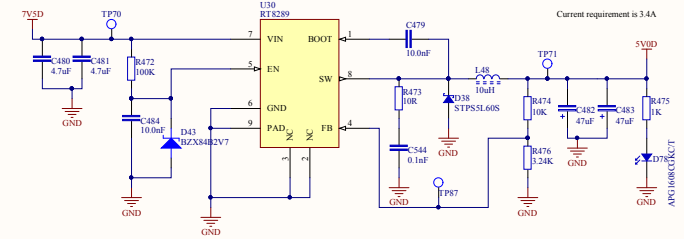
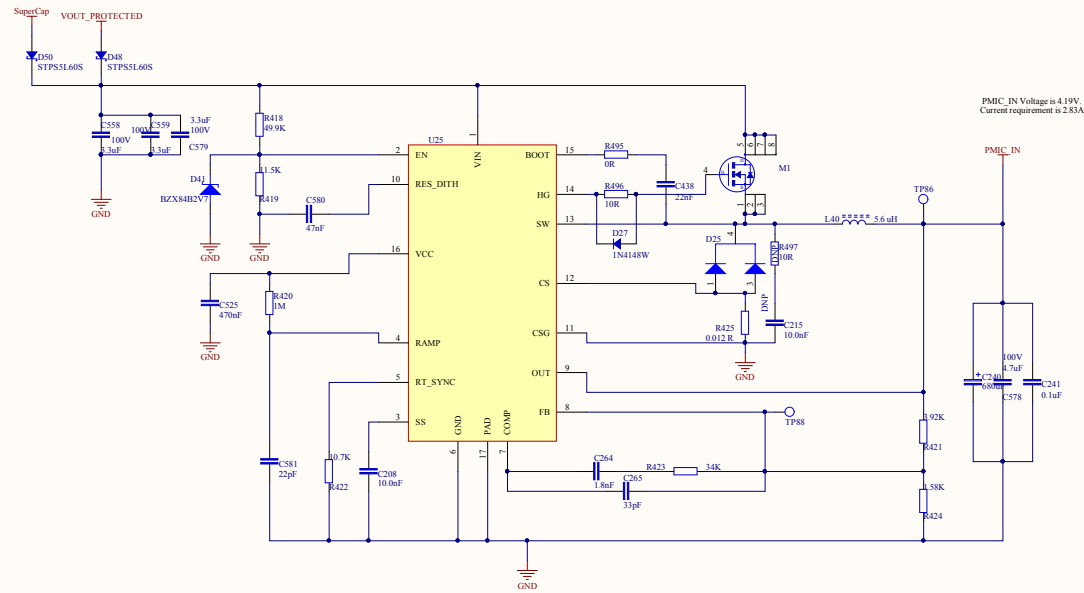
Layout:  
C11, 12, 13 and 14 will come after feedback  
resistor R24 and R25

PMIC STATUS

Sheet Title: PMIC1_IMX6		Project Number: AEF012	
PCB Name: Movis ApS IMX6		Project Name: Movis APS IMX6	
PCB Rev.: R0	SVN Rev.: Version control disabled	Prevas India System Dev Pvt Ltd	
Drawn By: *	Review by: None	G1:G6, Ground Floor	
Date: 30-09-2015	Time: 14:27:03	Sigma Tech Park, Alpha B	
File: PMIC1_IMX6.SchDoc	Sheet 19 of 22	Whitefield Bangalore	
		www.Prevas.com	

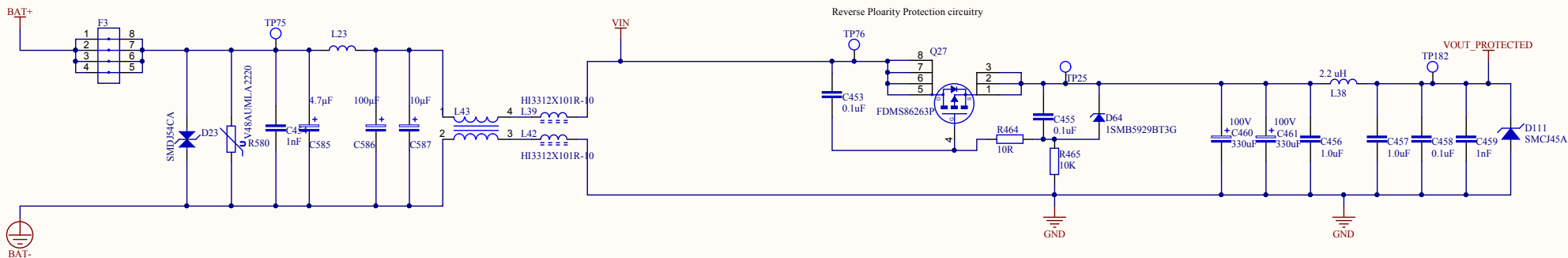




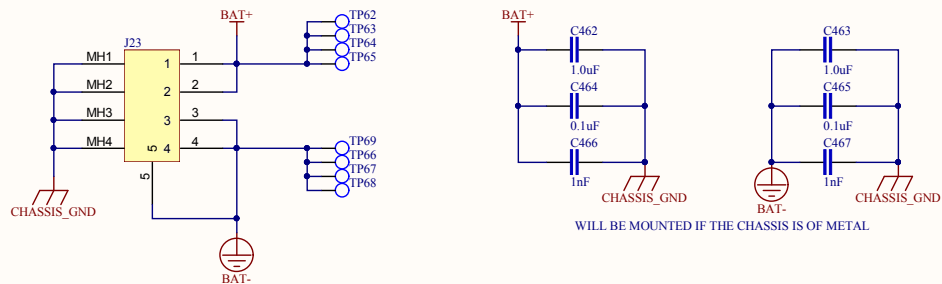


Recommended Fuses - 166.7000.4752, 166.7000.4506

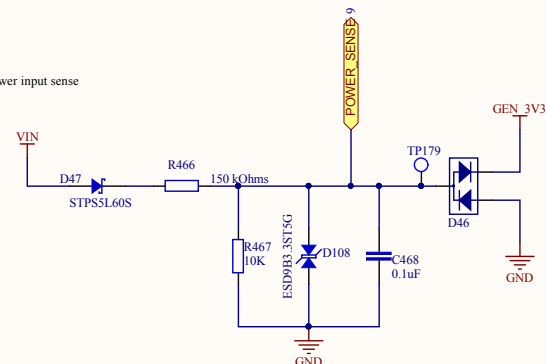
## BLADE FUSE HOLDER



## BATTERY CONNECTOR



## Power input sense



Sheet Title: Main Power Input		Project Number: AEF012	
PCB Name: Movis ApS IMX6		Project Name: Movis APS IMX6	
PCB Rev.: R0	SVN Rev.: Version control disabled	Prevas India System Dev Pvt Ltd	
Drawn By: *	Review by: None	G1:G6, Ground Floor	
Date: 30-09-2015	Time: 14:27:03	Sigma Tech Park, Alpha B	
File: Main Power Input.SchDoc	Sheet 22 of 22	Whitefield Bangalore	
		www.Prevas.com	