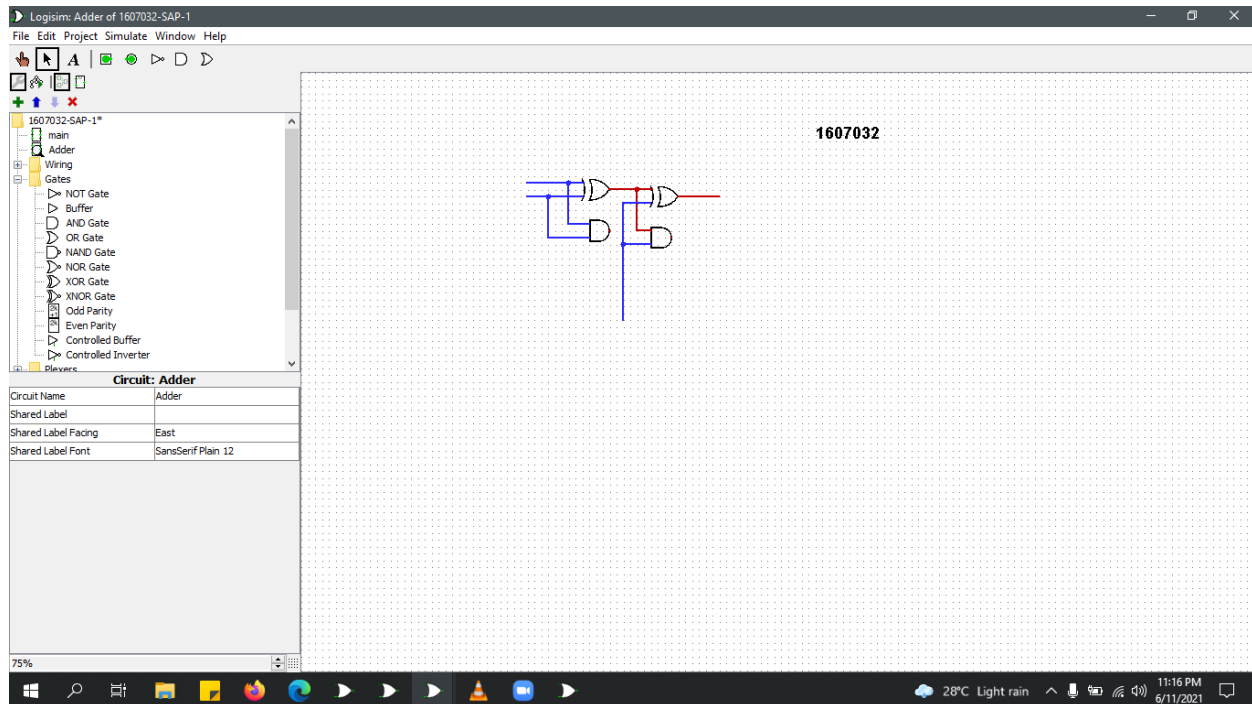
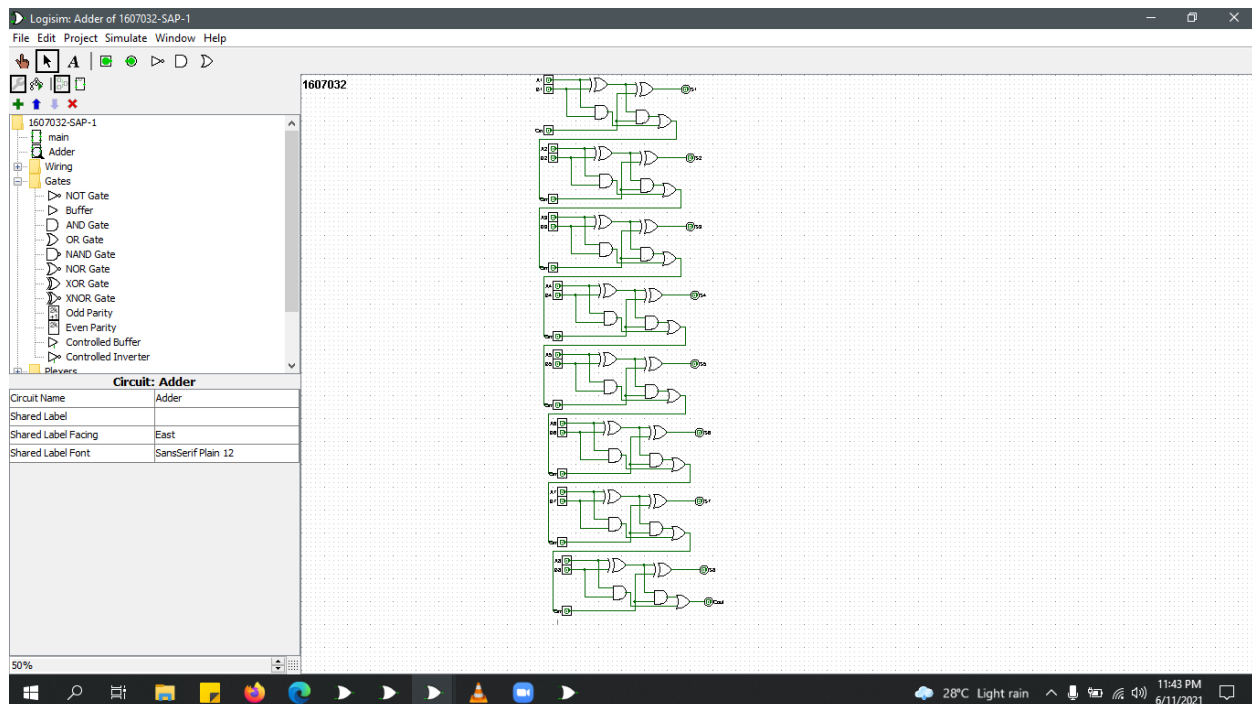


# 1607032 Workflow SS of SAP-1 Project:

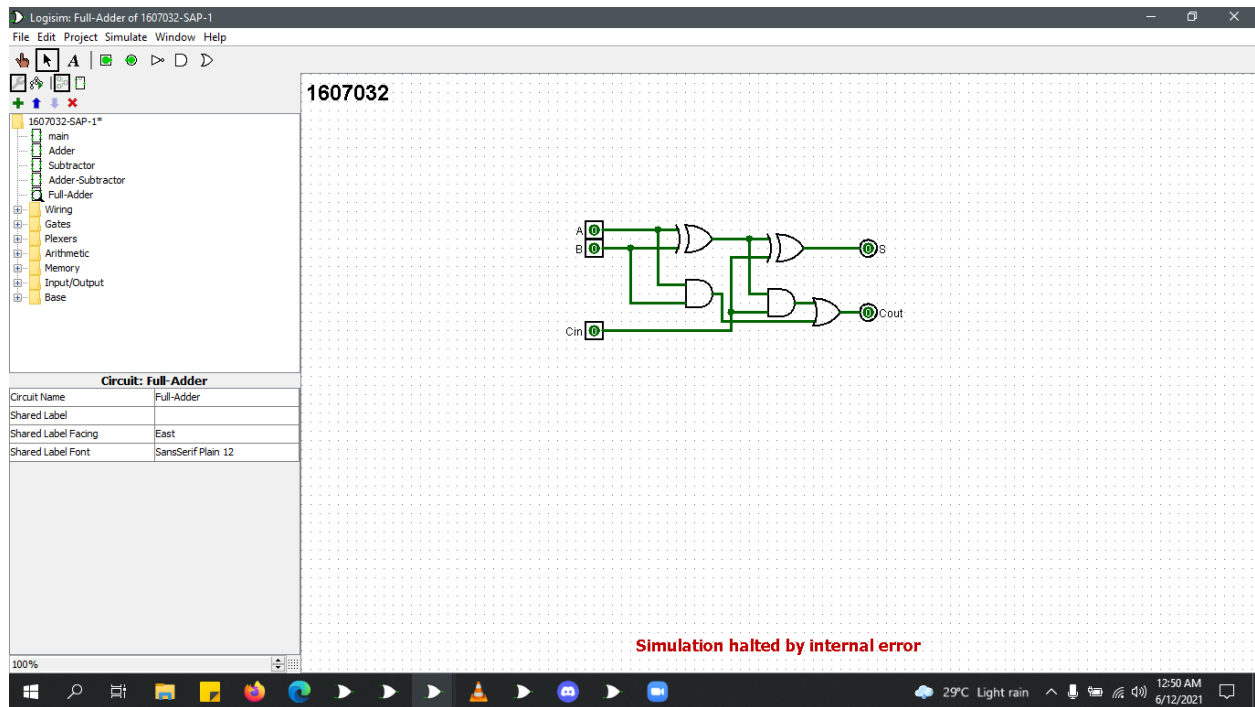
1607032: Adder Construction:



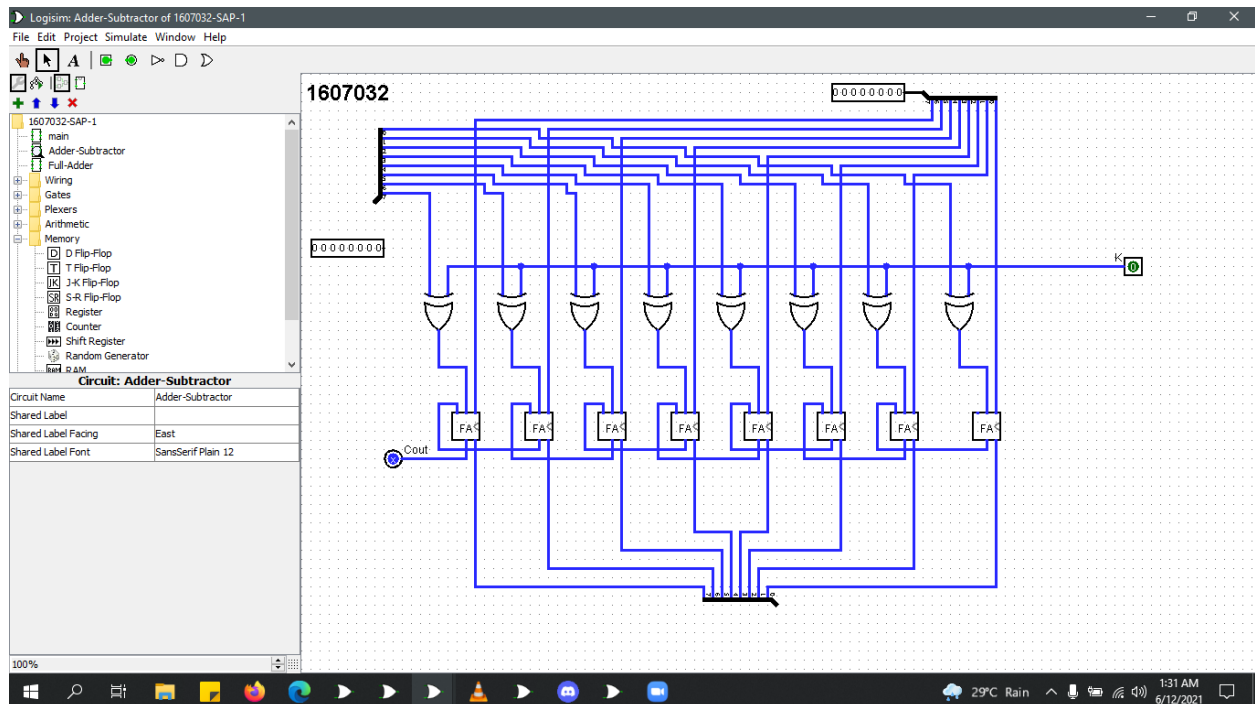
1607032: 8 bit Full Adder:



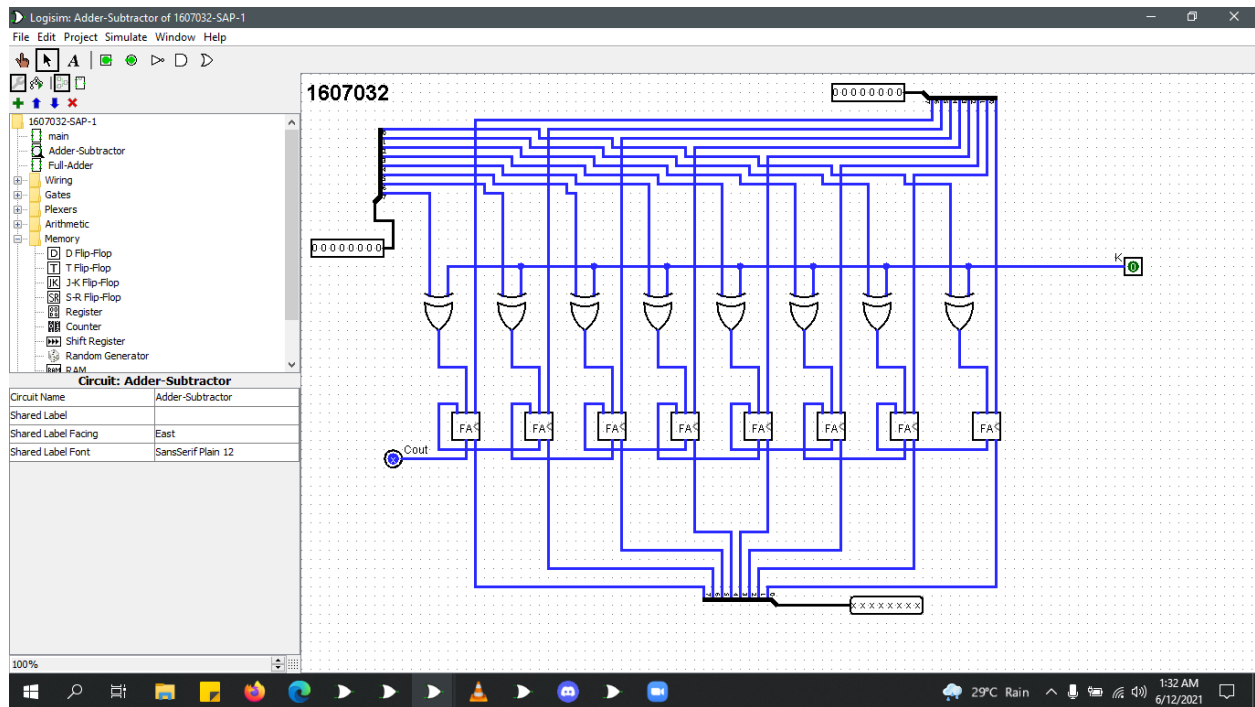
## 1607032: True Complement



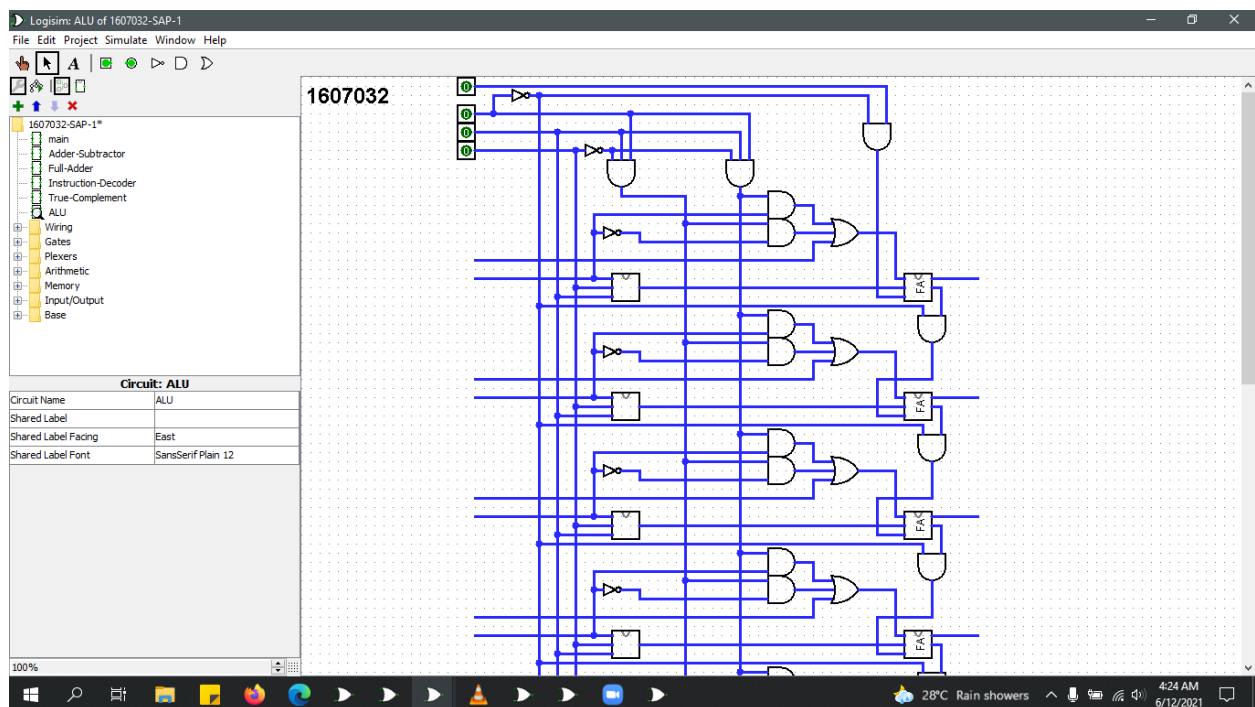
## 1607032: 8 Bit Adder- Subtractor:



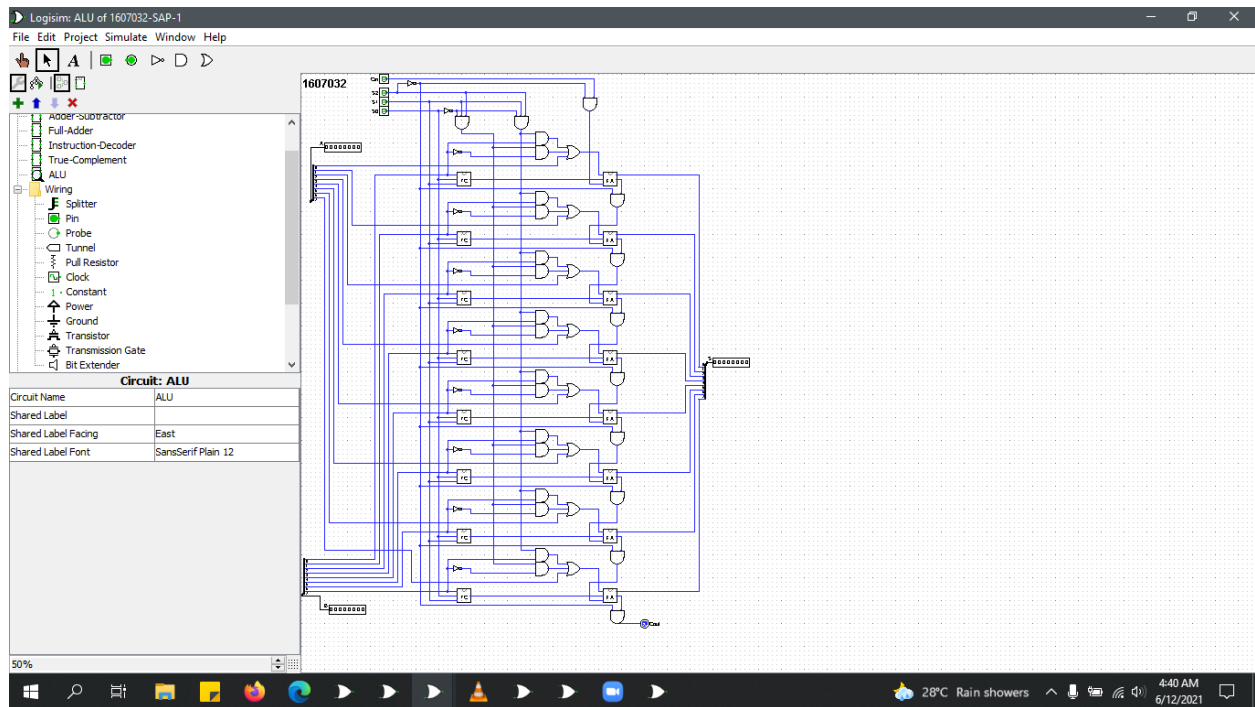
## 1607032: 8 bit Adder Subtractor:



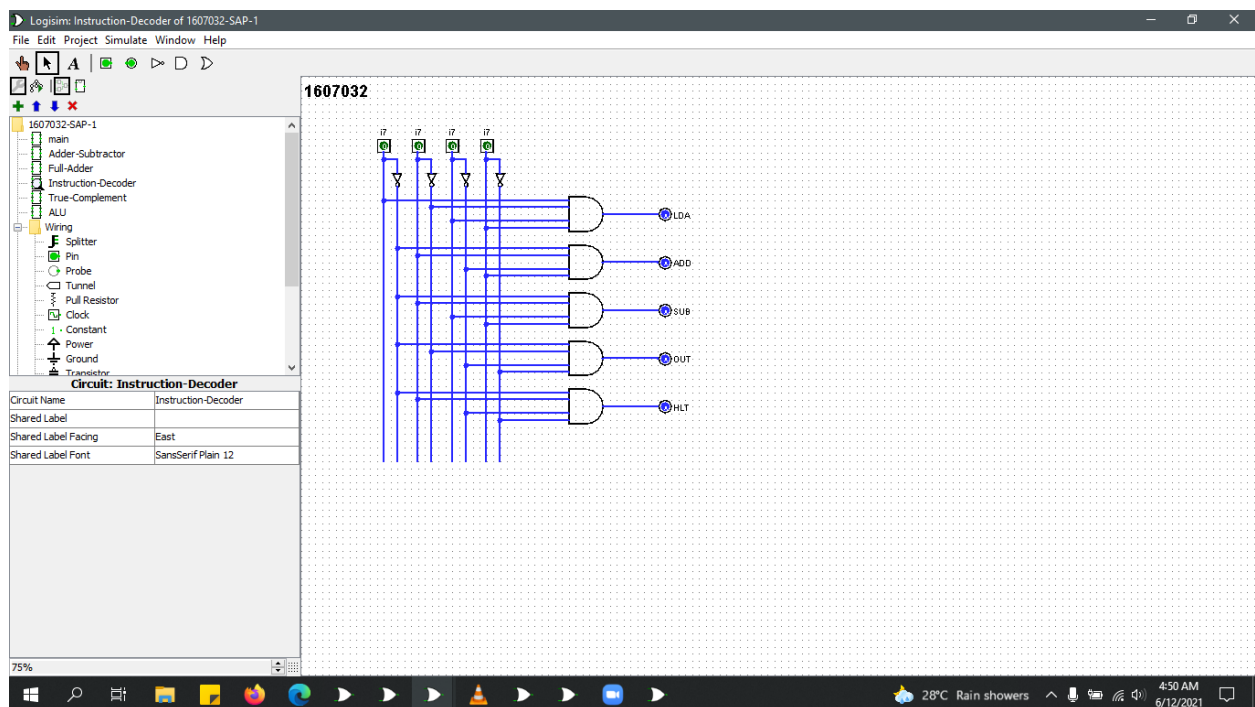
## 1607032: 8 Bit ALU design for check:



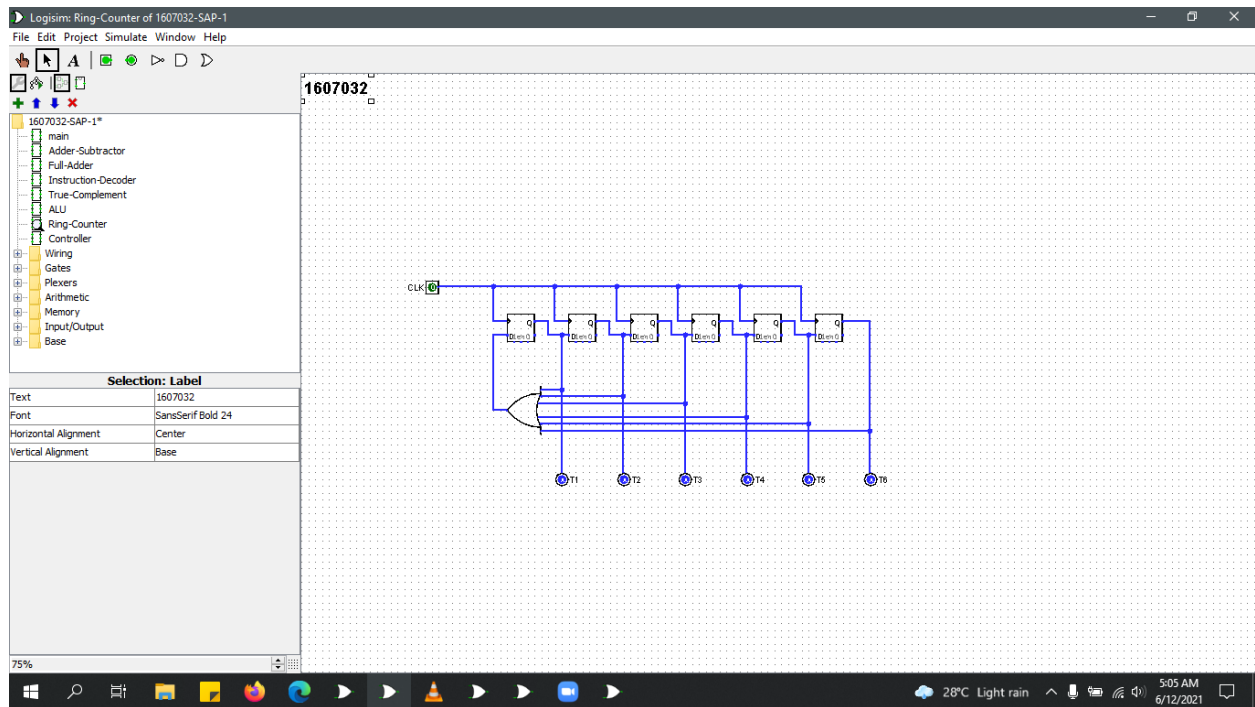
## 1607032 : 8 Bit ALU:



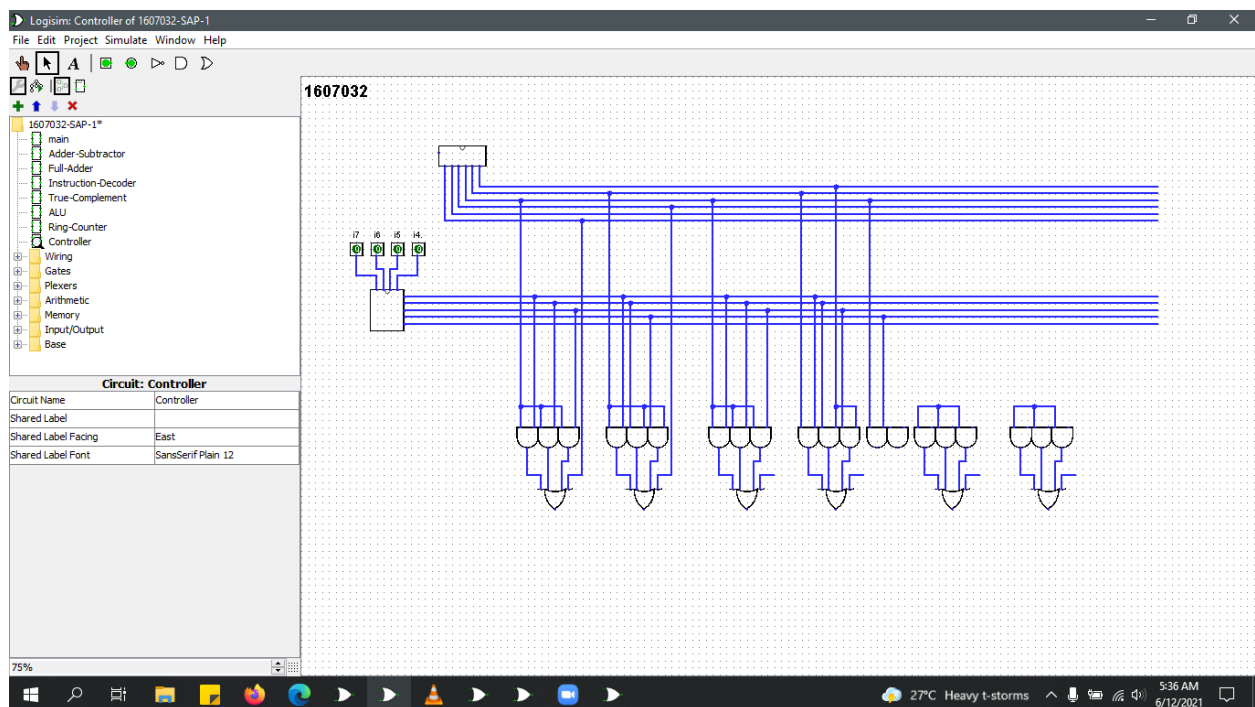
## 1607032 : Instruction Decoder:



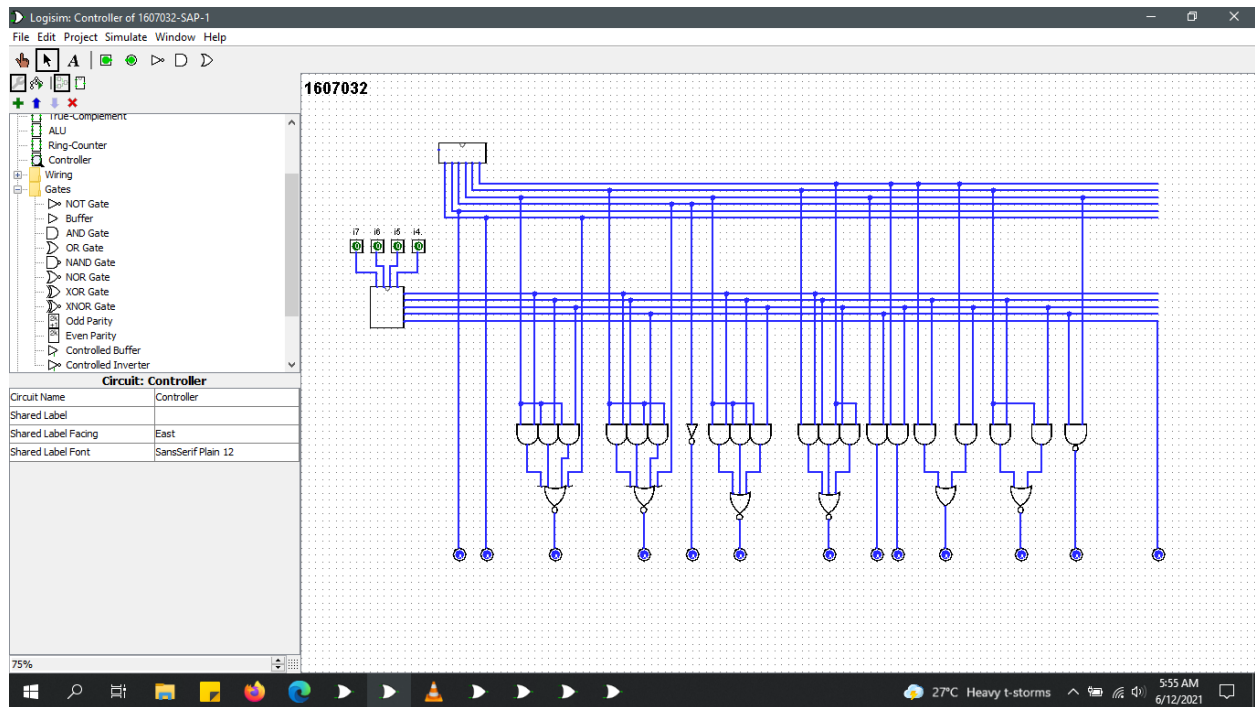
## 1607032 : Ring Counter



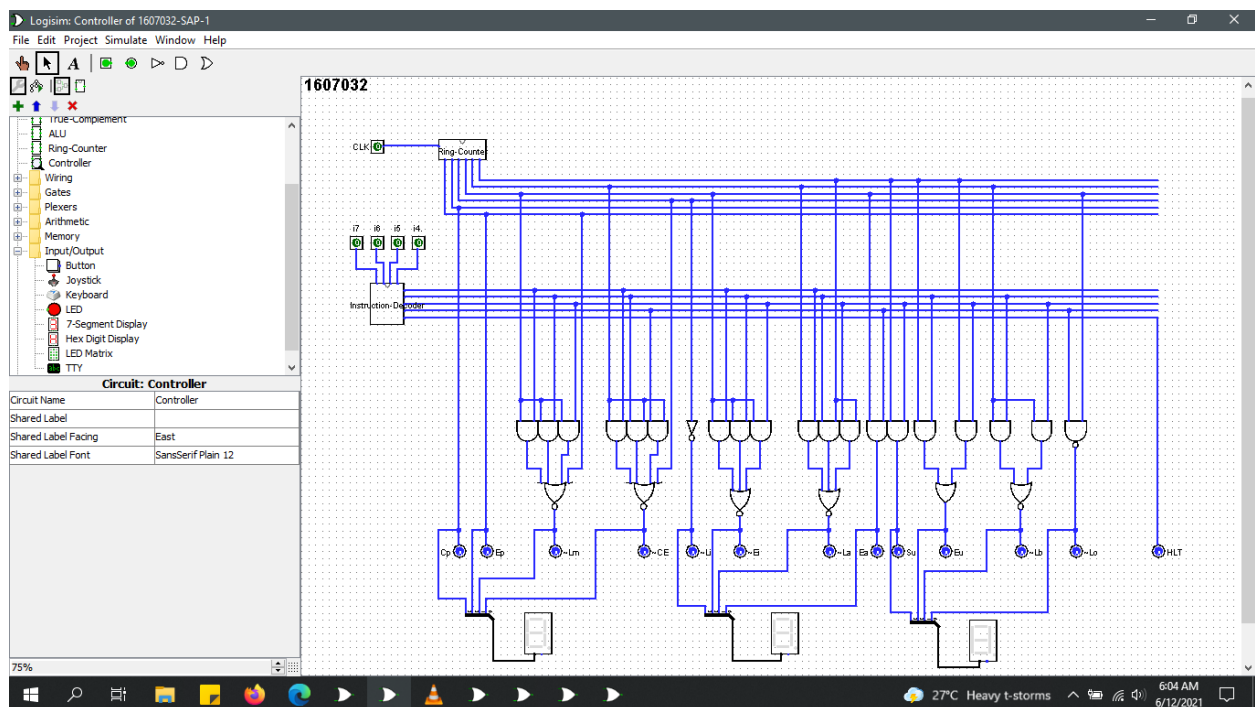
## 1607032 : Controller My part:



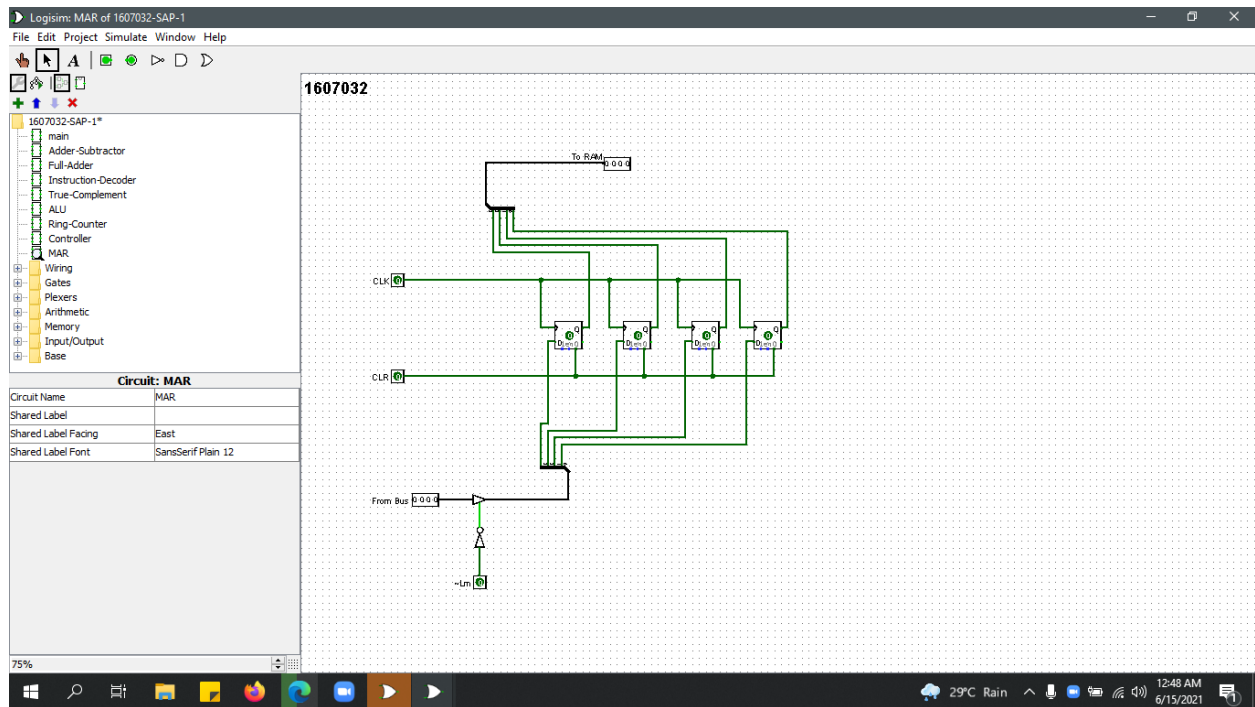
## 1607032 : Controller



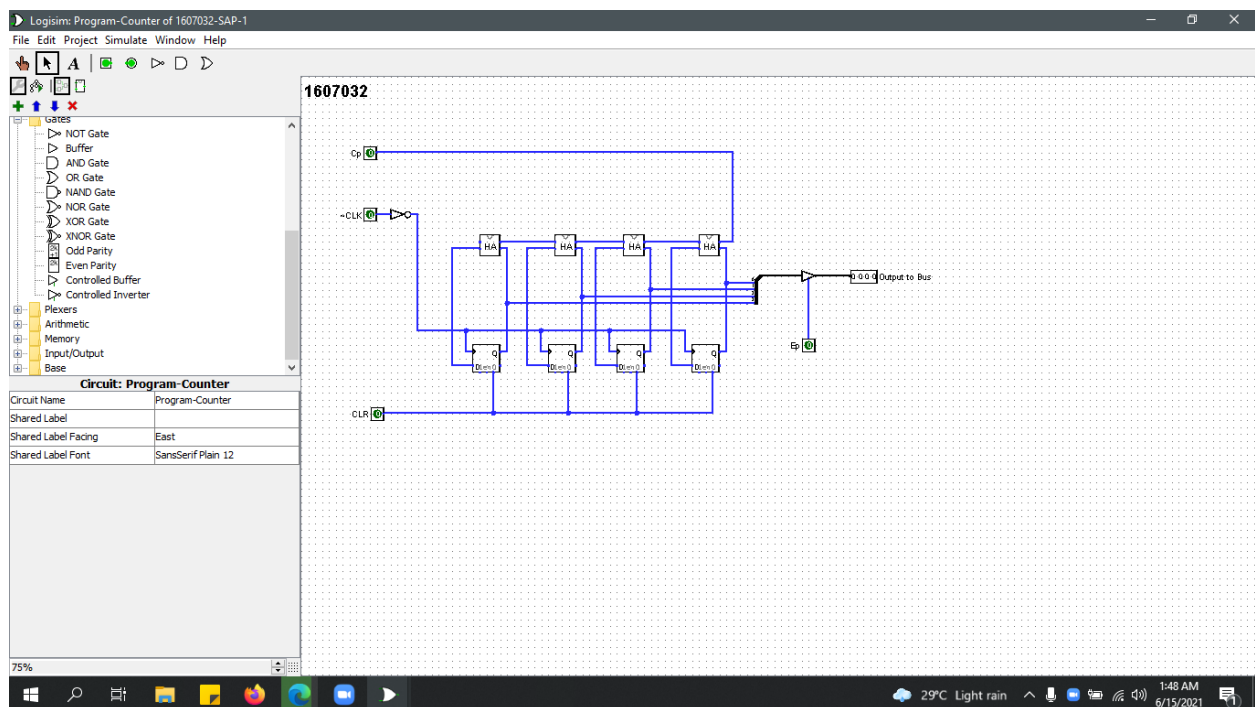
## 1607032 : Controller:



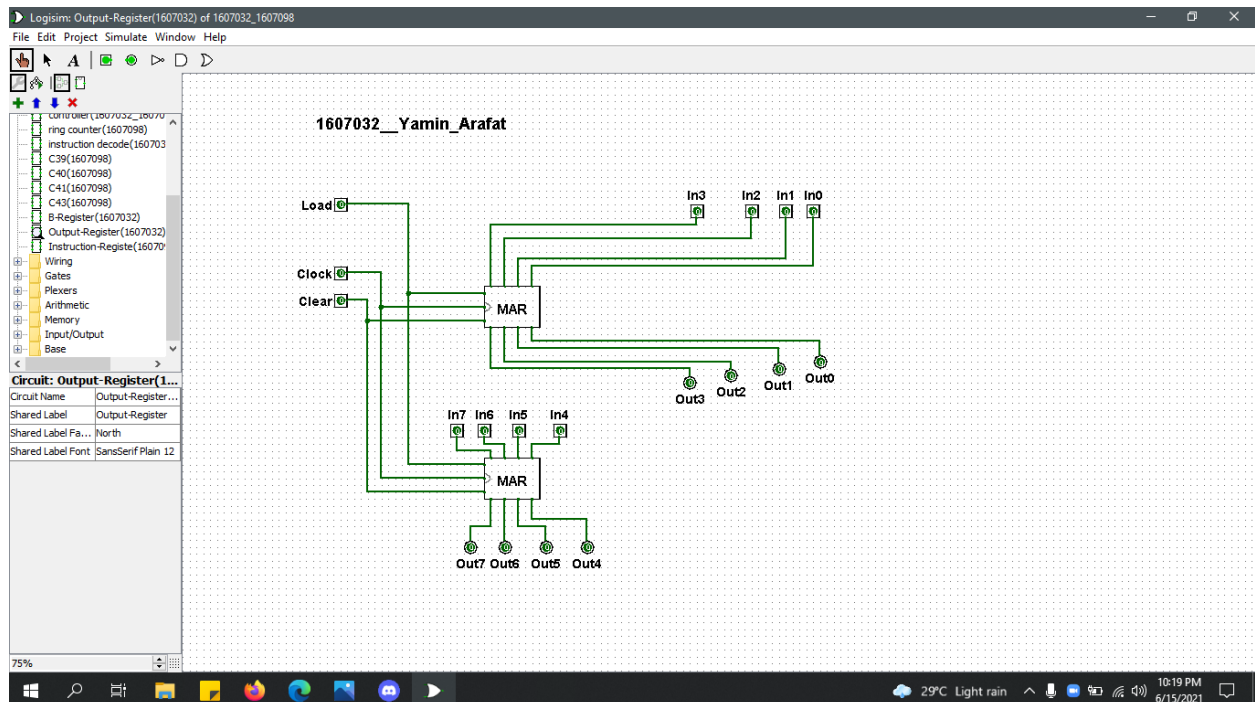
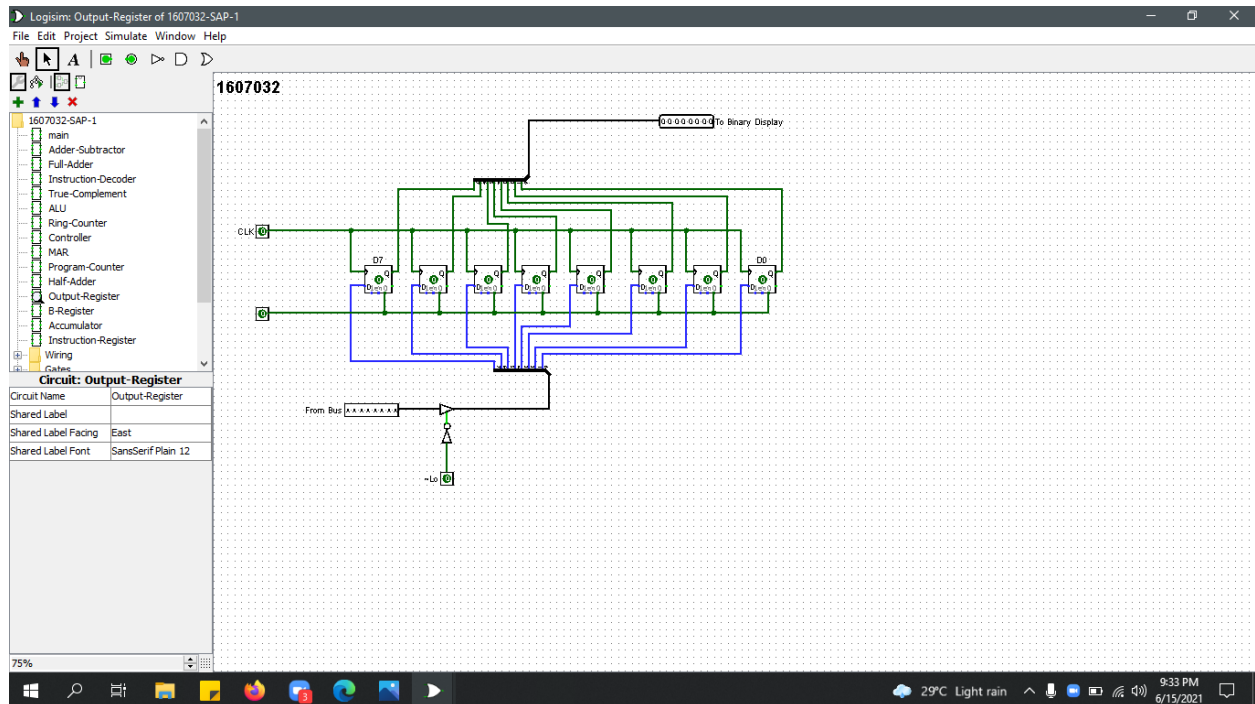
## 1607032 : Register



## 1607032 : B register:

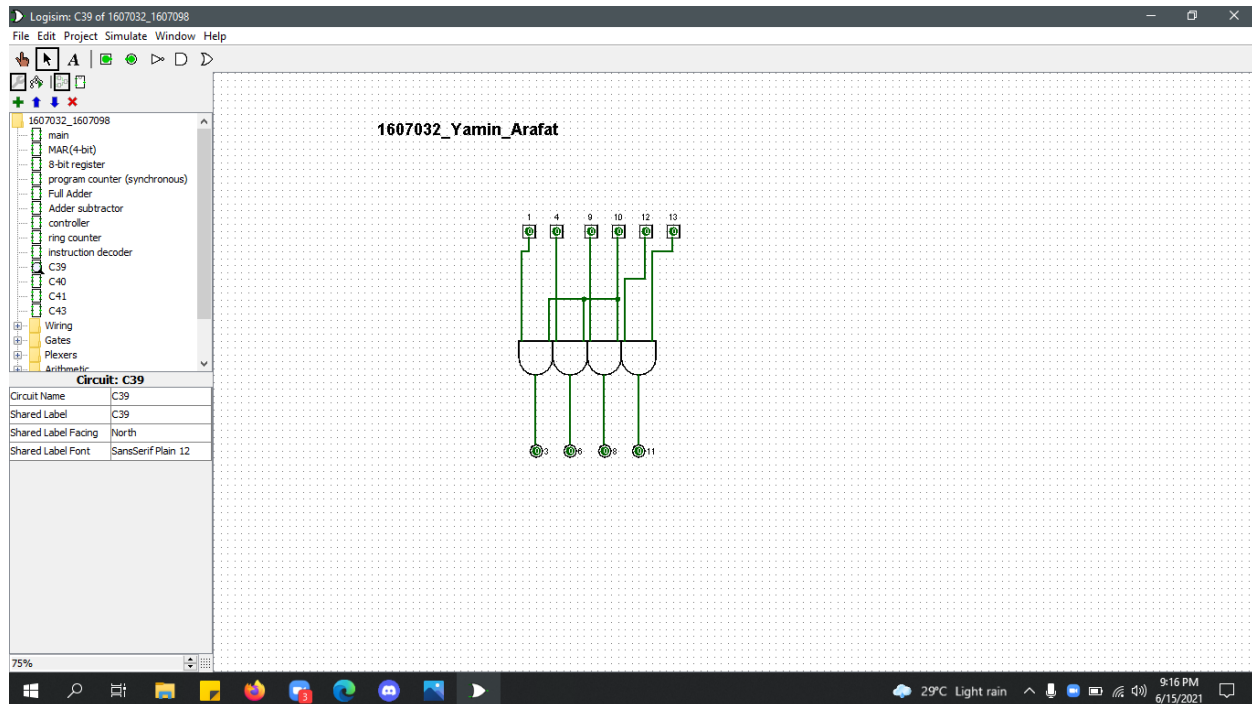


1607032 : Output register:

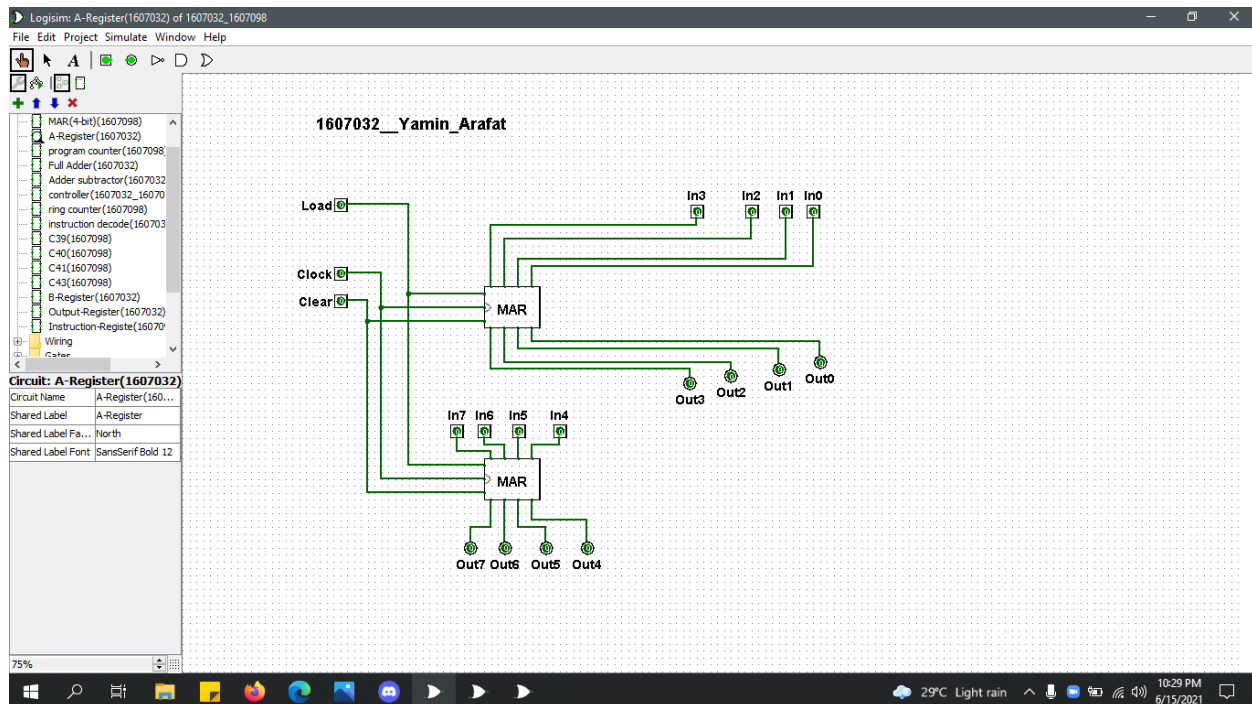




## 1607032: C39 Circuit



## 1607032 : A Register:



## 1607032 1607098: Final SAP – 1:

