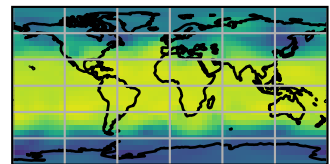
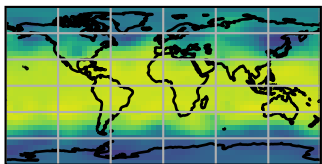


Z500, 2018-01-11 12:00:00 Initialisation

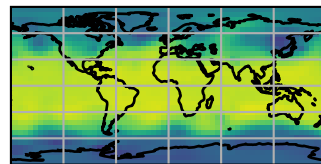
True t=0



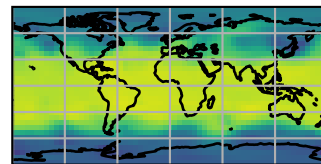
True t=1



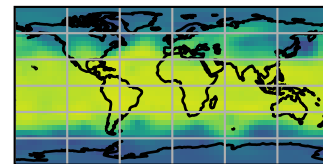
True t=2



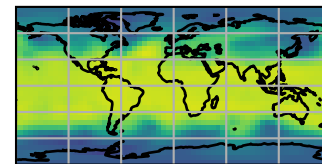
True t=3



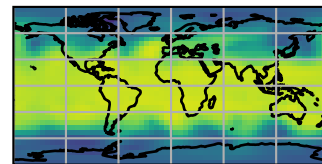
True t=4



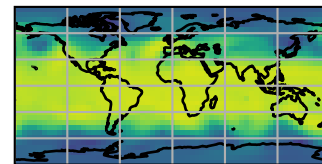
True t=5



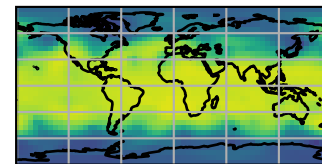
True t=6



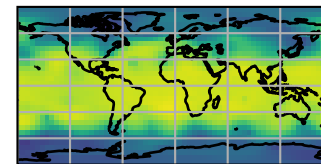
True t=7



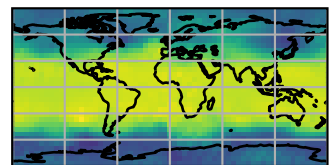
True t=8



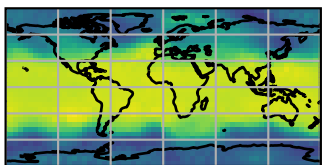
True t=9



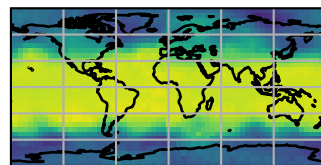
Pred t=0



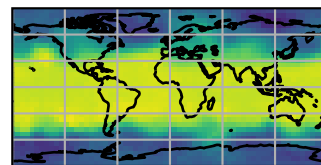
Pred t=1



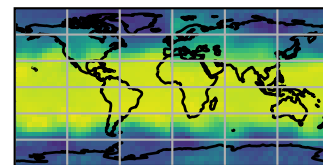
Pred t=2



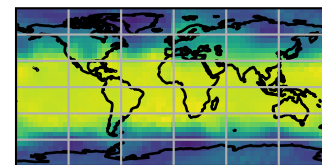
Pred t=3



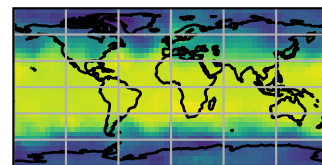
Pred t=4



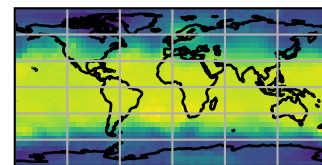
Pred t=5



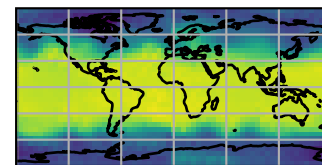
Pred t=6



Pred t=7



Pred t=8



Pred t=9

