Terms

SOC   - SYSTEM ON CHIP

IC   - integrated circuit

Foundry   - a factory

IP   - intellectual property

Macros   - series of commands that can be stored and run when ever you need to perform a task

RTL   - register transfer language

ASIC   - APPLICATION specific integrated circuit

GDSII   - graphic design system ll

EDA   - electronic design automation

SCL   - Semiconductor laboratory

PDK   - process design kit

Fabrication   - the process of creating integrated circuits on silicon wafers

Flop ratio   - DFF / number of cell

Utilisation factor   - area occupied by net list/ total area of core

Aspect ratio – height / width

Floor planning   - placement of IP's In a chip

DRC   - design rule checking

LVS   - layout vs schematic

Abutment   - a structure that connects cells by overlapping so that they form connecting wires

Threshold voltage (V_{th})- minimum voltage rewquored to turn on a MOSFET

MOSFET   - Metal oxide semiconductor field effect transistor

HPWL   - half perimeter wire length

PDA   - Physically designed automation

PnR   - place and route