



YatCC-Al 编译原理课程实验

中山大学

助教: 顾宇浩

2025/2/28

YatCC 团队敬上







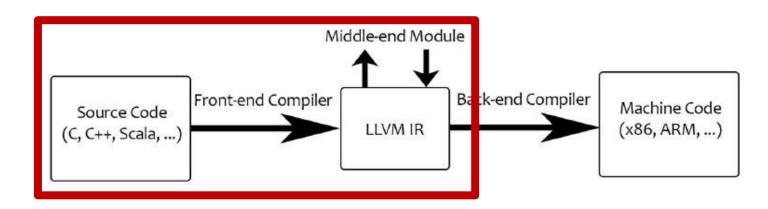
❖YatCC实验是基于 LLVM 的编译器构造实验

在本课程中,你需要:

➤ 使用LLVM工业级编译器基础设施 开发一个C语言子集的编译器

➤ 完成从C语言源代码到LLVM中间 表示(甚至是汇编代码)的转换过程

➤ 预估编码总量: 3000LOC左右





课程特色: 专业的授课团队





吴露



何静仪



顾宇浩



许宏鑫



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陈俊儒

23学硕...

黄鑫





韩云昊





课程特色:一站式集成实验平台



- https://yatcc-ai.com/ol.html
 - ➤ VS Code在线开发平台,基于我们自己的超算
 - > "配环境"的终极解决方案就是:不用配环境





HPC+AI多模式融合应用软件平台





课程特色: 多种打开方式

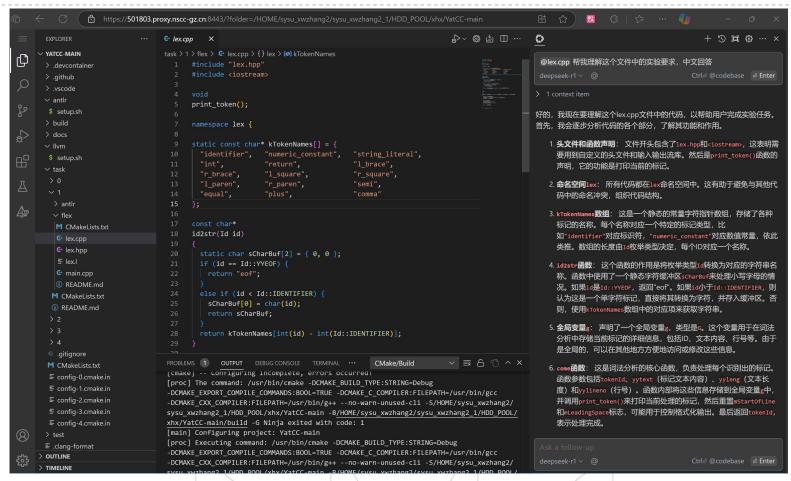


- ❖点击即用的一站式平台
 - https://yatcc-ai.com/ol.html
- ❖开箱即用的容器镜像
 - docker pull registry.cn-guangzhou.aliyuncs.com/ yhgu2000/yatcc:latest
- ❖配置自动化的代码仓库
 - pit clone
 https://github.com/arcsysu/YatCC.git



课程特色: DeepSeek AI大模型加持





*由于大模型需求火爆,中心资源紧张, AI服务会在课程过程中逐渐普及上线·····





课程特色:丰富的学习资源



❖Bilibili 教程

https://space.bilibili.com/ 3546650047941291



❖Github 文档

https://arcsysu.github.io/YatCC



❖LLVM 官方文档

https://llvm.org/docs/





课程特色: 学术竞赛&产业实践







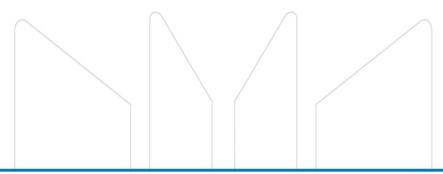








任务与考核







```
# 1 "./basic/000_main.sysu.c"
# 1 "<built-in>" 1
# 1 "<built-in>" 3
# 384 "<built-in>" 3
# 1 "<command line>" 1
# 1 "<built-in>" 2
# 1 "./basic/000_main.sysu.c" 2
int main(){
    return 3:
```

- 使用flex/antlr读取源代码,解析成TOKEN流
- 评分标准:识别足够的TOKEN 并识别位置

int 'int'

```
identifier 'main' [LeadingSpace] Loc=<./basic/0
l_paren '(' Loc=<./basic/000_main.sysu.c:1:9>
r_paren ')' Loc=<./basic/000_main.sysu.c:1:10>
l_brace '{' Loc=<./basic/000_main.sysu.c:1:11>
return 'return' [StartOfLine] [LeadingSpace] Lo
numeric_constant '3' [LeadingSpace] Loc=<./basic/000_main.sysu.c:1:11>
```

semi ';' Loc=<./basic/000_main.sysu.c:2:13>

eof '' Loc=<./basic/000_main.sysu.c:3:2>

r_brace '}' [StartOfLine] Loc=<./basic/000_main

[StartOfLine] Loc=<./basic/000_main.s

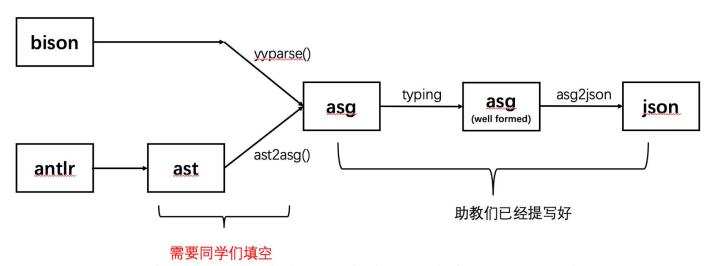


task2 (? 分)



```
int 'int' [StartOfLine] Loc=<./bas:
identifier 'main' [LeadingSpace] Lot
l_paren '(' Loc=<./basic/000_main.s
r_paren ')' Loc=<./basic/000_main.s
l_brace '{' Loc=<./basic/000_main.s
return 'return' [StartOfLine] [Leading
numeric_constant '3' [LeadingSpace]
semi ';' Loc=<./basic/000_main.syst
r_brace '}' [StartOfLine] Loc=<./basic/000_main.syst
cof '' Loc=<./basic/000_main.syst.syst
loc=<./basic/000_main.syst
loc=</basic/000_main.syst
loc=</basic/0000_main.sys
```

```
"name": "__int128_t",
19
20 >
           "type": { ···
22
            "inner": [
23
24
              "id": "0x55c47643ae90",
25
              "kind": "BuiltinType",
26
27 >
              "type": { ···
29
30
31
32
33
34
           "id": "0x55c47643b168",
           "kind": "TypedefDecl",
35
           "loc": {},
36
37 >
            "range": { ···
40
           "isImplicit": true,
41
           "name": " uint128 t",
42
```

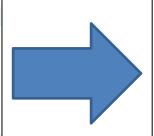




task3 (? 分)

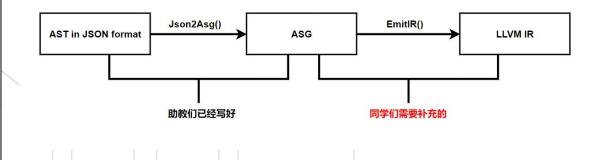


```
"kind": "TranslationUnitDecl",
       "loc": {}.
 4
       "range": { ···
       "inner": [
 9
10
            "id": "0x55c47643b0f8",
11
12
            "kind": "TypedefDecl",
            "loc": {},
13
14 >
            "range": { ···
17
            },
            "isImplicit": true,
18
           "name": "__int128_t",
19
20 >
            "type": { ···
22
            },
            "inner": [
23
24
                "id": "0x55c47643ae90",
25
                "kind": "BuiltinType",
26
27 >
                "type": { ···
29
30
31
32
33
            "id": "0x55c47643b168",
34
            "kind": "TypedefDecl",
35
            "loc": {},
36
            "range": { ···
37 >
40
            "isImplicit": true,
41
            "name": " uint128 t",
42
```



```
function Attrs: noinline nounwind optnone
Run Debug
define dso_local i32 @main() #0 {
  entry:
    %retval = alloca i32, align 4
    store i32 0, ptr %retval, align 4
    ret i32 3
}
```

- 使用LLVM库,将JSON语法树转换为LLVM IR
- 解析ASG的代码已由助教完成,节省工作量!
- 评分标准: 生成的LLVM IR能正确编译运行

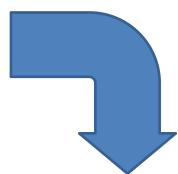




task4 (? 分)



```
; Function Attrs: noinline nounwind optnone
27
     Run Debug
     define dso local i32 @main() #0 {
28
29
     entry:
30
       %retval = alloca i32, align 4
       %sum = alloca i32, align 4
31
32
       %i = alloca i32, align 4
33
       %tmp = alloca i32, align 4
34
       %j = alloca i32, align 4
35
       store i32 0, ptr %retval, align 4
36
       store i32 0, ptr %sum, align 4
37
       store i32 0, ptr %i, align 4
38
       %call = call i32 (...) @ sysy getint()
39
       store i32 %call, ptr @loopCount, align 4
40
       call void @ sysy starttime(i32 noundef 21)
41
       br label %while.cond
42
     while.cond:
43
       %0 = load i32, ptr %i, align 4
44
45
       %1 = load i32, ptr @loopCount, align 4
46
       %cmp = icmp slt i32 %0, %1
       br i1 %cmp, label %while.body, label %while.end7
47
48
49
     while.body:
       store i32 0, ptr %tmp, align 4
50
51
       store i32 0, ptr %j, align 4
52
       br label %while.cond1
53
54
     while.cond1:
       %2 = load i32, ptr %j, align 4
55
       %cmp2 = icmp slt i32 %2, 6
56
57
       br i1 %cmp2, label %while.body3, label %while.end
58
59
     while.bodv3:
      %3 = load i32, ptr %i, align 4
60
      call void @func(i32 noundef %3)
61
62
       %4 = load i32, ptr %tmp, align 4
       %5 = load i32, ptr @global, align 4
       %add = add nsw i32 %4, %5
64
```

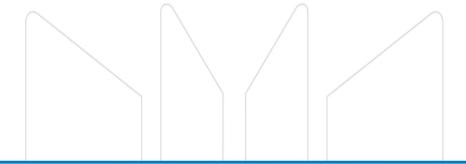


- 遵照LLVM框架要求, 编写LLVM Pass, 优化 LLVM IR
- · 评分标准: 你的程序和 clang -O1的相对性能

```
; Function Attrs: nounwind
     Run Debug
     define dso_local noundef i32 @main() local_unnamed_addr #1 {
       %call = tail call i32 (...) @_sysy_getint() #3
       store i32 %call, ptr @loopCount, align 4, !tbaa !2
21
       tail call void @_sysy_starttime(i32 noundef 21) #3
       %0 = load i32, ptr @loopCount, align 4, !tbaa !2
       %cmp17 = icmp sgt i32 %0, 0
24
       br i1 %cmp17, label %while.cond1.preheader, label %while.end7
25
     while.cond1.preheader:
                                                       ; preds = %entry, %while.cond1.preheader
       %indvars.iv = phi i32 [ %indvars.iv.next, %while.cond1.preheader ], [ 0, %entry ]
       %i.019 = phi i32 [ %add6, %while.cond1.preheader ], [ 0, %entry ]
       %sum.018 = phi i32 [ %rem, %while.cond1.preheader ], [ 0, %entry ]
       %div = udiv i32 %indvars.iv, 6
       %add5 = add nsw i32 %div, %sum.018
       %rem = srem i32 %add5, 134209537
       %add6 = add nuw nsw i32 %i.019, 1
       %indvars.iv.next = add i32 %indvars.iv, 6
       %exitcond.not = icmp eq i32 %add6, %0
       br i1 %exitcond.not, label %while.cond.while.end7_crit_edge, label %while.cond1.preheader, !llvm.loop !6
38
     while.cond.while.end7_crit_edge:
                                                       ; preds = %while.cond1.preheader
       %1 = add nsw i32 %0, -1
       store i32 %1, ptr @global, align 4, !tbaa !2
       br label %while.end7
                                                       ; preds = %while.cond.while.end7_crit_edge, %entry
       %sum.0.lcssa = phi i32 [ %rem, %while.cond.while.end7_crit_edge ], [ 0, %entry ]
       tail call void @_sysy_stoptime(i32 noundef 37) #3
       tail call void @_sysy_putint(i32 noundef %sum.0.lcssa) #3
      tail call void @_sysy_putch(i32 noundef 10) #3
       ret i32 0
```



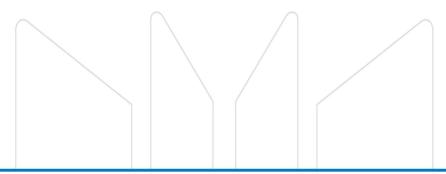








task0 演示&详解







预祝大家实验顺利!

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