

Net Length (mm)	FPGA Pin	FPGA Signal	JA1	FPGA Signal	FPGA Pin	Net Length (mm)
		VIO_OUT (+3.3V)	1	2		
	C19	VCCO_35	3	4	G14	
104.669	E18	IO_L5P_T0_AD9P_35	5	6	D20	104.650
104.652	E19	IO_L5N_T0_AD9N_35	7	8	D19	104.668
		GND	9	10		
104.658	F16	IO_L6P_T0_35	11	12	B20	104.654
104.663	F17	IO_L6N_T0_VREF_35	13	14	C20	104.677
		GND	15	16		
104.650	E17	IO_L3P_T0_DQS_AD1P_35	17	18	A20	104.672
104.673	D18	IO_L3N_T0_DQS_AD1N_35	19	20	B19	104.658
		GND	21	22		
104.063	F19	IO_L15P_T2_DQS_AD12P_35	23	24	G20	104.071
104.090	F20	IO_L15N_T2_DQS_AD12N_35	25	26	G19	104.083
		GND	27	28		
104.091	J20	IO_L17P_T2_AD5P_35	29	30	G18	104.085
104.066	H20	IO_L17N_T2_AD5N_35	31	32	G17	104.080
		GND	33	34		
104.092	J18	IO_L14P_T2_AD4P_SRCC_35	35	36	H17	104.081
104.057	H18	IO_L14N_T2_AD4N_SRCC_35	37	38	H16	104.058
		SMB_I2C_SCL0	39	40		

Net Length (mm)	FPGA Pin	FPGA Signal	JA2	FPGA Signal	FPGA Pin	Net Length (mm)
		VIO_OUT (+3.3V)	1	2		
	C19	VCCO_35	3	4	J15	
58.189	L14	IO_L22P_T3_AD7P_35	5	6	J16	58.195
58.203	L15	IO_L22N_T3_AD7N_35	7	8	K16	58.191
		GND	9	10		
58.198	M14	IO_L23P_T3_35	11	12	G15	58.193
58.193	M15	IO_L23N_T3_35	13	14	H15	58.203
		GND	15	16		
58.223	N15	IO_L21P_T3_DQS_AD14P_35	17	18	J14	58.213
58.201	N16	IO_L21N_T3_DQS_AD14N_35	19	20	K14	58.196
		GND	21	22		
101.835	L19	IO_L9P_T1_DQS_AD3P_35	23	24	J19	101.821
101.824	L20	IO_L9N_T1_DQS_AD3N_35	25	26	K19	101.819
		GND	27	28		
101.850	M17	IO_L8P_T1_AD10P_35	29	30	M20	101.842
101.841	M18	IO_L8N_T1_AD10N_35	31	32	M19	101.857
		GND	33	34		
101.841	L16	IO_L11P_T1_SRCC_35	35	36	K18	101.782
101.817	L17	IO_L11N_T1_SRCC_35	37	38	K17	101.841
		SMB_I2C_SCL1	39	40		

Net Length (mm)	FPGA Pin	FPGA Signal	JB1	FPGA Signal	FPGA Pin	Net Length (mm)
		VIO_OUT (+3.3V)	1	2	JB1_SMB_nINT	
	N19	VCCO_34	3	4	IO_25_34	T19
101.153	T11	IO_L1P_T0_34	5	6	IO_L2N_T0_34	U12
101.147	T10	IO_L1N_T0_34	7	8	IO_L2P_T0_34	T12
		GND	9	10	GND	
101.155	P14	IO_L6P_T0_34	11	12	IO_L4N_T0_34	W13
101.159	R14	IO_L6N_T0_VREF_34	13	14	IO_L4P_T0_34	V12
		GND	15	16	GND	
101.146	U13	IO_L3P_T0_DQS_PUDC_B_34	17	18	IO_L5N_T0_34	T15
101.159	V13	IO_L3N_T0_DQS_34	19	20	IO_L5P_T0_34	T14
		GND	21	22	GND	
57.498	T16	IO_L9P_T1_DQS_34	23	24	IO_L7N_T1_34	Y17
57.516	U17	IO_L9N_T1_DQS_34	25	26	IO_L7P_T1_34	Y16
		GND	27	28	GND	
57.498	W14	IO_L8P_T1_34	29	30	IO_L10N_T1_34	W15
57.519	Y14	IO_L8N_T1_34	31	32	IO_L10P_T1_34	V15
		GND	33	34	GND	
57.489	U14	IO_L11P_T1_SRCC_34	35	36	IO_L12N_T1_MRCC_34	U19
57.516	U15	IO_L11N_T1_SRCC_34	37	38	IO_L12P_T1_MRCC_34	U18
		SMB_I2C_SCL2	39	40	SMB_I2C_SDA2	

Net Length	FPGA Pin	FPGA Signal	JB2	FPGA Signal	FPGA Pin	Net Length
		VIO_OUT (+3.3V)	1	2	JB2_SMB_nINT	
	N19	VCCO_34	3	4	IO_0_34	R19
66.787	N17	IO_L23P_T3_34	5	6	IO_L24N_T3_34	P16
66.793	P18	IO_L23N_T3_34	7	8	IO_L24P_T3_34	P15
		GND	9	10	GND	
66.785	T17	IO_L20P_T3_34	11	12	IO_L19N_T3_VREF_34	R17
66.789	R18	IO_L20N_T3_34	13	14	IO_L19P_T3_34	R16
		GND	15	16	GND	
66.779	V17	IO_L21P_T3_DQS_34	17	18	IO_L22N_T3_34	W19
66.783	V18	IO_L21N_T3_DQS_34	19	20	IO_L22P_T3_34	W18
		GND	21	22	GND	
39.340	T20	IO_L15P_T2_DQS_34	23	24	IO_L18N_T2_34	W16
39.358	U20	IO_L15N_T2_DQS_34	25	26	IO_L18P_T2_34	V16
		GND	27	28	GND	
39.358	V20	IO_L16P_T2_34	29	30	IO_L17N_T2_34	Y19
39.336	W20	IO_L16N_T2_34	31	32	IO_L17P_T2_34	Y18
		GND	33	34	GND	
39.344	N20	IO_L14P_T2_SRCC_34	35	36	IO_L13N_T2_MRCC_34	P19
39.341	P20	IO_L14N_T2_SRCC_34	37	38	IO_L13P_T2_MRCC_34	N18
		SMB_I2C_SCL3	39	40	SMB_I2C_SDA3	

Net Length (mm)	FPGA Pin	FPGA Signal	JC1	FPGA Signal	FPGA Pin	Net Length (mm)
		VIO_OUT (+3.3V)	1 2	JC1_SMB_nINT		
	T8	VCCO_13	3 4	IO_L6N_T0_VREF_13	V5	
97.389	U7	IO_L11P_T1_SRCC_13	5 6	IO_L12N_T1_MRCC_13	U10	97.442
97.384	V7	IO_L11N_T1_SRCC_13	7 8	IO_L12P_T1_MRCC_13	T9	97.443
		GND	9 10	GND		
97.398	T5	IO_L19P_T3_13	11 12	IO_L20N_T3_13	Y13	97.262
97.394	U5	IO_L19N_T3_VREF_13	13 14	IO_L20P_T3_13	Y12	97.220
		GND	15 16	GND		
97.376	V11	IO_L21P_T3_DQS_13	17 18	IO_L22N_T3_13	W6	97.381
97.393	V10	IO_L21N_T3_DQS_13	19 20	IO_L22P_T3_13	V6	97.397
		GND	21 22	GND		
93.896	V8	IO_L15P_T2_DQS_13	23 24	IO_L18N_T2_13	Y11	93.881
93.862	W8	IO_L15N_T2_DQS_13	25 26	IO_L18P_T2_13	W11	93.865
		GND	27 28	GND		
93.869	U9	IO_L17P_T2_13	29 30	IO_L16N_T2_13	W9	93.866
93.884	U8	IO_L17N_T2_13	31 32	IO_L16P_T2_13	W10	93.875
		GND	33 34	GND		
93.884	Y9	IO_L14P_T2_SRCC_13	35 36	IO_L13N_T2_MRCC_13	Y6	93.877
93.873	Y8	IO_L14N_T2_SRCC_13	37 38	IO_L13P_T2_MRCC_13	Y7	93.872
		SMB_I2C_SCL4	39 40	SMB_I2C_SDA4		

Net Length (mm)	FPGA Pin	FPGA Signal	J3	FPGA Signal	FPGA Pin	Net Length (mm)
	A13	VCCO_MIO1_501 (+1.8V)	1 2	PS_MIO52_501	C10	
		NC	3 4	PS_MIO53_501	C11	
122.875	A19	PS_MIO16_501	5 6	PS_MIO19_501	D10	122.907
122.903	E14	PS_MIO17_501	7 8	PS_MIO18_501	B18	122.888
		GND	9 10	GND		
122.880	A17	PS_MIO20_501	11 12	PS_MIO23_501	D11	122.878
122.895	F14	PS_MIO21_501	13 14	PS_MIO22_501	B17	122.907
		GND	15 16	GND		
122.891	A16	PS_MIO24_501	17 18	PS_MIO27_501	D13	122.883
122.903	F15	PS_MIO25_501	19 20	PS_MIO26_501	A15	122.898
		GND	21 22	GND		
98.732	C16	PS_MIO28_501	23 24	PS_MIO31_501	E16	98.702
98.707	C13	PS_MIO29_501	25 26	PS_MIO30_501	C15	98.731
		GND	27 28	GND		
98.715	A14	PS_MIO32_501	29 30	PS_MIO35_501	F12	98.737
98.735	D15	PS_MIO33_501	31 32	PS_MIO34_501	A12	98.705
		GND	33 34	GND		
98.721	A11	PS_MIO36_501	35 36	PS_MIO39_501	C18	98.711
98.715	A10	PS_MIO37_501	37 38	PS_MIO38_501	E13	98.722
		SMB_I2C_SCL5	39 40	SMB_I2C_SDA5		

Net Length (mm)	FPGA Pin	FPGA Signal	J2	FPGA Signal	FPGA Pin	Net Length (mm)
	K9	VP_0	1 2	VN_0	L10	
		DAC_OUT1/ADC_IN4	3 4	DAC_OUT2/ADC_IN5		
		GND	5 6	GND		
		SMB_I2C_SCL7	7 8	SMB_I2C_SDA7		
		GND	9 10	GND		
		BT_AUD_CLK	11 12	BT_AUD_FSYNC		
		BT_AUD_OUT	13 14	BT_AUD_IN		
		MCU_SWCLK/PA13	15 16	MCU_SWDIO/PA14		
		MCU_nRST	17 18	MCU_VDDIO2 (+3.3V)		
	Zynq	nRST(PS_SRST_B_501)	19 20	GND		
	F9	TCK_0	21 22	TDI_0	G6	
	J6	TMS_0	23 24	TDO_0	F6	
	K6	VCCO_0 (+3.3V)	25 26	PGND		
		GND	27 28	GND		
		VIN (+3.7-17V)	29 30	VIN (+3.7-17V)		