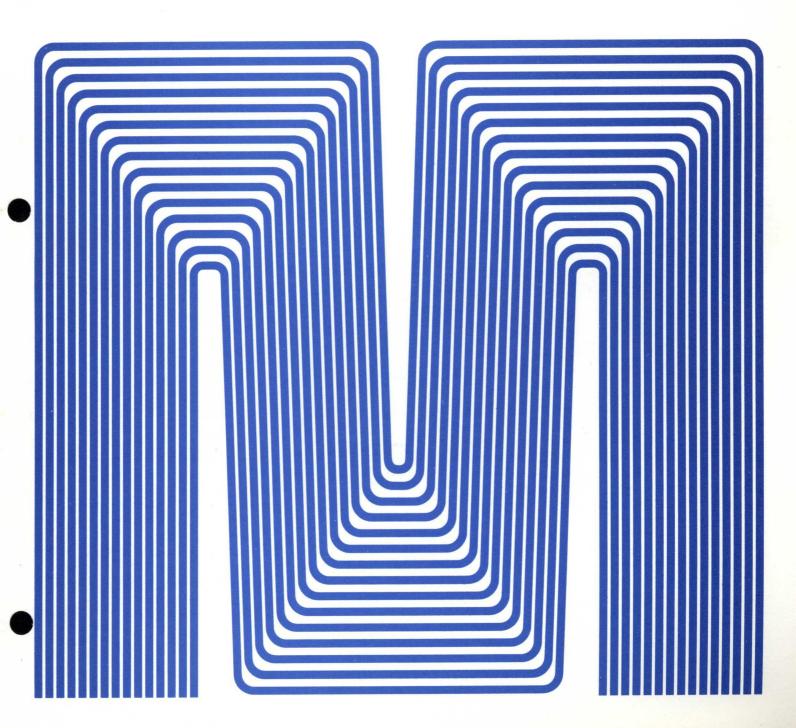
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Micro 810 Computer

AP810 Assembly Program



MICRO 810 COMPUTER

AP810 ASSEMBLY PROGRAM

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1. INTRODUCTION

AP810 is a symbolic assembly program for the MICRO 810 computer. The assembler provides for symbolic memory addressing, and mnemonics for machine and assembler instructions. This program is written in FORTRAN IV and may be adapted to many computer systems. The MICRO 810 source program is entered by punch cards and the output of the assembler includes an assembly listing and an object program card deck. This assembler generates non-relocatable programs.

The AP810 assembly language includes the following features:

Address Arithmetic – Decimal and hexadecimal numbers, symbolic addresses, and arithmetic expressions.

Data definitions - Variable precision integer, address expressions, character constants, and literals may be designated.

Listing control — The format of the listing may be controlled and comment cards included.

meiudet

Diagnostics – Diagnostics for source program errors are included in the output

listing.

2. SOURCE LANGUAGE

The source language is a sequence of symbolic instructions, called statements which are punched on cards. Each statement is punched on a single card. Each statement may consist of from one to four entries: a name field, an operation field, an operand field, and a comment field. Columns 73-80 are normally used for identification or sequence numbers. Entries in the operand field are expressions which may consist of decimal numbers, hexadecimal numbers and symbolic values.

STATEMENT FORMAT

Name Field

The name field entry is a symbol composed of from one to six characters starting with column I and terminating with the first blank. The first character of a symbol is alphabetic or period, subsequent characters may be alphabetic, numeric or a period. A name entry is usually optional and the type of instruction determines the legal content of the name field. The symbol takes on the current value of the assembler's location counter unless assigned another value by an assembler instruction. When an asterisk (*) appears in column I the remainder of the line is considered as comment and is not processed by the assembler except to place it on the listing.

Operation Field

The operation field entry is a mnemonic operation code specifying the machine or assembler instruction. The field begins in column 8 and is terminated by the first blank. Certain memory referencing instruction modes use special symbols suffixed to the mnemonic.

Operand Field

The operand field entries identify and describe data to be acted upon by instructions as, for example, memory locations, or literals. One or more operands may be written, depending on the needs of the instruction. Entries are separated by commas, and no blanks may appear in the field. The operand field may start with any column following the operation field, but must start by column 40. Column 14 is the normal starting column. It is terminated by the first blank column.

Comments Field

Comments describing the information about the program may be inserted between the end of the operand field and column 72. All characters, including spaces, may be used in writing a comment.

OPERAND FIELD EXPRESSIONS

Expressions in the operand field are made up of one or more terms which are connected by + and - arithmetic operators. No parenthetical expressions are allowed. Each term of the expression represents a value. Values may be assigned by the assembler program (symbols), or there may be inherent in the term itself, (constants). The range of values depends on the operand and the instruction. Address expressions for relative type addressing are written as if they are not relative. The assembler will convert these expressions to a relative displacement.

Symbols

A symbol is composed of one to six characters. The first character must be alphabetic or period, subsequent characters may be numeric, alphabetic or period. Imbedded blanks are not allowed and the assembler stops scanning the symbol with the first character which is not alphanumeric or a period. All symbols, except the special symbols * and **, used in an operand field must be defined by a single appearance in the name field of statement within the program.

Special Symbols

The special symbol, *, represents the momentary value of the assemblers location counter. It may be used as any other symbol in an expression but must never appear in the name field. When used in the operand field of a multi-byte instruction it will assume the value of the address of the first byte of the instruction.

The special symbol, **, may be used to indicate that the field will be changed during program execution. Normally it is used by itself without any other terms in the expression. The symbol yields a zero value and the byte length is governed by the usage.

Constants

The values of the constant terms are not assigned by the assembler program, but are inherent in the terms. There are two types of constant terms: decimal and hexadecimal.

a. Decimal Constant

A decimal constant is an unsigned decimal number. The value must be less than 65,536.

b. Hexadecimal Constant

A hexadecimal constant is an unsigned hexadecimal number of up to four characters written as a sequence of hexadecimal digits. The digits are enclosed in single quotation marks and preceded by the letter X. Each hexadecimal digit represents a four-bit binary number. The characters A through F are used to identify the hexadecimal integers 10 through 15.

3. MACHINE INSTRUCTIONS

Machine language instructions occupy one to five bytes of memory. The first byte contains the operation code and addressing mode code. The instruction is expressed in the assembly language by a three character mnemonic code in the operation field. Memory referencing instructions have eight addressing modes which occupy eight consecutive instruction codes. The addressing mode is expressed by the value of the address expression in the operand field and by a special symbol suffixed to the mnemonic operation code.

If the instruction is a memory referencing literal (mode 7) type, the second through fifth bytes of the instruction hold a literal which is expressed as a constant in the operand field. The types of constants and the way in which they are assembled is described later in this section under Mode 7. In general the literals are written and assembled in a manner similar to the DC assembler instruction.

Non-literal memory referencing instructions require 0, 1, or 2 bytes following the first byte to hold an address. Modes which make use of one byte for address contain an eight bit address of the first 256 memory locations (page 0), an eight bit signed displacement, or an 8 bit unsigned bias. Modes which make use of two bytes for the address hold a 16-bit address word with an index flag in bit 15.

The shift instructions use the second byte of the instruction to hold an eight bit shift count. The conditional jump instructions use the second byte to hold an eight bit signed displacement.

Examples of the method of writing machine instructions in the assembly language are shown in the sample listing at the end of section 5.

MEMORY ADDRESSING MODES

Each memory referencing instruction has eight addressing modes. The operation of the computer in each of these modes is explained in the MICRO 810 Computer Reference Manual. All addresses for modes 0-3 are written without regard for the mode to be used. The assembler will convert to relative addresses if the evaluated address is not in page 0.

Mode 0: Direct Page 0

When the evaluated address in the operand field is less than 256 and the mnemonic operation code is suffixed with a blank, the instruction is assembled as mode 0, and an eight bit address is placed in the second byte of the instruction.

Mode 1: Direct Relative

When the evaluated address in the operand field is greater than or equal to 256 and the mnemonic operation code is suffixed with a blank, the instruction is assembled as mode 1. If the address minus the location of the first byte of the instruction minus two is in the range +127 to -128 this value is assembled into the second byte of the instruction; otherwise a zero value is assembled and an 'R' diagnostic flag is placed in the listing.

Mode 2: Indirect Page 0

When the evaluated address in the operand field is less than 256 and the mnemonic operation code is suffixed with an *, the instruction is assembled as mode 2, and an eight bit address is placed in the second byte of the instruction.

Mode 3: Indirect Relative

When the evaluated address in the operand field is greater than or equal to 256 and the mnemonic operation code is suffixed with an *, the instruction is assembled as mode 3. If the address minus the location of the first byte of the instruction minus two is in the range +127 to -128 the value is assembled into the second byte of the instruction; otherwise a zero value is assembled an 'R' diagnostic flag is placed in the listing.

Mode 4: Indexed

When the instruction mnemonic operation code is suffixed with -, the instruction is assembled as mode 4 in a single byte instruction. The operand field is disregarded.

Mode 5: Indexed With Bias

When the instruction mnemonic operation code is suffixed with a +, the instruction is assembled as mode 5. If the value of the operand is less than 256 it is assembled into the second byte of the instruction; otherwise a zero value is assembled and an 'A' diagnostic flag is placed in the listing.

Mode 6: Extended Address

When the instruction mnemonic operation code is suffixed with a /, the instruction is assembled as mode 6. The value of the first operand is assembled as a 16-bit address in the second and third bytes of the instruction. If the character X preceded by a comma, is entered as a second operand the index flag in bit 15 of the address word is set to a 1-bit.

Mode 7: Indirect Extended Address (Jump and Return Jump)

When the instruction mnemonic operation code is suffixed with an =, the instruction is assembled as mode 7. The value of the first operand is assembled as a 16-bit address in the second and third bytes of the instruction. If the character X, preceded by a comma, is entered as a second operand the index flag in bit 15 of the address word is set to a 1-bit.

Mode 7: Literal

When the instruction mnemonic operation code is suffixed with an =, the instruction is assembled as mode 7 and the value of the operand field is assembled into the 1-4 bytes following the first byte. The length of a literal for the variable word length operand instructions (third character of mnemonic is V) depends on the literal type or the number of digits or characters. The programmer must assure that the length of these operands is consistent with the word length mode in effect. Instruction which are fixed word length operand type always assemble a two byte literal. A literal is enclosed in single quotation marks and is preceded by a single type identifier character. A literal without a type identifier and quotes is evaluated as a 16-bit address type.

H - Half Word Integer Literal

A half word integer constant is written as a signed or unsigned decimal number in the range +127 to -128. The number is converted to its binary equivalent and assembled into a single byte if a variable operand length instruction otherwise it is assembled into a two byte operand and right justified with leading zeros.

F - Full Word Integer Literal

A full word integer constant is written as a signed or unsigned decimal number in the range $+2^{15}-1$ to -2^{15} . The number is converted to its binary equivalent and assembled in two eight bit bytes.

E - Extended Word Integer Literal

An extended word integer constant is written as a signed or unsigned decimal number in the range $+2^{23}-1$ to -2^{24} . The number is converted to its binary equivalent and assembled in three eight bit bytes if the instruction is a variable operand length type; otherwise two bytes of zero are assembled and an 'A' diagnostic is placed in the listing.

D - Double Word Integer Literal

A double word integer constant is written as a signed or unsigned decimal number in the range $+2^{3}$ 1 -1 to -2^{3} 1 . The number is converted to its binary equivalent and assembled in four eight bit bytes if the instruction is a variable length type; otherwise two bytes of zero are assembled and an 'A' diagnostic is placed in the listing.

A - Address Constant

A 16-bit address constant is written as an expression consisting of decimal numbers, hexadecimal numbers and symbols. The value of the expression must be in the range 0 to 65535 and any symbol which is undefined wili result in a diagnostic error flag and a zero value. If the location counter symbol * appears in the address expression it will assume the value of the address of the first byte of the instruction. The index flag in bit 15 of the address word is set to a 1-bit by following the expression with a second operand consisting of the character X. The two operands are separated by a comma. An address constant may also be written without the A type identifier and quotes.

X - Hexadecimal Literal

A hexadecimal constant consists of one to eight hexadecimal digits (0-9 and A-F). If the instruction is a variable word length operand type, the number of bytes assembled is determined by the number of digits. When the number of digits is odd a leading zero is added. If the instruction is a fixed word length type, the constant is right justified with leading zeros in two eight-bit bytes, and when the number of digits is greater than four, an 'A' diagnostic is placed in the listing.

C - Character Literal

A character constant consists of a sequence of one to four characters, excluding the single quotation mark. Each character is converted to its ASCII value which is assembled as a single eight bit byte. If the instruction is a fixed word length type an 'A' diagnostic and zero value constant occurs if the number of characters is greater than two; if only a single character is included it is assembled right justified with leading zeros in two eight-bit bytes.

4. ASSEMBLER INSTRUCTIONS

Seven assembler instructions are included for control of the assembly process and the output listing.

ORG - Set Location Counter

The ORG assembler instruction alters the setting of the location counter. The name field entry, if any, will be assigned the value of the program counter after it is altered. The operand field of ORG must contain an expression whose value will be placed in the location counter. All symbols in the expression must have been previously defined when the instruction is first encountered. The next instruction which places object code in the program is forced to begin a new object card.

EQU - Equate Symbol

The EQU assembler instruction is used to define a symbol by assigning to it the value of the operand field. Any symbols appearing in the expression must have been previously defined when the instruction is first encountered. A name field entry must be present.

SET - Set Symbol

The SET assembler instruction assigned the value of the operand field to the symbol in the name field. Any symbols appearing in the operand field must have been previously defined when the instruction is first encountered. A name field entry must be present and may be a symbol previously defined. A multi-defined error cannot occur.

DC – Define Constant

The DC assembler instruction is used to provide constant data in memory. Each statement specifies only one constant. The constants which may be specified are:

8-bit decimal, 16-bit decimal, 24-bit decimal, 32-bit decimal, address, hexadecimal, and character.

A constant is enclosed in single quotation marks and is preceded by a single type identifier character. A constant without a type identifier and quotes is evaluated as a 16-bit address type. The method of expressing constants for the DC instruction is the same as for literals with the variable word length memory referencing instructions. The seven constant identifiers and a description of each follows:

H - Half Word Integer Constant

A half word integer constant is written as a signed or unsigned decimal number in the range +127 to -128. The number is converted to its binary equivalent and assembled as a single eight-bit byte.

F - Full Word Integer Constant

A full word integer constant is written as a signed or unsigned decimal number in the range $+2^{15}-1$ to -2^{15} . The number is converted to its binary equivalent and assembled in two eight-bit bytes.

E - Extended Word Integer Constant

An extended word integer constant is written as a signed or unsigned decimal number in the range $+2^{23}-1$ to -2^{24} . The number is converted to its binary equivalent and assembled in three eight-bit bytes.

D - Double Word Integer Constant

A double word integer constant is written as a signed or unsigned decimal number in the range $+2^{31}-1$ to -2^{31} . The number is converted to its binary equivalent and assembled in four eight-bit bytes.

A - Address Constant

A 16-bit address constant is written as an expression consisting of decimal numbers, hexadecimal numbers and symbols. The value of the expression must be in the range 0 to 65535 and any symbol which is undefined will result in a diagnostic error flag and a zero value. If the location counter symbol * appears in the address expression it will assume the value of the address of the first byte of the instruction. The index flag in bit 15 of the address word is set to a 1-bit by following the expression by a second operand consisting of the character X. The two operands are separated by a comma. An address constant may also be written without the A type identifier and quotes.

X - Hexadecimal Constant

A hexadecimal constant consists of one to eight hexadecimal digits, (0-9 and A-F). The number of bytes assembled is determined by the number of digits. If the number of digits is odd a leading zero is added.

C - Character Constant

A character constant consists of a sequence of characters, excluding the single quotation mark. The constant must be terminated by column 72. Each character is converted to its ASCII value and assembled as a single eight bit byte

DS - Define Storage

The DS instruction is used to reserve areas of memory and to assign names to these areas. The value of the operand field specifies the number of bytes to be reserved. A zero value in the operand field caused no bytes to be reserved, but causes the symbol in the name field to take on the current value of the location counter. No object code is assembled into the binary program and the next instruction which does place code in the program is forced to begin a new object card.

IDENT - Program Identification

The IDENT assembler instruction is used to identify the start of a program and to supply the program name which is located in the operand field. The IDENT must be the first statement in a source program.

END – End Assembly

The END assembler instruction terminates the assembly of a program and must be the last statement in a source program.

SPACE - Space Listing

The SPACE assembler instruction causes one or more blank lines to be inserted into the listing. The name field is disregarded by the assembler. The operand field contains an expression specifying the number of blank lines. If the spacing is beyond the end of the current page, the listing begins at the top of the next page.

EJECT – Start New Listing Page

The EJECT instruction causes the next line of the listing to appear at the top of the next page. The name and operand fields are disregarded by the assembler.

5. ASSEMBLY LISTING

The output listing from AP810 contains the memory address, and contents ofwords in the object program. The source statement is printed side-by-side with the object code.

FORMAT

Printer Columns	Contents
5 - 8	Error flags
11 — 14	Memory location of first byte of instruction, constant or reserved memory. Memory addresses are not printed for SET and EQU assembler instructions.
17 – 18	Instruction operation code. This field is blank for assembler instructions.
19 — 26	Operand
32 - 111	Source statement

ERROR FLAGS

A Address Error

This error occurs when an address expression in the operand field is incorrectly written or the value is out of range for one of the operands. An error flag will occur for each operand in error or out of range.

M Multidefined Symbol Error

This error occurs when the symbol in the name field has been previously defined by appearing in the name field of another instruction.

N Name Field Error

This error flag occurs when the symbol in the name field starts with a character other than alphabetic or period, or contains a non alphanumeric or non period character.

O Operation Mnemonic Error

This error occurs when the assembler does not recognize the contents of the operation field starting in column 8. A two byte zero value is assembled to allow patching.

R Range Error

This error occurs when the operand address of a relative addressing mode memory reference instruction or a Conditional Jump instruction is not in the range of +127 to -128 bytes from the location of the next instruction.

U Undefined Symbol Error

This error occurs when the symbol encountered in an expression of the operand field is not defined by an appearance in the name field.

SAMPLE LISTING

The sample listing on the next page shows the format of the listing and provides examples of how to write each instruction type, literals, constants, and assembler instructions. The six types of error conditions are also illustrated.

```
AP810 VERSION 2.1
                                  IDENT SAMPLE
                          * THIS SAMPLE PROGRM SHOWS HOW TO WRITE EACH INSTRUCTION TYPE
                          ** CONTROL INSTRUCTIONS
   0000
         0.0
                          START
                                 HI T
                                                          NO OPERAND FIELD FOR CONTROL INSTRUCTIONS
   0001
         34
                                  NOP
                          ** CONDITIONAL JUMP INSTRUCTIONS
   0002
         1000
                          CJ
                                  JOV
                                        #+2
                                                          A ZERO DISPLACEMENT ADDRESSES NEXT INST
   0004
         11FC
                                        2
                                  JAZ
                                                          A NEGATIVE DISPLACEMENT
   0006
         1EFA
                                  NAB CJ
                                                          OPERAND FIELD STARTS AFTER MNEMONIC
   0008
         1F00
                                 NAX
                                        *+300
                                                          THIS OPERAND ADDRESS IS OUT OF RANGE
   0 0 0 A
         0\ 0\ 0\ 0
0
                                 SSS
                                        CJ+2
                                                          ILLEGAL MNEMONIC OPERATION CODE
                          ** SHIFT INSTRUCTIONS
   0000
         2005
                          ALPHA
                                 LLA
                                        5
   000E
         2200
                                 LLL
                                        -5
                                                          NEGATIVE OPERANDS ARE NOT ALLOWED FOR SHIFT
   0010
         2810
                                        Y 1 1 1 1
                                 A I A
                                                          SHIFT COUNT MAY BE EXPRESSION
   0012
         2FOC
                          ALPHA
                                        TEN+2
                                                          SYMBOL IN NAME IS MULTI-DEFINED
                                 ARL
   0014
         2800
                                 ALA
                                                          ** INDICATES TO BE CHANGED DURING EXECUT.
                          ** INPUT/OUTPUT
   0016
         3000
                          BETA
                                                          NO OPERAND NEEDED FOR SERIAL I/O
                                 IBS
                                                         DEVICE ORDER, DEVICE NUMBER
   0018
         3142
                                 TBA
                                        2,2
         38848146
   001A
                                        4,4,BUF,X
                                 OBM
                                                          MEMORY I/O WITH INDEX FLAG - X
                          ** REGISTER OPERATE
   001E
         41
                                 XRA
                                                         NU OPERAND FOR REGISTER INSTRUCTIONS
                          ** NON LITERAL MEMORY REFERENCING INSTRUCTIONS
  001F
         E01F
                          123456 LNA
                                                         MODE 0 SYMBOL IN NAME IS NOT LEGAL
                                 ORG
                                        256
                                                         ORG ASSEMBLER INSTRUCTION
   0100
         F100
                          . DOT
                                 STA
                                        #+2
                                                         MODE 1
  0102
         8200
                                 LDX*
                                        OUPS
                                                         MODE 2
                                                                   SYMBOL IN OPERAND IS UNDEFINED
   0104
         8345
                                        BUF+5
                                                         MODE 3
                                 L [] X *
   0106
         8 C
                          A23456 STX-
                                                         MODE 4
                                                                  NO OPERAND FIELD FOR THIS MODE
   0107
         AD1E
                                 ADV+
                                        30
                                                         MODE 5
   0109
         96813A
                                 MIII /
                                        SAMAX
                                                         MODE 6
                                                                  WITH ADDRESS FLAG
   010C
         EF013A
                                 L D V Z
                                        SAM
                                                         MODE 6
                                                                  NO INDEX FLAG
                          ** FIXED WORD LENGTH LITERAL MODE INSTRUCTIONS (MODE 7)
   010F
         F700FF
                                 LDA=
                                       H'-1'
                                                         TWO BYTE LITERAL WITH FIRST BYTE ZERO
  0112
         F70000
                         SPOT
                                 SIA=
                                                         A REG. STORED IN SPOT+1
  0115
         970000
                                       E'100'
                                                         E AND D TYPES ARE NOT LEGAL IN FIXED WORD
                                 MUL=
  0118
         E7000C
                                        A ' A | PHA '
                                                         ADDRESS TYPE WITH NO INDEX FLAG
                                 I D A =
  011B
         E7800C
                                 LDA=
                                        A'ALPHA,X'
                                                         ADDRESS TYPE WITH INDEX FLAG - X
  011E
         87000C
                                 LDX =
                                       ALPHA
                                                         ADDRESS TYPE LITERAL WITHOUT A
  0121
         C700C1
                                 CPA=
                                       C'A'
                                                         TWO BYTE LITERAL WITH FIRST BYTE ZEROS
  0124
         D700FF
                                 4 N A =
                                       X'FF'
                                                         TWO BYTE LITERAL WITH HEX RIGHT JUSTIFIED
                            VARIABLE
                                      WORD LENGTH LITERALS (MODE 7)
  0127
         EF013A
                                 LDV=
                                            SAM
                                                         TWO BYTE ADDRESS LITERAL
         AF000003FF
  0124
                                 ADV =
                                       D'1023'
                                                         FOUR BYTE INTEGER
  012F
         DF09ABCDEF
                                 ANV =
                                       X'9ABCDEF'
                                                         FOUR BYTE, LENGTH DETERMINED BY COUNT
                                       C'A'
  0134
         CFC1
                                 CPV=
                                                         SINGLE BYTE LITERAL, LENGTH DET. BY COUNT
                         ** ASSEMBLER INSTRUCTIONS
           8000
                         TEN
                                 EQU
                                                         SYMBOL IS EQUATED TO 8
           000A
                         TEN
                                 SET
                                       10
                                                         SET DOES NOT ALLOW MULTI-DEFINED ERROR
  0136
           800B
                                 DC
                                        A'TEN+2-1,X'
                                                         ADDRESS CONSTANT
                                                         ADDRESS CONSTANT
  0138
           800B
                                       TEN+2-1,X
                                 DC
           D4C8C9D3
  0134
                                       C'THIS IS A DC'
                         SAM
                                 DC
  0146
                         BUF
                                 DS
                                                         FOUR BYTES ARE RESERVED
  014A
           0000
                                 DC
                                                         WORD TO BE CHANGED DURING EXECUTION
                         ** JUMP AND RETURN JUMP INSTRUCTIONS
                                                         MODE 6 TWO BYTE EXTENDED ADDRESS
MODE 7 TWO BYTE INDIRECT EXTENDED ADDRESS
  014C
         66000C
                                 JMP/
                                       ALPHA
  014F
         67800C
                                 JMP=
                                       ALPHA.X
  0152
           0000
                                 END
                                       ALPHA
                                                         OPERAND ADDRESS IS EXECUTE ADDRESS
```

FIGURE 1. SAMPLE LISTING

6. OBJECT PROGRAM CARD DECK

The AP810 assembly program generates a deck of cards which contain the binary object code. All information punched on the cards is in Hollerith code, with a single hexadecimal digit (four binary bits) punch in each column. This format allows easy visual reading of the cards after they are interpreted and permits rapid patching or generation of patches to the deck.

The cards have two fields as follows:

Load or execution addressObject code, two columns per byte Column 1-4

Columns 5-80

The card is terminated with the first blank column. If the card is blank except for an address in columns 1-4 the address is an execution address where the loader will transfer when the card is encountered. DS and ORG assembler instructions cause new cards to be started since a new load address is required. No information is loaded for the memory locations reserved by a DS instruction.

APPENDIXES

APPENDIX A

MICRO 810 INSTRUCTIONS

Operation Code	Mnemonic	Operand	Instruction Name
Control			
00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F 34	HLT TRP ESW PMP DIN EIN DRT ERT RO1 RO2 RO3 RO4 SO1 SO2 SO3 SO4 NOP		Halt Trap Enter Sense Switches Protect Memory Page Disable Interrupt System Enable Interrupt System Disable Real Time Clock Enable Real Time Clock Reset Overflow and Set Word Length to 1 Reset Overflow and Set Word Length to 2 Reset Overflow and Set Word Length to 3 Reset Overflow and Set Word Length to 4 Set Overflow and Set Word Length to 1 Set Overflow and Set Word Length to 1 Set Overflow and Set Word Length to 2 Set Overflow and Set Word Length to 3 Set Overflow and Set Word Length to 3 Set Overflow and Set Word Length to 4 No Operation
Conditional .	Jump		
10 11 12 13 14 15 16 17 18 19 1A 1B 1C 1D 1E 1F	JOV JAZ JBZ JXZ JAN JXN JAB JAX NOV NAZ NBZ NXZ NAN NXN NAN NAN	a a a a a a a a a a a a a a a a a a a	Jump if Overflow Set Jump if A Equal to Zero Jump if B Equal to Zero Jump if X Equal to Zero Jump if A Negative Jump if X Negative Jump if A Equals B Jump if A Equals X Jump if Overflow Not Set Jump if A Not Equal to Zero Jump if B Not Equal to Zero Jump if X Not Negative Jump if A Not Negative Jump if A Not Equal to B Jump if A Not Equal to X

Operation Code	Mnemonic	Operand	Instruction Name
Shift			
20 21 22 24 25 26 28 29 2A 2C 2D 2E	LLA LLB LRA LRB LRL ALA ALB ALL ARA ARB ARL	n n n n n n n n n n n n n n	Logical Left A Logical Left B Logical Left Long Logical Right A Logical Right B Logical Right Long Arithmetic Left A Arithmetic Left B Arithmetic Left Long Arithmetic Right A Arithmetic Right A Arithmetic Right B Arithmetic Right Long
Input/Outpu	ıt		
30 31 32 33 38 39 3A 3B	IBS IBA IBB IBM OBS OBA OBB	f,d f,d f,d,a or a,X f,d f,d f,d,a or a,X	Input Byte Serially Input Byte to A Input Byte to B Input Byte to Memory Output Byte Serially Output Byte from A Output Byte from B Output Byte from Memory
Register Ope	erate		
40 41 42 43 44 45 46 47 48 49 4A 4B 4C 4D 4E 4F	ORA XRA ORB XRB INX DCX AWX SWX INA INB OCA OCB TAX TBX TXA TXB		OR B with A Exclusive — OR B with A OR A with B Exclusive — OR A with B Increment X Decrement X Add Word Length to X Subtract Word Length from X Increment A Increment B One's Complement A One's Complement B Transfer A to X Transfer B to X Transfer X to A Transfer X to B

Operation Code	Mnemonic	Operand	Instruction Name
Memory Refe	erence		
60 68 70 78 80 88 90 98 A0 A8 B0 B8 C0 C8 D0 D8 E0 E8 F0 F8	JMP RTJ IWM DWM LDX STX MUL DIV ADA ADV SBA SBV CPA CPV ANA ANV LDA LDV STA STV	a or a,X or l a or a,X or l	Jump Return Jump Increment Word in Memory Decrement Word in Memory Load X Store X Multiply Divide Add to A Add Variable Subtract from A Subtract Variable Compare A Compare Variable And A And Variable Load A Load Variable Store A Store Variable
Operation Code	Mnemonic	Operand	Instruction Name and Mode
Jump and Re	turn Jump Add	ressing Modes (ty	pical)
60 61 62 63 64 65 66	JMP JMP* JMP* JMP- JMP+ JMP/ JMP=	a a a a a or a,X a or a,X	Jump — Page 0 Jump — Relative Jump — Indirect Page 0 Jump — Indirect Relative Jump — Indexed Jump — Indexed with Bias Jump — Extended Address Jump — Indirect Extended Address

Operation Code	Mnemonic	Operand	Instruction Name and Mode
Other Memo	ry Referencing	Addressing Mod	es (typical)
A0	ADA	а	Add to A — Page 0
A1	ADA	а	Add to A — Relative
A2	ADA*	а	Add to A — Indirect Page 0
A3	ADA*	a	Add to A — Indirect Relative
A4	ADA-		Add to A — Indexed
A5	ADA+	а	Add to A — Indexed With Bias
A6	ADA/	a	Add to A — Extended Address
A7	ADA=	I	Add to A $-$ Literal

Legend of Operand Symbols

a,X

Address expression
Address expression with index flag
Literal
Device Order
Device Number
Shift Count f d

APPENDIX B
STANDARD CHARACTER CODES

SYMBOL		EBCDIC (HEX)		ERITH (026)	SYMBOL	ASCII (HEX)			RITH (026)
blank	Α0	40	bla	nk	@	CO	7C	8-4	0-8-2
!	Α1	5A	11-	8-2	Α	C1	C1	12-1	
"	A2	7F	8-7	0-8-5	В	C2	C2	12-2)
#	A3	7B	8-3	0-8-7	С	C3	C3	12-3	}
\$	A4	5B	11-	8-3	D	C4	C4	12-4	•
%	A5	6C	0-8-4	11-8-7	Ę	C5	C5	12-5	•
&	A6	50	12	12-8-7	F	C6	C6	12-6	,
,	Α7	7D	8-5	8-4	G	C7	C7	12-7	
(A8	4D	12-8-5	0-8-4	Н	C8	C8	12-8	}
)	Α9	5D	11-8-5	12-8-4	1	C9	C9	12-9)
*	AA	5C	11-	8-4	J	CA	D1	11-1	
+	AB	4E	12-8-6	12	К	CB	D2	11-2	
,	AC	6B	0-8	-3	L	CC	D3	11-3	
	AD	60	11		М	CD	D4	11-4	
	ΑE	4B	12-	8-3	N	CE	D5	11-5	
/	AF	61	0-1		0	CF	D6	11-6	
0	В0	F0	0		Р	D0	D7	11-7	
1	B1	F1	1		Q	D1	D8	11-8	
2	B2	F2	2		R	D2	D9	11-9	
3	В3	F3	3		S	D3	E2	0-2	
4	B4	F4	4		Т	D4	E3	0-3	
5	B5	F5	5		U	D5	E4	0-4	
6	B6	F6	6		V	D6	E 5	0-5	
7	B7	F7	7		W	D7	E6	0-6	
8	B8	F8	8		Х	D8	E7	0-7	
9	В9	F9	9		Υ	D9	E8	0-8	
•	BA	7A	8-2	8-5	Z	DA	E9	0-9	
;	BB	5E	11-	8-6	[DB	4F	12-8-7	12-8-5
<	вс	4C	12-8-4	12-8-6	\	DC	4A	12-8-2	0-8-6
=	BD	7E	8-6	8-3]	DD	5F	11-8-7	11-8-5
>	BE	6E	0-8-6	8-6	↑	DE	6D	0-8-5	8-7
?	BF	6F	0-8-7	12-8-2	←	DF	6A	0-8-2	8-2

TELETYPE CONTROL AND TRANSMISSION CODES

APPENDIX C

FUNCTION	ASCII	
NULL	80	
SOM (Print on)	81	
EAO	82	
EOM	83	
EOT (Print off)	84	
WRU	85	
RU	86	
BELL	87	
FEO	88	
H.TAB	89	
LINE FEED	A8	
V.TAB	8B	
FORM	8C	
CARRIAGE RETURN	8D	
SO	8E	
SI	8F	
DCO	90	
X-ON (Reader on)	91	
TAPE (Punch on)	92	
X-OFF (Reader off)	93	
TAPE OFF (Punch off)	94	
ERROR	95	
SYNC	96	
LEM	97	
S0	98	
S1	99	
S2	9A	1
S3	9B	
S4	9C	
S5	9D	
S6	9E	
S7	9F	

APPENDIX D

TABLE OF POWERS OF TWO

```
2^n
                        2^{-n}
              1
                   0
                       1.0
              2
                   1
                       0,5
              4
                   2
                       0.25
              8
                   3
                       0.125
             16
                   4
                       0.062 5
             32
                   5
                       0.031 25
             64
                   6
                       0.015 625
            128
                   7
                       0.007 812 5
           256
                   8
                       0.003 906 25
           512
                   9
                       0.001 953 125
          1 024
                  10
                       0.000 976 562 5
          2 048
                  11
                       0.000 488 281 25
          4 096
                  12
                       0.000 244 140 625
                  13
          8 192
                       0.000 122 070 312 5
        16 384
                  14
                       0.000 061 035 156 25
        32 768
                  15
                       0.000 030 517 578 125
        65 536
                  16
                       0.000 015 258 789 062 5
       131 072
                  17
                       0.000 007 629 394 531 25
       262 144
                       0.000 003 814 697 265 625
                  18
       524 288
                  19
                       0.000 001 907 348 632 812 5
     1 048 576
                  20
                       0.000 000 953 674 316 406 25
     2 097 152
                  21
                       0.000 000 476 837 158 203 125
     4 194 304
                  22
                       0.000 000 238 418 579 101 562 5
     8 388 608
                  23
                       0.000 000 119 209 289 550 781 25
    16 777 216
                 24
                       0.000 000 059 604 644 775 390 625
    33 554 432
                 25
                       0.000 000 029 802 322 387 695 312 5
    67 108 864
                  26
                       0.000 000 014 901 161 193 847 656 25
                       0.000 000 007 450 580 596 923 828 125
   134 217 728
                 27
   268 435 456
                 28
                       0.000 000 003 725 290 298 461 914 062 5
   536 870 912
                       0.000 000 001 862 645 149 230 957 031 25
                 29
 1 073 741 824
                       0.000 000 000 931 322 574 615 478 515 625
                 30
 2 147 483 648
                       0.000 000 000 465 661 287 307 739 257 812 5
                 31
 4 294 967 296
                 32
                       0.000 000 000 232 830 643 653 869 628 906 25
 8 589 934 592
                       0.000 000 000 116 415 321 826 934 814 453 125
                 33
17 179 869 184
                       0.000 000 000 058 207 660 913 467 407 226 562 5
                 34
                       0.000 000 000 029 103 830 456 733 703 613 281 25
34 359 738 368
                 35
```

APPENDIX E

HEXADECIMAL – DECIMAL INTEGER CONVERSION TABLES

The following tables aid in	n converting hexadecimal	HEXADECIMAL	DECIMAL
values to decimal values, or	the reverse	1000	4096
values to decimal values, or	the levelse.	2000	8192
		3000	12288
		4000	16384
		5000	20480
Direct Conversion Table		6000	24576
m: . 11		7000	28672
This table provides direct of		8000	32768
hexadecimal numbers in the	se ranges:	9000	36864
	DECIMAL	A000	40960
HEXADECIMAL		B000	45056
000 to FFF	0000 to 4095	C000	49152
For numbers outside the	range of the table, add the	D000	53248
		E000	57344
following values to the table	ngures:	F000	61440

	0	1	2	3	4	5	6	7	8	9	A	В	С	D	E	F
00_	0000	0001	0002	0003	0004	0005	0006	0007	0008	0009	0010	0011	0012	0013	0014	0015
01_	0016	0017	0018	0019	0020	0021	0022	0023	0024	0025	0026	0027	0028	0029	0030	0031
02_	0032	0033	0034	0035	0036	0037	0038	0039	0040	0041	0042	0043	0044	0045	0046	0047
03_	0048	0049	0050	0051	0052	0053	0054	0055	0056	0057	0058	0059	0060	0061	0062	0063
04_	0064	0065	0066	0067	0068	0069	0070	0071	0072	0073	0074	0075	0076	0077	0078	0079
05_	0080	0081	0082	0083	0084	0085	0086	0087	0088	0089	0090	0091	0092	0093	0094	0095
06_	0096	0097	0098	0099	0100	0101	0102	0103	0104	0105	0106	0107	0108	0109	0110	0111
07_	0112	0113	0114	0115	0116	0117	0118	0119	0120	0121	0122	0123	0124	0125	0126	0127
08_	0128	0129	0130	0131	0132	0133	0134	0135	0136	0137	0138	0139	0140	0141	0142	0143
09_	0144	0145	0146	0147	0148	0149	0150	0151	0152	0153	0154	0155	0156	0157	0158	0159
0A_	0160	0161	0162	0163	0164	0165	0166	0167	0168	0169	0170	0171	0172	0173	0174	0175
0B_	0176	0177	0178	0179	0180	0181	0182	0183	0184	0185	0186	0187	0188	0189	0190	0191
0C_	0192	0193	0194	0195	0196	0197	0198	0199	0200	0201	0202	0203	0204	0205	0206	0207
0D_	0208	0209	0210	0211	0212	0213	0214	0215	0216	0217	0218	0219	0220	0221	0222	0223
0E_	0224	0225	0226	0227	0228	0229	0230	0231	0232	0233	0234	0235	0236	0237	0238	0239
0F_	0240	0241	0242	0243	0244	0245	0246	0247	0248	0249	0250	0251	0252	0253	0254	0255
10_	0256	0257	0258	0259	0260	0261	0262	0263	0264	0265	0266	0267	0268	0269	0270	0271
11_	0272	0273	0274	0275	0276	0277	0278	0279	0280	0281	0282	0283	0284	0285	0286	0287
12_	0288	0289	0290	0291	0292	0293	0294	0295	0296	0297	0298	0299	0300	0301	0302	0303
13_	0304	0305	0306	0307	0308	0309	0310	0311	0312	0313	0314	0315	0316	0317	0318	0319
14_	0320	0321	0322	0323	0324	0325	0326	0327	0328	0329	0330	0331	0332	0333	0334	0335
15_	0336	0337	0338	0339	0340	0341	0342	0343	0344	0345	0346	0347	0348	0349	0350	0351
16_	0352	0353	0354	0355	0356	0357	0358	0359	0360	0361	0362	0363	0364	0365	0366	0367
17_	0368	0369	0370	0371	0372	0373	0374	0375	0376	0377	0378	0379	0380	0381	0382	0383
18_	0384	0385	0386	0387	0388	0389	0390	0391	0392	0393	0394	0395	0396	0397	0398	0399
19_	0400	0401	0402	0403	0404	0405	0406	0407	0408	0409	0410	0411	0412	0413	0414	0415
1A_	0416	0417	0418	0419	0420	0421	0422	0423	0424	0425	0426	0427	0428	0429	0430	0431
1B_	0432	0433	0434	0435	0436	0437	0438	0439	0440	0441	0442	0443	0444	0445	0446	0447
1C_	0448	0449	0450	0451	0452	0453	0454	0455	0456	0457	0458	0459	0460	0461	0462	0463
1D_	0464	0465	0466	0467	0468	0469	0470	0471	0472	0473	0474	0475	0476	0477	0478	0479
1E_	0480	0481	0482	0483	0484	0485	0486	0487	0488	0489	0490	0491	0492	0493	0494	0495
1F_	0496	0497	0498	0499	0500	0501	0502	0503	0504	0505	0506	0507	0508	0509	0510	0511

	0	1	2	3	4	5	6	7	8	9	A	В	С	D	Е	F
20_	0512	0513	0514	0515	0516	0517	0518	0519	0520	0521	0522	0523	0524	0525	0526	0527
21_	0528	0529	0530	0531	0532	0533	0534	0535	0536	0537	0538	0539	0540	0541	0542	0543
22_ 23_	0544 0560	0545 0561	0546 0562	0547 0563	0548 0564	0549 0565	0550 0566	0551 0567	0552 0568	0553 0569	0554	0555	0556	0557	0558	0559
24_	0576	0577	0578	0579	0580	0581	0582	0583			0570	0571	0572	0573	0574	0575
25_	0592	0593	0594	0595	0596	0597	0598	0599	0584 0600	0585 0601	0586 0602	0587 0603	0588 0604	0589 0605	0590 0606	0591 0607
26_	0608	0609	0610	0611	0612	0613	0614	0615	0616	0617	0618	0619	0620	0621	0622	0623
27_	0624	0625	0626	0627	0628	0629	0630	0631	0632	0633	0634	0635	0636	0637	0638	0639
28_	0640	0641	0642	0643	0644	0645	0646	0647	0648	0649	0650	0651	0652	0653	0654	0655
29_ 2A_	0656 0672	0657 0673	0658 0674	0659 0675	0660 0676	0661	0662	0663	0664	0665	0666	0667	0668	0669	0670	0671
2B_	0688	0689	0690	0691	0692	0677 0693	0678 0694	0679 0695	0680 0696	0681 0697	0682 0698	0683 0699	0684 0700	$0685 \\ 0701$	0686 0702	0687
2C_	0704	0705	0706	0707	0708	0709	0710	0711	0712	0713	0714	0715	0716	0701	0702	0703
2D_	0720	0721	0722	0723	0724	0725	0726	0727	0728	0713	0730	0713	0732	0717	0718	0719 0735
2E_	0736	0737	0738	0739	0740	0741	0742	0743	0744	0745	0746	0747	0748	0749	0750	0751
2F_	0752	0753	0754	0755	0756	0757	0758	0759	0760	0761	0762	0763	0764	0765	0766	0767
30_	0768	0769	6770	0771	0772	0773	0774	0775	0776	0777	0778	0779	0780	0781	0782	0783
31_ 32_	0784 0800	0785	0786	0787	0788	0789	0790	0791	0792	0793	0794	0795	0796	0797	0798	0799
33_	0816	$0801 \\ 0817$	0802 0818	0803 0819	0804 0820	$0805 \\ 0821$	0806 0822	0807 0823	0808 0824	0809 0825	0810 0826	$0811 \\ 0827$	0812 0828	0813 0829	0814	0815
34_	0832	0833	0834	0835	0836	0837	0838	0839	0840	0841	0842	0843			0830	0831
35_	0848	0849	0850	0851	0852	0853	0854	0855	0840 0856	0841	0842 0858	0843 0859	0844 0860	0845 0861	0846 0862	0847 0863
36_	0864	0865	0866	0867	0868	0869	0870	0871	0872	0873	0874	0875	0876	0877	0878	0879
37_	0880	0881	0882	0883	0884	0885	0886	0887	0888	0889	0890	0891	0892	0893	0894	0895
38_ 39_	0896 0912	0897 0913	0898 0914	0899 0915	0900	0901	0902	0903	0904	0905	0906	0907	0908	0909	0910	0911
3A_	0912	0913	0914	0915	0916 0932	0917 0933	0918 0934	0919 0935	0920 0936	0921 0937	0922 0938	0923 0939	0924	0925	0926	0927
3B_	0944	0945	0946	0947	0948	0949	0950	0951	0952	0953	0954	0939	0940 0956	0941 0957	0942 0958	0943 0959
3C_	0960	0961	0962	0963	0964	0965	0966	0967	0968	0969	0970	0971	0972	0973	0974	0975
3D_	0976	0977	0978	0979	0980	0981	0982	0983	0984	0985	0986	0987	0988	0989	0990	0991
3E_ 3F_	0992 1008	0993	0994	0995	0996	0997	0998	0999	1000	1001	1002	1003	1004	1005	1006	1007
	1008	1009	1010	1011	1012	1013	1014	1015	1016	1017	1018	1019	1020	1021	1022	1023
	0	1	2	3	4	5	6	7	8	9	A	В	С	D	Е	F
40_	1024	1025	1026	1027	1028	1029	1030	1031	8	1033	1034	B 1035	1036	D 1037		
41_	1024 1040	1025 1041	1026 1042	1027 1043	1028 1044	1029 1045	1030 1046	1031 1047	8 1032 1048	1033 1049	1034 1050	B 1035 1051	1036 1052	D 1037 1053	E 1038 1054	F 1039 1055
41_ 42_	1024	1025 1041 1057	1026 1042 1058	1027 1043 1059	1028 1044 1060	1029 1045 1061	1030 1046 1062	1031 1047 1063	8 1032 1048 1064	1033 1049 1065	1034 1050 1066	B 1035 1051 1067	1036 1052 1068	D 1037 1053 1069	E 1038 1054 1070	F 1039 1055 1071
41_ 42_ 43_	1024 1040 1056 1072	1025 1041 1057 1073	1026 1042 1058 1074	1027 1043 1059 1075	1028 1044 1060 1076	1029 1045 1061 1077	1030 1046 1062 1078	1031 1047 1063 1079	8 1032 1048 1064 1080	1033 1049 1065 1081	1034 1050 1066 1082	B 1035 1051 1067 1083	1036 1052 1068 1084	D 1037 1053 1069 1085	E 1038 1054 1070 1086	F 1039 1055 1071 1087
41_ 42_ 43_ 44_ 45_	1024 1040 1056 1072 1088 1104	1025 1041 1057 1073 1089 1105	1026 1042 1058 1074 1090 1106	1027 1043 1059 1075 1091 1107	1028 1044 1060	1029 1045 1061	1030 1046 1062	1031 1047 1063	8 1032 1048 1064	1033 1049 1065 1081 1097	1034 1050 1066	B 1035 1051 1067 1083 1099	1036 1052 1068 1084 1100	D 1037 1053 1069 1085 1101	E 1038 1054 1070 1086 1102	F 1039 1055 1071 1087 1103
41_ 42_ 43_ 44_ 45_ 46_	1024 1040 1056 1072 1088 1104 1120	1025 1041 1057 1073 1089 1105 1121	1026 1042 1058 1074 1090 1106 1122	1027 1043 1059 1075 1091 1107 1123	1028 1044 1060 1076 1092 1108 1124	1029 1045 1061 1077 1093 1109 1125	1030 1046 1062 1078 1094 1110 1126	1031 1047 1063 1079 1095 1111 1127	8 1032 1048 1064 1080 1096 1112 1128	1033 1049 1065 1081 1097 1113 1129	1034 1050 1066 1082 1098 1114 1130	B 1035 1051 1067 1083 1099 1115 1131	1036 1052 1068 1084 1100 1116 1132	D 1037 1053 1069 1085 1101 1117 1133	E 1038 1054 1070 1086 1102 1118 1134	F 1039 1055 1071 1087 1103 1119 1135
41_ 42_ 43_ 44_ 45_ 46_ 47_	1024 1040 1056 1072 1088 1104 1120 1136	1025 1041 1057 1073 1089 1105 1121 1137	1026 1042 1058 1074 1090 1106 1122 1138	1027 1043 1059 1075 1091 1107 1123 1139	1028 1044 1060 1076 1092 1108 1124 1140	1029 1045 1061 1077 1093 1109 1125 1141	1030 1046 1062 1078 1094 1110 1126 1142	1031 1047 1063 1079 1095 1111 1127 1143	8 1032 1048 1064 1080 1096 1112 1128 1144	1033 1049 1065 1081 1097 1113 1129 1145	1034 1050 1066 1082 1098 1114 1130 1146	B 1035 1051 1067 1083 1099 1115 1131 1147	1036 1052 1068 1084 1100 1116 1132 1148	D 1037 1053 1069 1085 1101 1117 1133 1149	E 1038 1054 1070 1086 1102 1118 1134 1150	F 1039 1055 1071 1087 1103 1119 1135 1151
41_ 42_ 43_ 44_ 45_ 46_ 47_ 48_	1024 1040 1056 1072 1088 1104 1120 1136 1152	1025 1041 1057 1073 1089 1105 1121 1137 1153	1026 1042 1058 1074 1090 1106 1122 1138 1154	1027 1043 1059 1075 1091 1107 1123 1139 1155	1028 1044 1060 1076 1092 1108 1124 1140	1029 1045 1061 1077 1093 1109 1125 1141 1157	1030 1046 1062 1078 1094 1110 1126 1142 1158	1031 1047 1063 1079 1095 1111 1127 1143 1159	8 1032 1048 1064 1080 1096 1112 1128 1144 1160	1033 1049 1065 1081 1097 1113 1129 1145 1161	1034 1050 1066 1082 1098 1114 1130 1146 1162	B 1035 1051 1067 1083 1099 1115 1131 1147 1163	1036 1052 1068 1084 1100 1116 1132 1148 1164	D 1037 1053 1069 1085 1101 1117 1133 1149 1165	E 1038 1054 1070 1086 1102 1118 1134 1150 1166	F 1039 1055 1071 1087 1103 1119 1135 1151 1167
41_ 42_ 43_ 44_ 45_ 46_ 47_	1024 1040 1056 1072 1088 1104 1120 1136	1025 1041 1057 1073 1089 1105 1121 1137 1153 1169 1185	1026 1042 1058 1074 1090 1106 1122 1138 1154 1170	1027 1043 1059 1075 1091 1107 1123 1139 1155 1171	1028 1044 1060 1076 1092 1108 1124 1140 1156 1172	1029 1045 1061 1077 1093 1109 1125 1141 1157 1173	1030 1046 1062 1078 1094 1110 1126 1142 1158 1174	1031 1047 1063 1079 1095 1111 1127 1143 1159 1175	8 1032 1048 1064 1080 1096 1112 1128 1144 1160 1176	1033 1049 1065 1081 1097 1113 1129 1145 1161 1177	1034 1050 1066 1082 1098 1114 1130 1146 1162 1178	B 1035 1051 1067 1083 1099 1115 1131 1147 1163 1179	1036 1052 1068 1084 1100 1116 1132 1148 1164 1180	D 1037 1053 1069 1085 1101 1117 1133 1149 1165 1181	E 1038 1054 1070 1086 1102 1118 1134 1150 1166 1182	F 1039 1055 1071 1087 1103 1119 1135 1151 1167 1183
41_ 42_ 43_ 44_ 45_ 46_ 47_ 48_ 49_	1024 1040 1056 1072 1088 1104 1120 1136 1152 1168	1025 1041 1057 1073 1089 1105 1121 1137 1153 1169	1026 1042 1058 1074 1090 1106 1122 1138 1154	1027 1043 1059 1075 1091 1107 1123 1139 1155	1028 1044 1060 1076 1092 1108 1124 1140	1029 1045 1061 1077 1093 1109 1125 1141 1157	1030 1046 1062 1078 1094 1110 1126 1142 1158	1031 1047 1063 1079 1095 1111 1127 1143 1159	8 1032 1048 1064 1080 1096 1112 1128 1144 1160	1033 1049 1065 1081 1097 1113 1129 1145 1161	1034 1050 1066 1082 1098 1114 1130 1146 1162	B 1035 1051 1067 1083 1099 1115 1131 1147 1163	1036 1052 1068 1084 1100 1116 1132 1148 1164	D 1037 1053 1069 1085 1101 1117 1133 1149 1165	E 1038 1054 1070 1086 1102 1118 1134 1150 1166	F 1039 1055 1071 1087 1103 1119 1135 1151 1167 1183 1199
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60_	1536	1537	1538	1539	1540	1541	1542	1543	1544	1545	1546	1547	1548	1549	1550	1551
61_	1552	1553	1554	1555	1556	1557	1558	1559	1560	1561	1562	1563 1579	1564	1565	1566 1582	1567 1583
62_ 63_	1568 1584	1569 1585	1570 1586	1571 1587	1572 1588	1573 1 58 9	1574 1590	1575 1591	1576 1592	1577 1593	1578 1 594	1595	1580 1596	1581 1597	1598	1599
64_	1600	1601	1602	1603	1604	1605	1606	1607	1608	1609	1610	1611	1612	1613	1614	1615
65_	1616	1617	1618	1619	1620	1621	1622	1623	1624	1625	1626	1627	1628	1629	1630	1631
6 ₀ _	1632 1648	1633 1649	1634 1650	1635 1651	1636 1652	1637 1653	1638 1654	1639 1655	1640 1656	1641 1657	1642 1658	1643 1659	1644 1660	1645 1661	1646 1662	1647 1663
67_ 68_	1664	1665	1666	1667	1668	1669	1670	1671	1672	1673	1674	1675	1676	1677	1678	1679
69_	1680	1681	1682	1683	1684	1685	1686	1687	1688	1689	1690	1691	1692	1693	1694	1695
6A_ 6B_	1696	1697 1713	1698 1714	1699 1715	1700 1716	1701 1717	$\frac{1702}{1718}$	1703 1719	$1704 \\ 1720$	$1705 \\ 1721$	$1706 \\ 1722$	$1707 \\ 1723$	$1708 \\ 1724$	$1709 \\ 1725$	$1710 \\ 1726$	1711 1727
6C_	1712 1728	1713	1730	1731	1732	1733	1734	1735	1736	1737	1738	1739	1740	1741	1742	1743
6D_	1744	1745	1746	1747	1748	1749	1750	1751	1752	1753	1754	1755	1756	1757	1758	1759
6E_ 6F_	1760 1776	1761 1777	$\frac{1762}{1778}$	1763 1779	1764 1780	1765 1781	1766 1782	1767 1783	1768 1784	1769 1785	1770 1786	1771 1787	$1772 \\ 1788$	1773 1789	1774 1790	1775 1791
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A0_	2560	2561	2562	2563	2564	2565	2566	2567	2568	2569	2570	2571	2572	2573	2574	2575
A1_ A2_	2576 2592	2577 2593	2578 2594	2579 2595	2580 2596	2581 2597	2582 2598	2583 2599	2584 2600	2585 2601	2586 2602	2587 2603	2588 2604	2589 2605	2590 2606	$2591 \\ 2607$
A3_	2608	2609	2610	2611	2612	2613	2614	2615	2616	2617	2618	2619	2620	2621	2622	2623
A4_ A5_	2624 2640	2625 2641	2626 2642	2627 2643	2628 2644	2629 2645	2630 2646	2631 2647	2632 2648	2633 2649	2634 2650	2635 2651	2636 2652	2637 2653	2638 2654	2639 2655
A6_	2656	2657	2658	2659	2660	2661	2662	2663	2664	2665	2666	2667	2668	2669	2670	2671
A7_ A8_	2672 2688	2673 2689	2674 2690	2675 2691	2676 2692	2677 2693	2678 2694	2679 2695	2680 2696	2681 2697	2682 2698	2683 2699	2684 2700	2685 2701	2686 2702	2687
A9_	2704	2705	2706	2707	2708	2709	2710	2711	2712	2713	2714	2715	2716	2701	2718	$\frac{2703}{2719}$
AA_ AB_	2720 2736	2721 2737	2722 2738	2723 2739	2724 2740	2725 2741	2726 2742	2727 2743	2728 2744	2729 2745	2730 2746	2731 2747	2732 2748	2733 2749	2734 2750	$2735 \\ 2751$
AC_	2752	2753	2754	2755	2756	2757	2758	2759	2760	2761	2762	2763	2764	2765	2766	2767
AD_ AE_	2768	$2769 \\ 2785$	2770 2786	$\frac{2771}{2787}$	2772 2788	$2773 \\ 2789$	2774 2790	$2775 \\ 2791$	$2776 \\ 2792$	$2777 \\ 2793$	2778 2794	$2779 \\ 2795$	2780	2781	2782	2783
AF_	2800	2801	2802	2803	2804	2805	2806	2807	2808	2809	2810	2811	2796 2812	$2797 \\ 2813$	$\frac{2798}{2814}$	2799 2815
B0_	2816	2817	2818	2819	2820	2821	2822	2823	2824	2825	2826	2827	2828	2829	2830	2831
B1_ B2_	2832 2848	2833 2849	2834 2850	$2835 \\ 2851$	2836 2852	$2837 \\ 2853$	2838 2854	2839 2855	2840 2856	$2841 \\ 2857$	2842 2858	2843 2859	2844 2860	2845 2861	2846 2862	2847
B3_	2864	2865	2866	2867	2868	2869	2870	2871	2872	2873	2874	2875	2876	2877	2878	2863 2879
B4_ B5_	2880 2896	$\frac{2881}{2897}$	2882 2898	2883 2899	2884 2900	$2885 \\ 2901$	2886 2902	2887 2903	2888	2889	2890	2891	2892	2893	2894	2895
B6_	2912	2913	2914	2915	2916	2917	2918	2903 2919	2904 2920	$\frac{2905}{2921}$	$\frac{2906}{2922}$	$\frac{2907}{2923}$	2908 2924	2909 2925	2910 2926	2911 2927
B7_ B8_	2928	2929	2930	2931	2932	2933	2934	2935	2936	2937	2938	2939	2940	2941	2942	2943
B9_	2944 2960	2945 2961	2946 2962	2947 2963	2948 2964	$\frac{2949}{2965}$	2950 2966	2951 2967	2952 2968	2953 2969	2954 2970	$2955 \\ 2971$	2956 2972	$\frac{2957}{2973}$	$\frac{2958}{2974}$	2959 2975
BA_ BB_	2976 2992	2977 2993	2978 2994	2979 2995	2980 2996	2981 2997	2982	2983	2984	2985	2986	2987	2988	2989	2990	2991
BC_	3008	3009	3010	3011	3012	3013	2998 3014	2999 3015	3000 3016	3001 3017	3002 3018	3003 3019	3004 3020	3005 3021	3006 3022	3007 3023
BD_ BE_	3024	3025	3026	3027	3028	3029	3030	3031	3032	3033	3034	3035	3036	3037	3038	3039
	3040	3041	3042	3043	3044	3045	3046	3047	3048	3049	3050	3051	3052	3053	3054	3055
BF_	3056	3057	3058	3059	3060	3061	3062	3063	3064	3065	3066	3067	3068	3069	3070	3071
BF_	0	3057	3058	3059	3060	3061	-									
C0_	0 3072	1 3073					3062 6 3078	7	3064 8 3080	3065 9 3081	A	В	С	D	E	F
C0_ C1_	0 3072 3088	1 3073 3089	2 3074 3090	3 3075 3091	4 3076 3092	5 3077 3093	6 3078 3094	7 3079 3095	8 3080 3096	9 3081 3097	A 3082 3098	B 3083 3099	C 3084 3100	D 3085 3101	E 3086 3102	F 3087 3103
C0_	0 3072	1 3073	2 3074	3 3075	4 3076	5 3077	6 3078	7 3079	8 3080	9 3081	A 3082	B 3083	C 3084	D 3085	E 3086	F 3087 3103 3119
C0_ C1_ C2_ C3_ C4_	0 3072 3088 3104 3120 3136	1 3073 3089 3105 3121 3137	2 3074 3090 3106 3122 3138	3 3075 3091 3107 3123 3139	4 3076 3092 3108 3124 3140	5 3077 3093 3109 3125 3141	6 3078 3094 3110 3126 3142	7 3079 3095 3111 3127 3143	8 3080 3096 3112 3128 3144	9 3081 3097 3113 3129 3145	A 3082 3098 3114 3130 3146	B 3083 3099 3115 3131 3147	C 3084 3100 3116 3132 3148	D 3085 3101 3117 3133 3149	E 3086 3102 3118 3134 3150	F 3087 3103 3119 3135 3151
C0_ C1_ C2_ C3_ C4_ C5_ C6_	0 3072 3088 3104 3120 3136 3152 3168	1 3073 3089 3105 3121 3137 3153 3169	2 3074 3090 3106 3122 3138 3154 3170	3 3075 3091 3107 3123 3139 3155 3171	3076 3092 3108 3124	5 3077 3093 3109 3125	6 3078 3094 3110 3126	7 3079 3095 3111 3127	8 3080 3096 3112 3128	9 3081 3097 3113 3129	A 3082 3098 3114 3130	B 3083 3099 3115 3131 3147 3163	C 3084 3100 3116 3132 3148 3164	D 3085 3101 3117 3133 3149 3165	E 3086 3102 3118 3134 3150 3166	F 3087 3103 3119 3135 3151 3167
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C0_ C1_ C2_ C3_ C4_ C5_ C6_ C7_ C8_ C9_	0 3072 3088 3104 3120 3136 3152 3168 3184 3200 3216	1 3073 3089 3105 3121 3137 3153 3169	2 3074 3090 3106 3122 3138 3154 3170	3 3075 3091 3107 3123 3139 3155 3171	4 3076 3092 3108 3124 3140 3156 3172 3188 3204	5 3077 3093 3109 3125 3141 3157 3173 3189 3205	6 3078 3094 3110 3126 3142 3158 3174 3190 3206	7 3079 3095 3111 3127 3143 3159 3175 3191 3207	8 3080 3096 3112 3128 3144 3160 3176 3192 3208	9 3081 3097 3113 3129 3145 3161 3177 3193 3209	A 3082 3098 3114 3130 3146 3162 3178 3194 3210	B 3083 3099 3115 3131 3147 3163 3179 3195 3211	C 3084 3100 3116 3132 3148 3164 3180 3196 3212	D 3085 3101 3117 3133 3149 3165 3181 3197 3213	E 3086 3102 3118 3134 3150 3166 3182 3198 3214	F 3087 3103 3119 3135 3151 3167 3183 3199 3215
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E5_	3664	3665	3666	3667	3668	3669	3670	3671	3672	3673	3674	3675	3676	3677	3678	3679
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F8_	3968	3969	3970	3971	3972	3973	3974	3975	3976	3977	3978	3979	3980	3981	3982	3983
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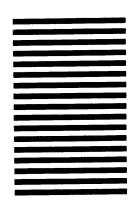
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