**ACKNOWLEDGEMENT**

First and foremost, the author is grateful to Prof. Dr. Thant Zaw Oo, Principal of Technological University (Kalay), for giving the permission to submit the thesis for the Bachelor Degree at Technological University (Kalay).

The author would like to express grateful thanks to Daw Naing, Lecturer and Head, Department of Electronic Engineering, Technological University (Kalay), for permission to carry out this thesis and helps, suggestions and supervisions for this thesis.

The author especially thanks his supervisor, Daw Cho Thet Nwe, Assistant Lecturer, Department of Electronic Engineering from Technological University (Kalay), for her teaching, advices, supervisions, and supports during the preparation of this thesis.

The author deeply appreciates U Than Htun Aung, Assistant Lecturer, Department of Electronic Engineering from Technological University (Kalay), for his excellent guidance and valuable suggestions.

The author wishes to express his gratitude to, Daw Bawi Nun Thiam, Department of Electronic Engineering, Technological University (Kalay), for her helpful comments and suggestions.

Last but not the least, the author is deeply grateful to his parents and brothers, and to all teachers who shared their knowledge and to all of his friends who heartily helped him.