

Kyle Stevens

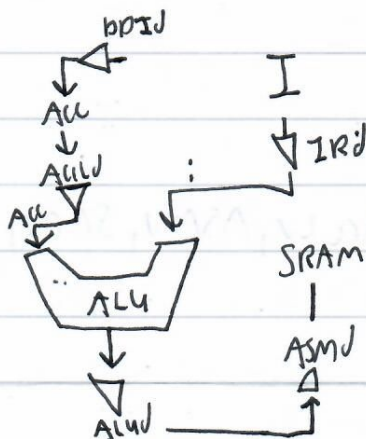
SI71115

Betriebssysteme

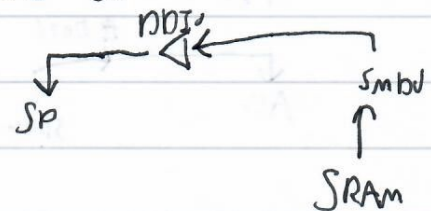
Übung 2

Aufg 1 a STOREIN Acc SP ;  
 $MC \langle Acc \rangle + \langle i \rangle := SP$

Execute - Phase <sup>Teil 1</sup> 1 - Acc



Teil 2 - SP

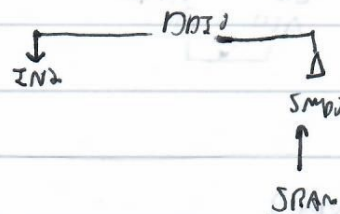
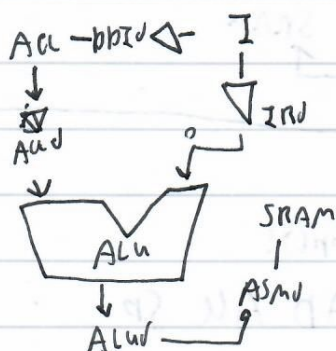


Trichterliste: DDID, ~~Acc~~, IRJ, ~~Acc~~ ALUD, ALUAD, ASMD, SMDJ

b MOVE IN2 Acc

$Acc := IN2$

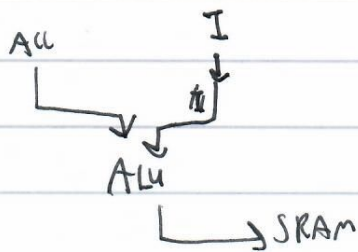
Teil 1



Trichterliste: DDID, ACCLD, ASMD, IRJ, SMDJ

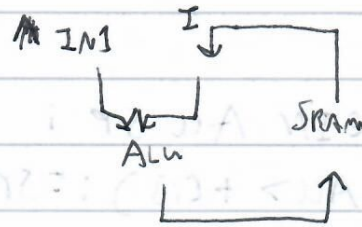
c Add Acc IN1

Teil 1

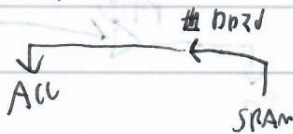


[ACC] := [Acc] + [IN1]

Teil 2



Teil 3



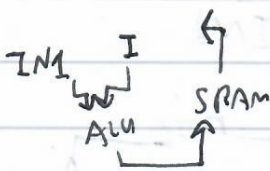
Registerliste: IR, ALU, Acc, ASM, SMD, DSD, IN1

Also 3u.

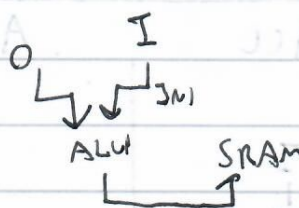
d) Etwas wie Add IN1 01.

Also

Teil 1



Teil 2



Aufgabe 2

Push():

STORE Acc SP

Pop():

LOAD Acc SP

den Rest nicht getun...