ENGR 120/121

Design Labs

I acknowledge with respect the Lekwungen peoples on whose traditional territory the university stands and the Songhees, Esquimalt and WSÁNEĆ peoples whose historical relationships with the land continue to this day.

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Lab rules

- Lab attendance is mandatory to pass the course

    - Attendance will be recorded at the beginning of a lab session

    - Late arrival is not permitted – student will be marked absent

- No food or drinks (including water) inside the lab

- Masks are recommended

- Lab stations must be cleaned before leaving the lab

- Maintaining respectful behaviour

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Interfacing electronic devices lab - objectives

- Configure: Analog and Digital ports

- Wire: Electronic circuits on breadboard using schematic supplied

- Test: Infrared detection circuit and code

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In lab activities

Each student team must demonstrate the successful completion of exercise 2 and exercise 3.

Mark distribution	Max. Points
Exercise 2	2
Exercise 3 – Step 1	1
Exercise 3 – Step 2	1
Exercise 3 – Step 3	1

Note: For exercise 2, please feel free to take help from the TA

To secure the full allotted marks, your circuit should be neat, color coded, and should be easy to trace (or troubleshooting)

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Submission requirements

- A video of the circuit working as intended

Questions

In case you have any questions about lab marking

Please contact your section's lab TAs

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