Fall 2013 CENG 355

Solution 1

```
1.
#define PBIN (volatile char *) 0xFFFFFFF3
#define PBOUT (volatile char *) 0xFFFFFFF4
#define PBDIR (volatile char *) 0xFFFFFFF5
#define PCONT (volatile char *) 0xFFFFFFF7
#define CNTM (volatile int *) 0xFFFFFFD0
#define CTCON (volatile char *) 0xFFFFFD8
#define CTSTAT (volatile char *) 0xFFFFFFD9
#define IVECT (volatile int *) (0x20)
interrupt void intserv();
unsigned char led = 0x4;
                                  /* 0x0 = LED on, 0x4 = LED off */
signed char digit = 0;
                                   /* digit for display */
int main() {
  *PBDIR = 0xF4;
                                          /* Set Port B direction */
  *IVECT = (volatile int *) &intserv;
                                         /* Set interrupt vector */
                                         /* CPU responds to IRQ */
  asm("MoveControl PSR,#0x40");
  *PCONT = 0x40;
                                         /* Enable PBIN interrupts */
  *PBOUT = 0x4;
                                          /* Turn off LED, display 0 */
                                          /* 1-second timeout */
  *CNTM = 100000000;
  *CTCON = 0x1;
                                          /* Start countdown */
  while (1) {
    *CTSTAT = 0x0;
                                  /* Clear "Reached 0" flag */
    while ((*CTSTAT & 0x1) == 0); /* Wait until 0 reached */
                                   /* If off, turn LED on */
    if (led == 0x4) led = 0x0;
                                   /* Else, turn LED off */
    else led = 0x4;
    *PBOUT = ((digit << 4) | led); /* Update LED, same display */
  exit(0);
interrupt void intserv() {
  if ((*PBIN & 0x1) == 0) digit = (digit + 1)%10; /* INC pressed */
  if ((*PBIN & 0x2) == 0) digit = (digit - 1)%10; /* DEC pressed */
  *PBOUT = ((digit << 4) | led); /* Update display, same LED */
}
#define PAOUT (volatile char *) 0xFFFFFFF1
#define PADIR (volatile char *) 0xFFFFFFF2
#define PBIN (volatile char *) 0xFFFFFFF3
#define PBDIR (volatile char *) 0xFFFFFFF5
#define CNTM (volatile int *) 0xFFFFFFD0
#define CTCON (volatile char *) 0xFFFFFD8
#define CTSTAT (volatile char *) 0xFFFFFFD9
#define IVECT (volatile int *) (0x20)
```

```
interrupt void intserv();
int main() {
 *PADIR = 0xFF;
                             /* Configure Port A direction */
 *PBDIR = 0 \times 0;
                             /* Configure Port B direction */
                             /* 100,000,000 cycles = 1 sec */
 *CNTM = 10000000;
 *CTSTAT = 0x0;
                              /* Clear "Reached 0" flag */
 *IVECT = (volatile int *) &intserv;
                                   /* Set up interrupt vector */
 asm("MoveControl PSR,#0x40");
                                   /* CPU responds to IRQ */
 *CTCON = 0x11;
                                   /* Enable timer interrupts
                                     and start countdown */
 *PAOUT = 0x1;
                              /* Turn off LED, display 0 */
 while (1) {
                              /* Infinite loop */
  exit(0);
interrupt void intserv() {
                              /* Clear "Reached 0" flag */
 *CTSTAT = 0x0;
 if (led == 0x0) led = 0x1;
                             /* If on, turn LED off */
                               /* If off, turn LED on */
 else led = 0x0;
 *PAOUT = ((digit << 4) | led); /* Update LED, same display */
```

3. Let **x** denote the I/O device activity percentage to be determined.

Maximum I/O data rate for DMA transfer is $R_{I/O}/d_{I/O-DMA} = 1K$ transfers/s. DMA cost: $(x*1K)(N_{DMA-start} + N_{DMA-end}) = x*2.4M$ cycles/s.

Maximum I/O data rate for polling is $R_{I/O}/d_{I/O} = 128K$ transfers/s. Polling cost: $(x*128K)N_{poll-ready} + ((1-x)*128K)N_{poll-not-ready} = x*51.2M + 51.2M$ cycles/s.

We know that the DMA cost is 100 times cheaper than the polling cost; therefore, 100*(x*2.4M) = x*51.2M + 51.2M, which yields $x \approx 0.27$ (i.e., 27%).

(Note: $1K = 2^{10}$ and $1M = 2^{20}$.)