



K. J. Somaiya College of Engineering, Mumbai-77

Batch: B1 Roll No.: 1711072

Experiment / assignment / tutorial No. 4

Grade: AA / AB / BB / BC / CC / CD / DD

Signature of the Staff In-charge with date

TITLE : To study and implement Non Restoring method of division

AIM : The basis of algorithm is based on paper and pencil approach and the operation involve repetitive shifting with addition and subtraction. So the main aim is to depict the usual process in the form of an algorithm.

Expected OUTCOME of Experiment:

CO 2-Detail working of the arithmetic logic unit and its sub modules

CO 3-Understand the Central processing unit with addressing modes and working of control unit

Books/ Journals/ Websites referred:

1. Carl Hamacher, Zvonko Vranesic and Safwat Zaky, “Computer Organization”, Fifth Edition, TataMcGraw-Hill.
2. William Stallings, “Computer Organization and Architecture: Designing for Performance”, Eighth Edition, Pearson.
3. Dr. M. Usha, T. S. Srikanth, “Computer System Architecture and Organization”, First Edition, Wiley-India.

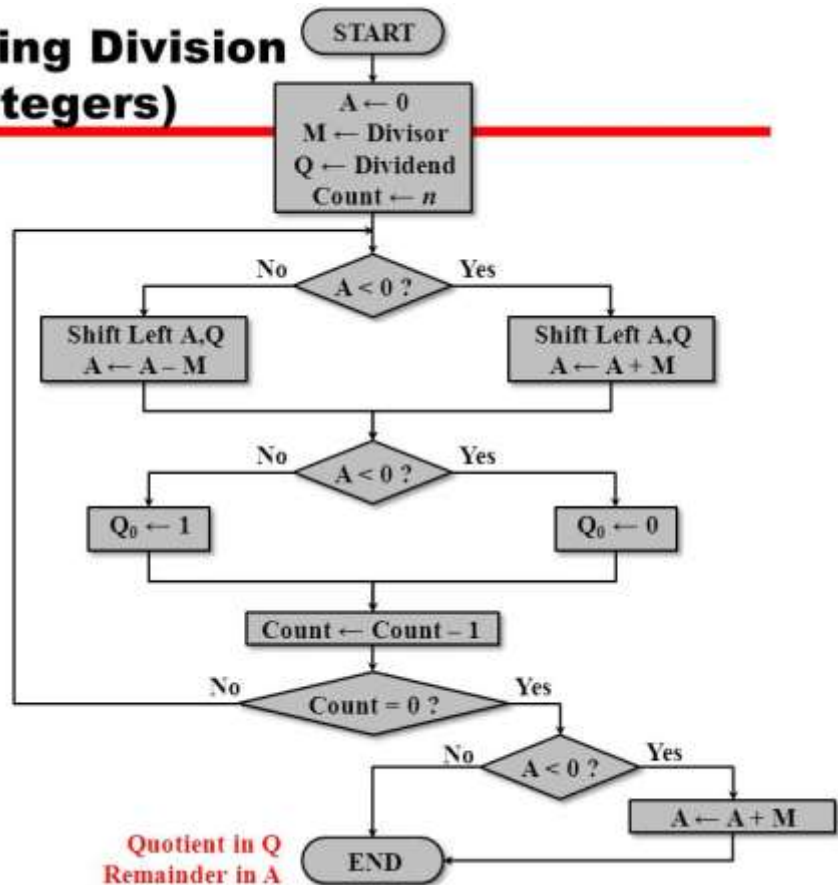
Pre Lab/ Prior Concepts:

The Non Restoring algorithm works with any combination of positive and negative numbers.



Flowchart for Non-Restoring of Division:

Non-Restoring Division (Positive Integers)



Implementation (in Java):

```
import java.util.*;
class Main {
    public static void main(String[] args) {
        int M,Q;
        int BITS=4;
        int count=BITS;
        int M_BIN[]=new int[BITS];
        int Q_BIN[]=new int[BITS];
        int answer[]=new int[BITS];
        int M_TEMP[]=new int[BITS];
        Scanner sc=new Scanner(System.in);
        System.out.println("NON-RESTORING DIVISION: ");
        System.out.print("Enter Divisor(M): ");
        M=sc.nextInt();
```



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```
System.out.print("Enter Dividend(Q): ");
Q=sc.nextInt();

M_BIN=binary(M, BITS);
Q_BIN=binary(Q, BITS);
if(M<0){
    M_BIN=complement(M_BIN, BITS);
}
if(Q<0){
    Q_BIN=complement(Q_BIN, BITS);
}
System.out.print("\nM in binary is: ");
Arrays.stream(M_BIN).forEach(System.out::print);
System.out.print("\nQ in binary is: ");
Arrays.stream(Q_BIN).forEach(System.out::print);
System.out.println("\n");
Arrays.stream(answer).forEach(System.out::print);
System.out.print(" : ");
Arrays.stream(Q_BIN).forEach(System.out::print);

while(count>0){
    System.out.println("\n\nCOUNT= "+count);
    for(int i=0;i<BITS-1;i++){
        answer[i]=answer[i+1];
    }
    answer[BITS-1]=Q_BIN[0];
    for(int i=0;i<BITS-1;i++){
        Q_BIN[i]=Q_BIN[i+1];
    }
    System.out.println("\nArray after left shift: ");
    Arrays.stream(answer).forEach(System.out::print);
    System.out.print(" : ");
    Arrays.stream(Q_BIN).forEach(System.out::print);
    if(answer[0]==0){
        System.out.println("\nArray after A=A-M: ");
        M_TEMP=complement(M_BIN.clone(), BITS);
        answer=add(answer, M_TEMP, BITS);
        Arrays.stream(answer).forEach(System.out::print);
        System.out.print(" : ");
        Arrays.stream(Q_BIN).forEach(System.out::print);
    }

    else if(answer[0]==1){
        M_TEMP=M_BIN.clone();
```



```
        answer=add(answer, M_TEMP, BITS);
        System.out.println("\nArray after A=A+M: ");
        Arrays.stream(answer).forEach(System.out::print);
        System.out.print(" : ");
        Arrays.stream(Q_BIN).forEach(System.out::print);
    }
    if(answer[0]==0)
        Q_BIN[BITS-1]=1;
    else if(answer[0]==1)
        Q_BIN[BITS-1]=0;
    count--;
    if(count==0){
        if(answer[0]==1){
            answer=add(answer, M_TEMP, BITS);
            System.out.println("\nArray after A=A+M: ");
            Arrays.stream(answer).forEach(System.out::print);
            System.out.print(" : ");
            Arrays.stream(Q_BIN).forEach(System.out::print);
        }
    }
}
System.out.print("\nRemainder : Quotient: ");
Arrays.stream(answer).forEach(System.out::print);
System.out.print(" : ");
Arrays.stream(Q_BIN).forEach(System.out::print);
}
public static int[] binary(int dec, int BITS){
    int[] dec_bin=new int[BITS];
    for(int i=BITS-1;i>=0;i--){
        dec_bin[i]=dec%2;
        dec=dec/2;
    }
    return dec_bin;
}
public static int[] add(int[] arr1,int[] arr2, int BITS){
    int carry=0;
    int arr[]=new int[BITS];
    for(int i=BITS-1;i>=0;i--){
        {
            arr[i]=(arr1[i]+arr2[i]+carry)%2;
            carry=(arr1[i]+arr2[i]+carry)/2;
        }
    }
    return arr;
}
```



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```
public static int[] complement(int[] bin, int BITS){
    for(int i=0;i<BITS;i++){
        bin[i]=(bin[i]==0)?1:0;
    }
    int plus_one[]=new int[BITS];
    plus_one[BITS-1]=1;
    bin=add(bin,plus_one,BITS);
    return bin;
}
}
```

For verification, my code is available on:

<https://repl.it/@ARGHYADEEPDAS/COAExpt4>

Output Screen:

```
NON-RESTORING DIVISION:
Enter Divisor(M): 3
Enter Dividend(Q): 5

M in binary is: 0011
Q in binary is: 0101

0000 : 0101

COUNT= 4

Array after left shift:
0000 : 1011
Array after A=A-M:
1101 : 1011

COUNT= 3

Array after left shift:
1011 : 0100
Array after A=A+M:
1110 : 0100

COUNT= 2                                COUNT= 1
Array after left shift:                    Array after left shift:
1100 : 1000                                1111 : 0000
Array after A=A+M:                          Array after A=A+M:
1111 : 1000                                0010 : 0000
Remainder : Quotient: 0010 : 0001
```



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Example:
M=0101; Q=1101

Count	A	Q	Operation
4	0000	1101	
	0001	101_	Arithmetic Left Shift
	1100	101_	A=A-M
	0001	1010	Q ₀ =0; A=A+M
3	0011	010_	Arithmetic Left Shift
	1110	010_	A=A-M
	0011	0100	Q ₀ =0; A=A+M
2	0110	100_	Arithmetic Left Shift
	0001	1001	A=A-M; Q ₀ =1
1	0011	001_	Arithmetic Left Shift
	1110	001_	A=A-M
	0011	0010	Q ₀ =0; A=A+M

Conclusion:

Restoring division algorithm was successfully executed and hence verified as the desired outputs were achieved.

Post Lab Descriptive Questions (Add questions from examination point view)

1. What are the advantages of non-restoring division over restoring division?

Ans.

1. Non-restoring division is faster as it requires less time; whereas restoring division is slower as there is restoration in each cycle.

Sign bit determines whether addition or subtraction is to be performed in non-restoring, whereas there is no such thing in restoring division.



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- 2. Simulate non restoring division algorithm for unsigned numbers A=1101 and B=0101.**

Ans.

M=0101; Q=1101

Count	A	Q	Operation
4	0000	1101	
	0001	101_	Arithmetic Left Shift
	1100	101_	A=A-M
	0001	1010	Q ₀ =0; A=A+M
3	0011	010_	Arithmetic Left Shift
	1110	010_	A=A-M
	0011	0100	Q ₀ =0; A=A+M
2	0110	100_	Arithmetic Left Shift
	0001	1001	A=A-M; Q ₀ =1
1	0011	001_	Arithmetic Left Shift
	1110	001_	A=A-M
	0011	0010	Q ₀ =0; A=A+M

Date: 01/08/2018

Signature of faculty in-charge

Department of Computer Engineering