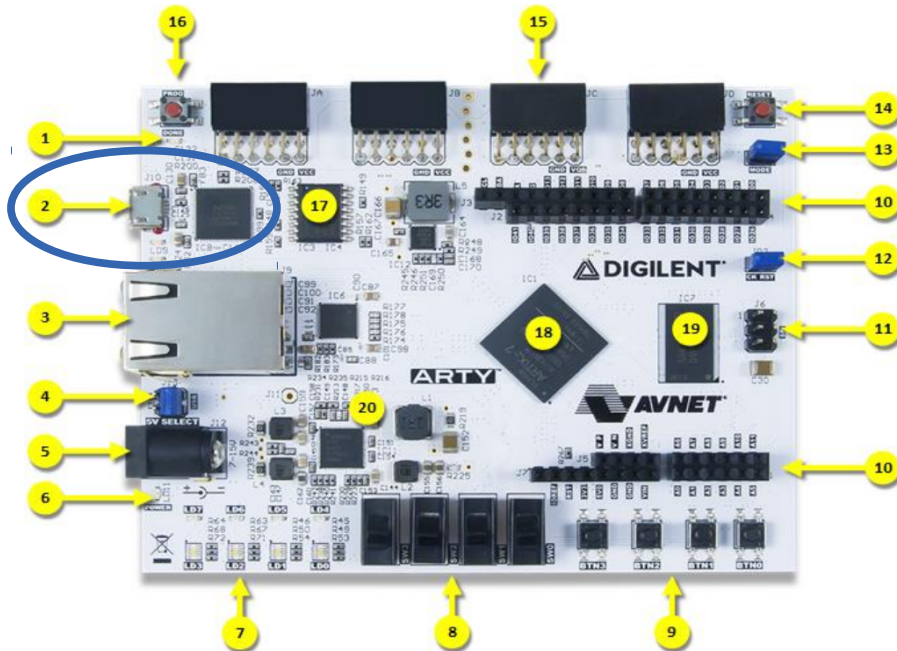


UART – Universal Async. Rx/Tx



- Internal clock speeds exceeding 450MHz;
- On-chip analog-to-digital converter (XADC).
- Programmable over JTAG and Quad-SPI Flash

System Features

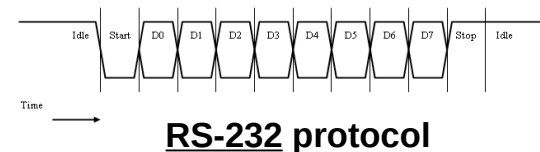
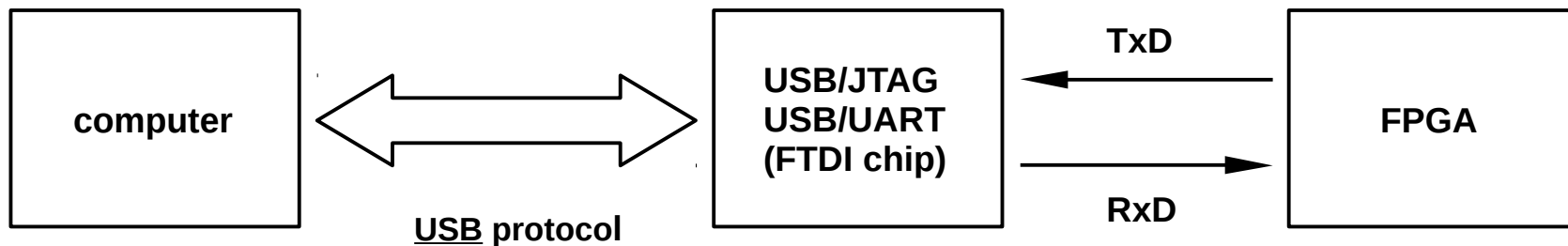
- 256MB DDR3L with a 16-bit bus @ 667MHz
- 16MB Quad-SPI Flash
- USB-JTAG Programming circuitry
- Powered from USB or any 7V-15V source

System Connectivity

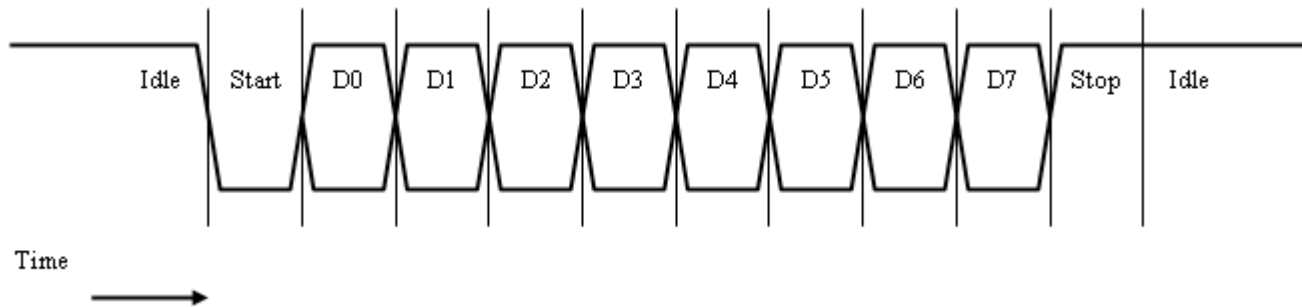
- 10/100 Mbps Ethernet
- **USB-UART Bridge**

Interaction and Sensory Devices

- 4 Switches
- 4 Buttons
- 1 Reset Button



RS-232 Serial Communication



- asynchronous communication
- **IDLE** state 1'b1
- 1x **START** bit 1'b0
- 4/5/6/7/8/9 **PAYLOAD** data
- 1x optional **PARITY** bit for error detection
- 1 (2) **STOP** bit(s) 1'b1
- optional **handshake** via SW/HW

