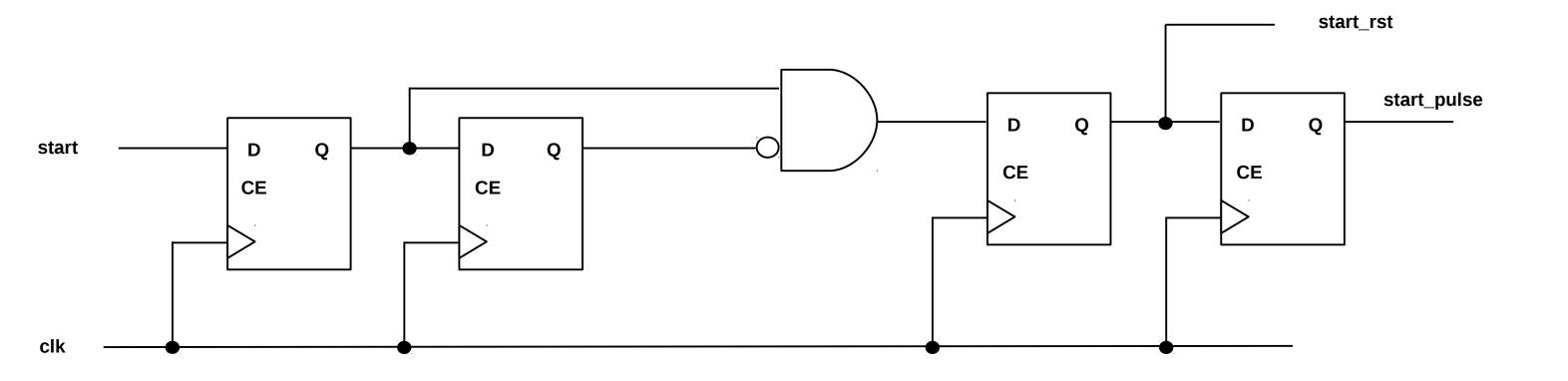
Single-pulse generator



¹∰ clk				
🖫 start_ff0				
₩ start_ff1				
™ start_ff2				
⅓ start_ff3				
⅓ start_rst				
⅓ start_pulse				
Start_ff0 Start_ff1 Start_ff2 Start_ff3 Start_rst Start_pulse Start_reg				

START logic (similar for STOP logic)

