

DECEMBER 2022

By Arian Hajizadeh



Contents

1	Int	roductionroduction	1	
2				
	_	, , , , , , , , , , , , , , , , , , , ,		
3	Ph	ase Lead Compensator	4	
	3.1	Design using graphical method	4	
	3.2	Design using placement of zero	8	
	3.3	Comparison Between Two Methods	10	
4	Ph	ase Lag Compensator	11	
	4.1	Phase lag on the graphical method	11	
	4.2	Phase lag on the graphical method	12	
	4.3	Result of the design	13	
5	Co	nclusion	17	
	5.1	Applying the suggested method	18	

1 Introduction

This is the final project regarding linear control systems course conducted by Dr. Soheil Ganjefar at Iran University of Science and Technology during the fall of 2022 and winter of 2023. In this project we tackle the task of designing a controller for a given system illustrated in Figure 1.1 This system is structured as a closed-loop configuration with negative feedback. Our objective is to analyze the provided information in each section and devise a well-suited controller that ensures optimal performance for the system. Join us on this journey as we delve into the intricacies of control theory and embark on the exciting process of controller design.

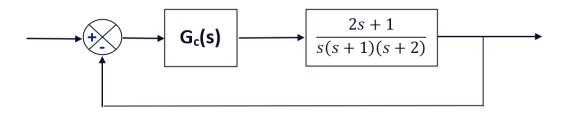


Figure 1-1 Block diagram of the system

In this project, our objective is to design a controller that meets specific performance requirements for the system. We aim to achieve a maximum overshoot of 30 percent, a settling time of less than 2 seconds, and a k_v value greater than or equal to 40.

2 System Analysis and Design Parameters

In this project, the initial step involves analyzing the system to determine whether an additional controller is required, and if the desired properties cannot be achieved using a simple gain as the control unit. The first step is on obtaining and studying the root locus of the system, as well as the desired poles. This analysis will provide valuable insights into the system's stability and performance characteristics, enabling informed decisions regarding the controller design. Therefore, the root locus and desired poles can be seen in figure 2-1.

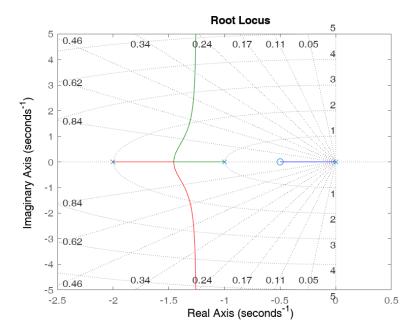


Figure 2-1 root locus of the given system

As for the desired poles we can write the following:

$$M_P = 30\% \to \xi = \frac{\ln(M_p)}{\sqrt{\ln^2(M_P) + \pi^2}}$$
 (2-1)

$$(ln(0.3) = 3.4, \pi^2 = 9.87) \rightarrow \xi = \frac{3.4}{\sqrt{21.43}} = \frac{3.4}{4.62} \approx 0.36$$
 (2-2)

$$t_P = \frac{4}{\omega_n \xi} \to t_{pmax} = 2s \to \frac{4}{\omega_n \xi} = 2s \to \omega_n = \frac{4}{2\xi} \to \omega_n = \frac{4}{0.72} = 5.56$$
 (2-3)

$$\beta = \cos^{-1}(0.36) = 68.89$$
, $\omega_n \xi = 2$, $\omega_n \sin(\beta) = 5.2$ (2-4)

With these calculations the desired points are obtained and can be checked on the s plane alongside the root locus diagram in figure 2-2.

As it is inferred from figure 2-2, the desired poles are not on the root locus diagram of the system. Therefore, additional controller design is needed to achieve the desired characteristics given.

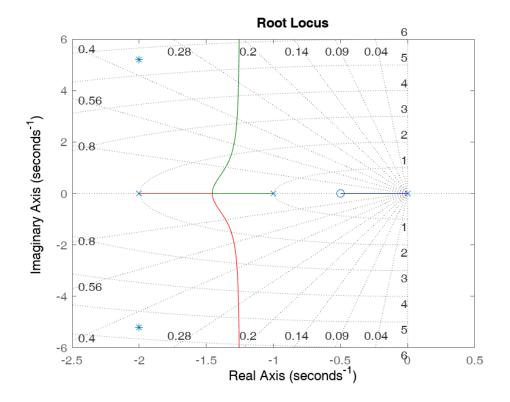


Figure 2-2 root locus of the system alongside desired poles

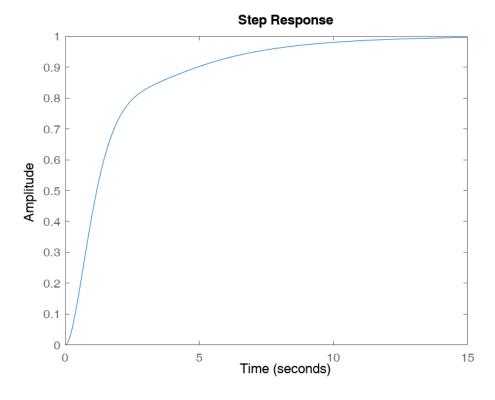


Figure 2-3 step response of the initial system

Once it has been established that an additional controller is necessary for the system, the next step in the project is to design a suitable controller. However, due to the limitations of linear control systems, the scope of this project focuses on the design of a phase lead and phase lag controllers. More complex controller designs are not within the scope of this project. Consequently, the upcoming chapters will be dedicated to the objective of designing both phase lead and phase lag controllers. Through these controller designs, we aim to enhance the system's performance and achieve the desired system characteristics.

3 Phase Lead Compensator

The general form of a phase lead compensator can be seen in (3-1).

$$k \frac{s+z}{s+p} ; p>z>0$$
 (3-1)

Applying equation (3-1) and drawing upon existing knowledge, it is evident that the magnitude of the added pole to the system is consistently greater than magnitude of the added zero. This observation highlights that the injected angle of zero will invariably exceed the angle of the injected pole. Consequently, this design principle enables us to consistently achieve a lead angle (φ). Notably, the larger zero angle, in comparison to the phase lead angle of the injected pole, ensures that the injected angle remains positive. As a result, this controller ensures that using phase criterion the injected angle is such that the desired poles are on the root locus diagram of the system. Therefore, these conditions ensure that the desired properties or specifications are met.

3.1 Design using graphical method

In this method, the real-axis of the frequency response plot for the linear system is first plotted. Then, based on the desired specifications, the desired pole locations are placed on the real-axis, and the corresponding angles are calculated. Using the component elements, the Phase Lead control network is designed, which alters the phase angle and increases the stability of the system.

The angles of zeros and poles can be determined by referencing Figure 3-1. By using this figure, we can calculate the respective angles associated with the zeros and poles of the system. Subsequently, the phase lead angle can be computed utilizing Equation (3-2).

$$\begin{split} & \sum \theta_{z_i} - \sum \theta p_i = 180 \qquad (3\text{-}2) \\ \theta_{P_1} &= 180 - \beta \to \beta = \cos^{-1}(0.357) \approx 69.0 \to 180 - 69.01 = 110.99 \qquad (3\text{-}3) \\ \theta_{z_1} &= 180 - atan\left(\frac{5\cdot 2}{1\cdot 5}\right) = 106.06 \qquad \qquad (3\text{-}4) \\ \theta_{P_2} &= 180 - atan\left(\frac{5\cdot 2}{1}\right) = 100.87 \qquad \qquad (3\text{-}5) \\ \theta_{P_3} &= 90 \qquad \qquad (3\text{-}6) \end{split}$$

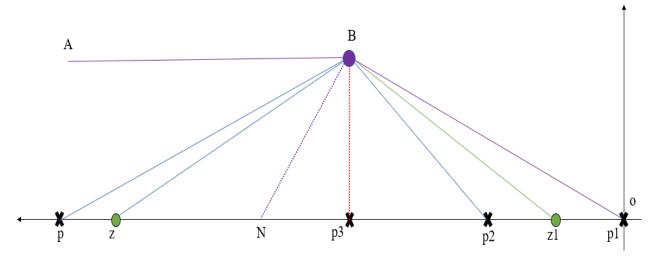


Figure 3-1 system root locus diagram after adding the controller

Using the equation (3-2) now the following can be written:

$$\theta_z - \theta_P = 180 - \theta_{z1} + \theta_{P_1} + \theta_{P_2} + \theta_{P_3} = 375.8 = 15.8 \rightarrow \varphi = 15.8$$
 (3-7)

With the calculated phase lead angle, it becomes possible to determine the pole and zero of the phase lead controller.

$$A\hat{B}O = 180 - \beta = 110.99 \rightarrow A\hat{B}N = \frac{110.99}{2} = 55.495$$
 (3-8)

$$\theta_p = A\hat{B}N - \frac{\varphi}{2} = 55.495 - \frac{15.8}{2} = 47.595$$
 (3-9)

$$\theta p + \varphi = \theta_z \to \theta_z = 47.595 + 15.8 = 63.95$$
 (3-10)

Now the precise location of zero and pole of the controller can be obtained.

$$\theta_P = \to tan(\theta_P) \approx 1.1 \to P = \left(\frac{5.2}{1.1}\right) + 2 = 6.72$$
 (3-11)

$$\theta_z = 63.95 \rightarrow tan(\theta_z) = 1.996 \rightarrow Z = \left(\frac{5.2}{2}\right) + 2 = 4.6 \quad (3-12)$$

With this information now the gain of the phase lead controller can be determined as follows using gain criterion.

$$\left| G_c(s)G_p(s) \right| = \frac{1}{|k|}$$

$$|k| = \frac{s(s+1)(s+2)(s+6.72)}{(2s+1)(s+4.6)} \to s = -2 \pm 5.2j \to k \approx 17.12$$
(3-14)

Therefore, the phase lead controller using graphical design method can be seen at (3-15)

$$G_{c1}(s) = 17.12 \frac{(s+4.55)}{(s+6.63)}$$
 (3-15)

The performance and functionality of the phase lead controller can be seen in figure 3-3.

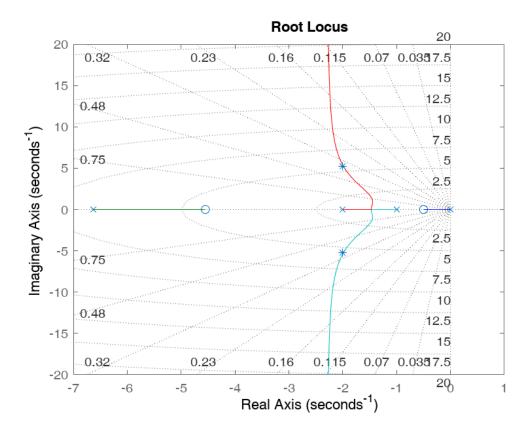
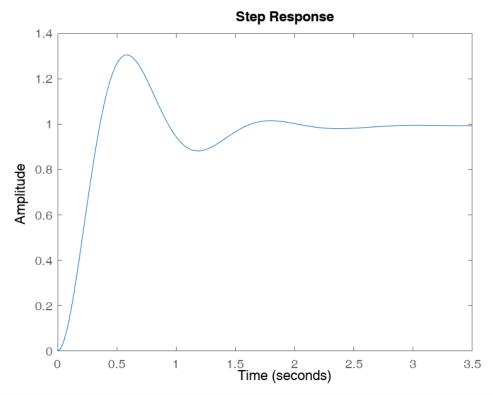


Figure 3-3 successful implementation of phase lead controller using graphical design method

Figure 3-3 demonstrates that the desired poles align with the root locus diagram of the system, indicating that the designed phase lead controller possesses the necessary characteristics to meet the desired performance goals. This graphical representation provides valuable confirmation that the controller design has been successful in achieving the desired pole locations, which are crucial for system stability and performance optimization. By observing this alignment, we can conclude that the controller is well-suited to meet the specified requirements and can effectively regulate the system's behavior in accordance with the desired performance criteria.

In addition to the insights provided by Figure 3-3, further validation of the controller design's success can be obtained by analyzing the step response. The step response provides a valuable means to verify if the designed controller effectively meets the desired performance objectives. The step response of the system can be seen in figure (3-4)



stepinfo(sys)

ans = struct with fields:
 RiseTime: 0.2429
TransientTime: 2.3883
SettlingTime: 2.3883
SettlingMin: 0.8819
SettlingMax: 1.3054
Overshoot: 30.5420
Undershoot: 0
Peak: 1.3054
PeakTime: 0.5795

Figure 3-4 step response and the characteristics of the step response

3.2 Design using placement of zero

In this method, the undesired poles in the system are eliminated by analyzing the linear system and determining the desired pole locations. Then, by calculating the desired phase angle and utilizing control design algorithms, the Phase Lead control network is designed to achieve the desired phase angle and ensure system reaches desired characteristics. Using this method and the information given by equation (3-7) the following can be written.

$$\varphi = 15.8, z = -2 \rightarrow P = \tan(15.8)(5.2) + 2 = 3.47$$
 (3-16)

$$|k| = \frac{s(s+1)(s+3.47)}{2s+1} \to s = -2 \pm 5.2j \to k \approx 14.72$$
 (3-17)

The designed controller can be seen in (3-18)

$$G_{c2}(s) = 14.72 \frac{(s+2)}{(s+3,47)}$$
 (3-18)

It is obvious that designing the controller using this method is quite simpler. The reason for the graphical method design over this method in some designs will be discussed in the following sections.

The performance of this controller can be analyzed similar to the previous one.

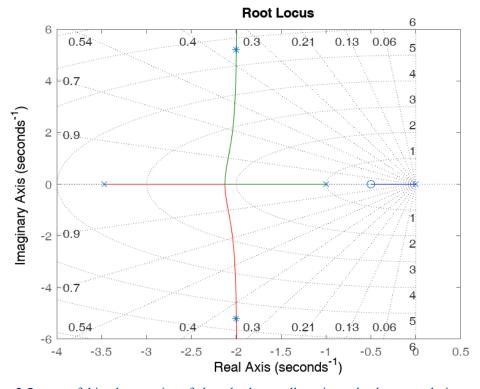
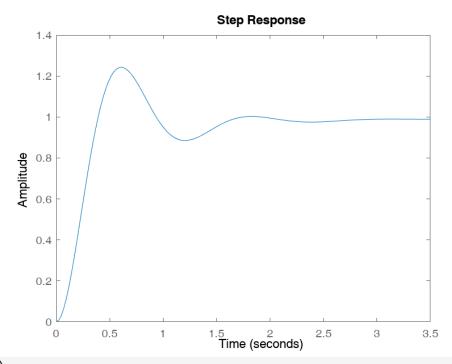


Figure 3-5 successful implementation of phase lead controller using pole placement design method

This method has proven to provide the desired characteristics as well. The comparison between this method and the graphical method will take place in the following chapter.



stepinfo(sys2)

```
ans = struct with fields:
    RiseTime: 0.2650
TransientTime: 2.6093
SettlingTime: 2.6093
SettlingMin: 0.8851
SettlingMax: 1.2426
    Overshoot: 24.2624
Undershoot: 0
    Peak: 1.2426
PeakTime: 0.5994
```

Figure 3-6 step response and the characteristics of the step response

3.3 Comparison Between Two Methods

By utilizing two different methods analyzed in the field of linear control systems, a comparison can be made regarding the effectiveness, performance, and simplicity of the phase lead controller designs.

Both methods aim to modify the root locus diagram of the system, with the objective of placing the desired poles on the diagram. While achieving this goal is crucial for stability and performance, the performance can be further enhanced by employing fewer and more precise approximations.

Although both controller designs can yield satisfactory performance, in this case, the pole placement method offers superior results. By eliminating a pole from the system, the complexity decreases, making it easier to modify the system characteristics. Additionally,

the pole placement method requires a smaller gain, simplifying the implementation process.

However, it is important to note that in certain systems, eliminating a pole may not be desired or feasible. In such cases, the graphical method for designing a phase lead controller is more appropriate. Overall, the choice between the two methods depends on the specific system requirements and constraints.

To summarize, the comparison of the effectiveness, performance, and simplicity of the two, phase lead controller designs is subjective and depends on the specific system characteristics. While the pole placement method generally offers advantages in terms of simplicity and performance, the graphical method remains a viable option in systems where pole elimination is not desired.

4 Phase Lag Compensator

In this problem, the task is to calculate the steady-state error constant for the slope input (k_{ν}) in such a way that its value becomes greater than or equal to 40. Initially, the current value of this constant in the controller will be examined to determine if any controller design is necessary.

$$k_{v,old} = \lim_{s \to 0} SG_C(S)G(S)$$
 (4-1)

It is clear that the current value deviates from the desired value, indicating the need for controller design.

In general, a phase lag compensator is defined as follows.

$$G_c(s) = \hat{k} \frac{s+z}{s+p} \quad z > p > 0$$
 (4-2)

After careful observation, it has been observed that the steady-state error constant for the slope input (k_v) in this system is lower than the desired value. The calculation will now be performed to determine the exact value.

4.1 Phase lag on the graphical method

The calculations for the phase lag compensator are as follows.

$$k_{v,old} = \lim_{s \to 0} \frac{17.04(2s+1)(s+4.55)}{(s+1)(s+2)(s+6.63)} \approx 5.85$$
 (4-3)

$$k_{v,ne\omega} = 6,90k_{v,old} = 40$$
 (4-4)

Therefore, it can be said that:

$$\frac{k_{v,new}}{k_{v,old}} = \frac{Z}{P} = 6.90 \qquad (4-5)$$

$$\frac{C(S)}{R(S)} = \frac{G_c(S)G(S)}{1 + G_c(S)G(S)} \tag{4-6}$$

The roots of the transfer function of the closed-loop system are calculated, and the dominant pole is determined below.

$$\frac{\frac{17.04(2s+1)(s+4.55)}{(s+1)(s+2)(s+6.63)}}{1+\frac{17.04(2s+1)(s+4.55)}{(s+1)(s+2)(s+6.63)}} = \frac{34.08s^2 + 172.104s + 77.532}{s^4 + 9.63s^3 + 55.97s^2 + 185.364s + 77.532}$$
(4-7)

$$s_1 = -0.48$$
 $s_2 = -5.18$ $s_3 = -1.99 + 5.20i$ $s_4 = -1.99 - 5.20i$ (4-8)

Therefore, the dominant pole is s1.

$$z = \frac{|\alpha|}{10} \to \alpha = 0.48 \to z = 0.048$$
 (4-9)

$$\frac{Z}{P} = 6.90$$
, $z = 0.048 \rightarrow P = \frac{0.048}{6.90} = 0.007$ (4-10)

As the result the zero and pole of the lag compensator are determined and the phase lag compensator is.

$$G_l(S) = \frac{s + 0.049}{s + 0.0069} \tag{4-11}$$

The final designed lead-lag controller is:

$$G_c(S) = \frac{17.04(s+0.049)(s+4.55)}{(s+0.0069)(s+6.63)}$$
(4-12)

4.2 Phase lag on the graphical method

The steps in this section are similar to the previous one.

$$\frac{\frac{14.72(s+2)}{(s+3,47)}}{1+\frac{14.72(s+2)}{(s+3,47)}} = \frac{29.44 \, s + 14.72}{s^3 + 4.44s^2 + 32.88s + 14.72} \tag{4-13}$$

The dominant pole of this system is on -0.47

$$k_{v,old} = \lim_{s \to 0} \frac{14.72(2s+1)}{(s+1)(s+3.47)} \approx 4.28$$
 (4-14)

$$\frac{k_{v,new}}{k_{v,old}} = \frac{z}{p} = 9.35 \tag{4-15}$$

$$z = \frac{|\alpha|}{10} \to \alpha = -0.47 \to z = 0.047$$
 (4-16)

$$\frac{Z}{P} = 9.35$$
, $z = 0.048 \rightarrow P = \frac{0.047}{9.35} = 0.0050$ (4-17)

Therefore, the phase lag controller is:

$$G_l(S) = \frac{s + 0.047}{s + 0.0050} \tag{4-18}$$

The final controller on this method is:

$$G_c(S) = \frac{14.72(s+0.047)(s+2)}{(s+0.0050)(s+3.47)}$$
(4-19)

4.3 Result of the design

After completing the design of the controllers, the overall performance can now be evaluated by examining the root locus diagram of the final systems and analyzing the corresponding step response. The results are presented in Figures 4-1 to 4-4.

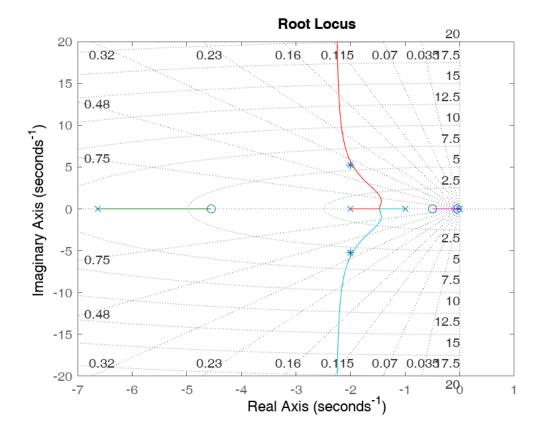
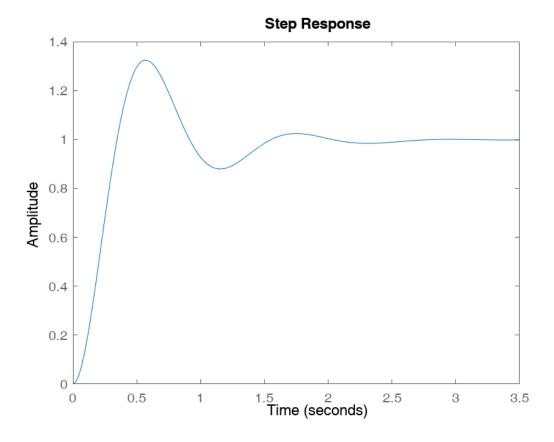


Figure 4-1 root locus of the system designed based on graphical phase lead design



stepinfo(temp)

ans = struct with fields:
 RiseTime: 0.2340
TransientTime: 1.8463
SettlingTime: 1.8463
SettlingMin: 0.8796
SettlingMax: 1.3242
Overshoot: 32.4233
Undershoot: 0
 Peak: 1.3242
PeakTime: 0.5591

Figure 4-2 step response and step info of this system

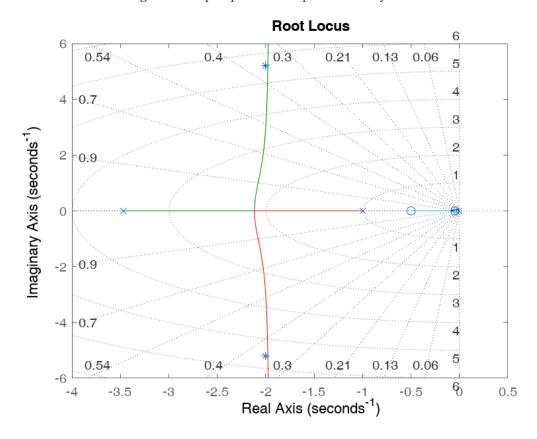
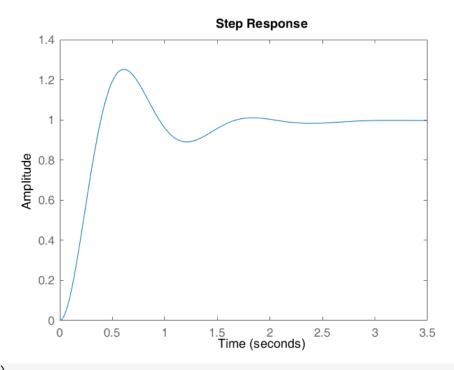


Figure 4-3 root locus of the system designed based on graphical phase lead design



stepinfo(temp)

ans = struct with fields:
 RiseTime: 0.2641
TransientTime: 1.5807
SettlingTime: 1.5807
SettlingMin: 0.8899
SettlingMax: 1.2520
Overshoot: 25.2034
Undershoot: 0
Peak: 1.2520
PeakTime: 0.6054

Figure 4-4 step response and step info of this system

It is evident that by adding a phase lag compensator in both the graphical and pole elimination methods, the root locus plots deviate slightly from the ideal behavior. However, the change introduced by the pole elimination method is less significant. It was expected that injecting a new zero and pole into the system would result in a small change, which was intentionally done to reduce the steady-state error of the system.

5 Conclusion

In general, it became apparent from the examination of the system in its initial state that the desired points on the root locus plot are not achieved, indicating the need for controller design.

For the design of the phase lead controller using the graphical method, precise calculations were performed, and to ensure accuracy, the calculations were repeated using MATLAB software. Based on the obtained values, the controller was designed, and it was observed that the desired points were achieved on the root locus plot.

Following the design of the graphical method controller, the controller using the pole elimination method was also calculated and designed for comparison. By plotting the root locus plots for both methods, it was evident that in both cases, the desired points were successfully located on the root locus plot.

With the designed phase lead controller, the required condition of reducing the steady-state error to a given value was examined. Due to the insufficient conditions of the system, the need for a phase lag controller arose. By performing the necessary manual and software calculations, a phase lag controller was designed for both previous controllers, and the root locus plots were plotted again.

By observing the root locus and analyzing the step response of the designed system, it was determined that the graphical method had a slightly higher overshoot value compared to the desired 30%. This result is attributed to the effect of the phase lag controller, and this issue can be resolved by making a slight adjustment to the gain value (without significantly altering the root locations). For example, reducing the gain by 0.5 would result in an overshoot of 30%.

However, in the case of the system designed using the pole elimination method, it was observed that both overshoot and settling time deviated less significantly from the desired values. As observed in the root locus plot, the effect of the phase lag controller on the system designed using the pole elimination method is less significant compared to the graphical method, and some of this deviation is due to this factor.

Furthermore, by examining the influential factors on this system, it can be concluded that removing a pole from the system leads to unintended consequences, which are observed in the higher-order behavior of the system. Despite performing calculations and obtaining ideal results in the calculations, practical observations indicate a discrepancy between the obtained and desired results. By making a slight adjustment to the gain value, without significantly altering the root locations, this state can be improved.

Another approach to reducing the error is to calculate a controller with an overshoot value less than 30% and injecting a phase lag controller with an error calculation reaching 30%.

5.1 Applying the suggested method

As the steps for designing this controller are applied in previous chapter, only the calculations will be shown in this section. The lead compensator will be designed such that the overshoot is 28 percent but no change is needed in settling time condition as it was evident that settling time was not a challenging parameter and didn't surpass the limit in previous designs.

$$M_{P} = 28\% \rightarrow \xi = \frac{\ln(M_{p})}{\sqrt{\ln^{2}(M_{P}) + \pi^{2}}} \qquad (5-1)$$

$$(ln(0.28) = 3.33, \pi^{2} = 9,87) \rightarrow \frac{3.33}{\sqrt{20.96}} = \frac{3.33}{4.57} \approx 0.376 \quad (5-2)$$

$$t_{P} = \frac{4}{\omega_{n}\xi} \rightarrow t_{pmax} = 2s \rightarrow \frac{4}{\omega_{n}\xi} = 2s \rightarrow \omega_{n} = \frac{4}{2\xi} \quad (5-3)$$

$$\omega_{n} = \frac{4}{0.75} = 5.33 \quad (5-4)$$

$$\beta = \cos^{-1}(0.376) = 67.91, \quad \omega_{n}\xi = 2, \quad \omega_{n}\sin(\beta) = 4.94 \quad (5-5)$$

$$\theta_{P} = \rightarrow \tan(\theta_{P}) \approx 1.1 \rightarrow P = \left(\frac{4.94}{1.1}\right) + 2 = 6.49 \quad (5-6)$$

$$\theta_{Z} = 63.95 \rightarrow \tan(\theta_{Z}) = 1.996 \rightarrow Z = \left(\frac{4.94}{2}\right) + 2 = 4.47 \quad (5-7)$$

$$|k| = \frac{s(s+1)(s+2)(s+6.49)}{(2s+1)(s+4.47)} \rightarrow s = -2 \pm 4.94j \rightarrow k \approx 15.53 \quad (5-8)$$

$$\frac{15.53(2s+1)(s+4.47)}{(s+1)(s+2)(s+6.49)} \rightarrow s = -2 \pm 4.94j \rightarrow k \approx 15.53 \quad (5-8)$$

$$\frac{15.53(2s+1)(s+4.47)}{(s+1)(s+2)(s+6.49)} = \frac{31.06s^{2} + 154.368s + 69.4149}{s^{3} + 40.55s^{2} + 175.838s + 82.3991}$$

$$\rightarrow s_{dom} = -0.49$$

$$z = \frac{|\alpha|}{10} \rightarrow \alpha = 0.49 \rightarrow z = 0.049$$

$$z = \frac{|\alpha|}{10} \rightarrow \alpha = 0.49 \rightarrow z = 0.049$$

$$\frac{Z}{P} = 7.59, z = 0.049 \rightarrow P = \frac{0.049}{7.59} = 0.0064$$

As the result, the designed controller is:

$$G_c(S) = \frac{15.53(s+0.049)(s+4.47)}{(s+0.0064)(s+6.49)}$$

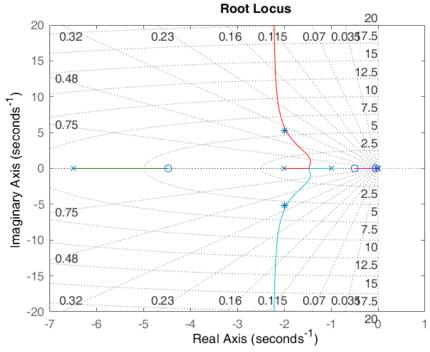
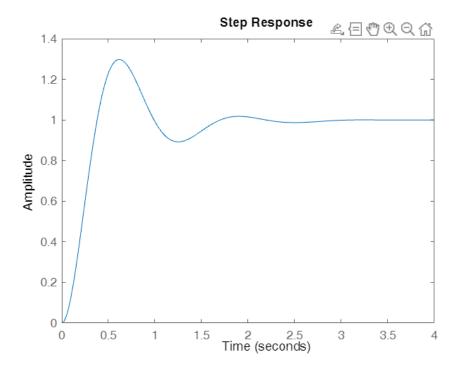


Figure 5-1 root locus of the suggested compensator design



ans = struct with fields: RiseTime: 0.2574

stepinfo(sys5)

TransientTime: 1.6142
SettlingTime: 1.6142
SettlingMin: 0.8921
SettlingMax: 1.2981
Overshoot: 29.8100
Undershoot: 0
Peak: 1.2981
PeakTime: 0.6201

Figure 5-2 step response and step info of the said system

As observed, the system was successfully brought close to an overshoot of 30% and the settling time was reduced by implementing the new controller. A suitable controller was designed according to the problem requirements. The settling time can be further adjusted by making slight changes to the system gain or by designing the system with slightly different assumed parameters to slightly increase the settling time.