

Circuit for batching Groth16 proofs

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For reference, recall the **Groth16** verification equation.

$$e(\pi_A, \pi_B) = \epsilon(\pi_C, [\delta]_2) \cdot \text{PI}$$

Where PI is an element in \mathbb{G}_t derived by the verifier from the public input. We move the second pairing to the LHS to receive

$$e(\pi_A, \pi_B) \cdot \epsilon(-\pi_C, [\delta]_2) = \text{PI}$$

We use **ML** to denote a miller loop and **FE** to denote a multiexponentiation

The private input for the circuit is a set of m **Groth16** proofs $S := \{\pi_{A,i}, \pi_{B,i}, \pi_{C,i}\}_{i \in [m]}$

The public inputs of the circuit are

1. P - the alleged pedersen hash of all proof elements
2. F - the alleged correct randomized combination of pairings of proof elements
3. and $r = \text{Blake}(P)$ interpreted as an element of \mathbb{F} .

The verifier computes r from P outside of the circuit, and checks outside of the circuit that

$$F = \prod_{i \in m} \text{PI}_i^{r^i}$$

The circuit computes

1. $M := \prod_{i \in [m]} \text{ML}(\pi_{A,i}, \pi_{B,i})$.
2. $C' := - \sum_{i \in [m]} r^i \cdot \pi_{C,i}$
3. $C = \text{ML}(C', [\delta]_2)$

The circuit checks that

1. $F = \text{FE}(M \cdot C)$.
2. $P = \text{ped}(S)$.

References