## Circuit for batching Groth16 proofs

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For reference, recall the Groth16 verification equation.

$$e(\pi_A, \pi_B) = \epsilon(\pi_C, [\delta]_2) \cdot \mathsf{PI}$$

Where PI is an element in  $\mathbb{G}_t$  derived by the verifier from the public input. Specificially,

$$\mathsf{PI} = \left[ \alpha \beta + K_0 + \sum_{i=1}^{\ell} a_i \cdot K_i \right]_t$$

where  $(a_1, \ldots, a_n)$  is the public input, and  $K_i = \beta u_i(x) + \alpha v_i(x) + w_i(x)$ .

We move the second pairing to the LHS to receive

$$e(\pi_A, \pi_B) \cdot \epsilon(-\pi_C, [\delta]_2) = \mathsf{PI}$$

We use ML to denote a miller loop and FE to denote the final exponetiation. We assume the verifer and prover both know the set of public inputs and thus the derived values  $\{PI_i\}_{i\in[m]}$ . The private input for the circuit is a set of m Groth16 proofs  $S := \{\pi_{A,i}, \pi_{B,i}, \pi_{C,i}\}_{i\in[m]}$ 

The public inputs of the circuit are

- 1. P the alleged pedersen hash of all proof elements.
- 2. F the alleged correct randomized combination of pairings of proof elements.
- 3.  $r = \mathsf{Blake}(\mathsf{P}, (\mathsf{Pl}_1, \dots, \mathsf{Pl}_{\mathsf{m}}))$ , interperted as an element of  $\mathbb{F}$ .

The verifier has the corresponding m public input elements  $\mathsf{PI}_1,\ldots,\mathsf{PI}_m,$  and checks outside of the circuit that

$$F = \prod_{i \in m} \operatorname{Pl}_i^{r^i}$$

The circut computes

- 1.  $M := \prod_{i \in [m]} \mathsf{ML}(r^i \cdot \pi_{\mathsf{A},\mathsf{i}}, \pi_{\mathsf{B},\mathsf{i}}).$
- $2. C' := -\sum_{i \in [m]} r^i \cdot \pi_{\mathsf{C},\mathsf{i}}$
- $3. \ C = \mathsf{ML}(C', [\delta]_2)$

The circuit checks that

- 1.  $F = \mathsf{FE}(M \cdot C)$ .
- 2. P = ped(S).

**Remark 0.1.** It is possible to not do FE inside the circuit and have the verifier do it outside; however since it's only once per batch it might be better to leave it in the circuit.

## References