PLONK: Permutations over Lagrange-Bases for Oecumenical Noninteractive arguments of Knowledge

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Plonk in two sips

- 1. All you need is a permutation check.
- 2. Permutations are easier to check on mutliplicative subgroups

Part 1: All you need is a permutation check

Our setting: want short proofs about fan-in 2 unlimited fan-out circuits, trusted setup depends only on circuit size.

example: Prove knowledge of a, b, c with

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$$a, b, c$$
 with

 $(a+b) \cdot c = 7$

Right values: \mathbf{r}_1 , \mathbf{r}_2 Output values: \mathbf{o}_1 , \mathbf{o}_2

Left values: l_1 , l_2

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Output values: \mathbf{o}_1 , \mathbf{o}_2

Right values: \mathbf{r}_1 , \mathbf{r}_2

Wire/copy checks: $\mathbf{o}_1 = \mathbf{l}_2$ Public input checks: $o_2 = 7$.

Gate checks: $l_1 + r_1 = o_1$, $l_2 \cdot r_2 = o_2$

Left values: l_1, l_2

Right values: \mathbf{r}_1 , \mathbf{r}_2

Output values: o_1 , o_2

Wire/copy checks: $o_1 = l_2$ (hard) Public input checks: $o_2 = 7$ (easy)

Gate checks: $l_1 + r_1 = o_1, l_2 \cdot r_2 = o_2$ (easy)

Copy checks with permutations similar to [Groth09,BCGGHJ17]

$$\mathbf{V} = (\mathbf{l}_1, \mathbf{l}_2, \mathbf{r}_1, \mathbf{r}_2, \mathbf{o}_1, \mathbf{o}_2)$$

Copy checks with permutations similar to [Groth09,BCGGHJ17]

$$\mathbf{V} = (\mathbf{l}_1, \mathbf{l}_2, \mathbf{r}_1, \mathbf{r}_2, \mathbf{o}_1, \mathbf{o}_2)$$
$$-\boldsymbol{\sigma}(\mathbf{V})$$

 $\mathbf{o}_1 = \mathbf{l}_2$ iff $\mathbf{V} = \mathbf{\sigma}(\mathbf{V})$ For permutation $\mathbf{\sigma} = (25)$

Part 2: Permutations are easier to check on mutliplicative subgroups

[Bayer-Groth12] - perm checks with products

example: Given $a, b \in \mathbb{F}^3$, want to check $(b_1, b_2, b_3) = (a_3, a_1, a_2)$

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step 1: Choose random $\beta \in \mathbb{F}$. Let

$$a'_1 = a_1 + \beta$$
, $a'_2 = a_2 + 2\beta$, $a'_3 = a_3 + 3\beta$
 $b'_1 = b_1 + 3\beta$, $b'_2 = b_2 + \beta$, $b'_3 = b_3 + 2\beta$

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If claim false - w.h.p as mutliset $\{\alpha'_1, \alpha'_2, \alpha'_3\} \neq \{b'_1, b'_2, b'_3\}$:

[Bayer-Groth12] - reducing permutation checks to products

step 2: Choose random $\gamma \in \mathbb{F}$. Let

$$a_i'' = a_i' + \gamma, b_i'' = b_i + \gamma$$

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step 2: Choose random $\gamma \in \mathbb{F}$. Let

$$a_i'' = a_i' + \gamma$$
, $b_i'' = b_i + \gamma$

If
$$\{a_1', a_2', a_3'\} \neq \{b_1', b_2', b_3'\}$$
 as multiset - w.h.p

$$\alpha_1'' \cdot \alpha_2'' \cdot \alpha_3'' \neq b_1'' \cdot b_2'' \cdot b_3''.$$

Idealized Polynomials Protocols

Preprocessing: \mathcal{V} chooses polynomials $g_1, \ldots, g_t \in \mathbb{F}_{< d}[X]$.

Protocol:

- 1. \mathcal{P} 's msgs are to ideal party \mathbf{I} . Must be $f_i \in \mathbb{F}_{<\mathbf{d}}[\mathbf{X}]$.
- 2. At protocol end \mathcal{V} asks \mathbf{I} if some identities hold between $\{\mathbf{f}_1, \ldots, \mathbf{f}_{\ell}, g_1, \ldots, g_t\}$. Outputs \mathbf{acc} iff they do.

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Using [KZG10], can compile to real protocol with each msg of \mathfrak{P} being 32-64 bytes according to your NFSPL.

H-ranged Polynomials Protocols

Preprocessing: \mathcal{V} chooses polynomials $g_1, \ldots, g_t \in \mathbb{F}_{< d}[X], \ H \subset \mathbb{F}$.

Protocol:

- 1. \mathcal{P} 's msgs are to ideal party \mathbf{I} . Must be $\mathbf{f_i} \in \mathbb{F}_{<\mathbf{d}}[\mathbf{X}]$.
- 2. At end, \mathcal{V} asks \mathbf{I} if some identities hold between $\{f_1, \ldots, f_\ell, g_1, \ldots, g_t\}$ on \mathbf{H} .

H-ranged protocol using polynomial protocol:

 \mathcal{V} wants to check identities P_1 , P_2 on H.

- ▶ After \mathcal{P} finished sending $\{f_i\}$, \mathcal{V} sends random $\mathfrak{a}_1, \mathfrak{a}_2 \in \mathbb{F}$.
- $ightharpoonup \mathcal{P}$ sends $\mathsf{T} \in \mathbb{F}_{< d}[\mathsf{X}]$.
- $ightharpoonup \mathcal{V}$ checks identity $a_1 \cdot P_1 + a_2 \cdot P_2 \equiv T \cdot Z_H$.

Checking permutations with **H**-ranged protocols

Permutation $\sigma:[n] \to [n]$. $H = \{\alpha, \alpha^2, ..., \alpha^n\}$.

 $\mathcal P$ has sent $f \in \mathbb F_{< d}[X]$.

Wants to prove $f = \sigma(f)$:

$$\forall i \in [n], f(\alpha^i) = f(\alpha^{\sigma(i)})$$

Using [BG12] reduces to:

$$H = \left\{\alpha, \, \alpha^2, \, \ldots, \, \alpha^n \right\}.$$

$${\mathcal P}$$
 has sent f, $g\in {\mathbb F}_{{<\!d}}[X].$

Wants to prove:

$$\prod_{i \in [n]} f(\alpha^i) = \prod_{i \in [n]} g(\alpha^i)$$

Checking products with **H**-ranged protocols

- 1. \mathcal{P} computes \mathbf{Z}_f with $\mathbf{Z}_f(\alpha) = 1$, $\mathbf{Z}_f(\alpha^i) = \prod_{j < i} f(\alpha^j)$, i = 2..n + 1. Computes \mathbf{Z}_g similarly.
- 2. Sends Z_f , Z_g to I.

Checking products with **H**-ranged protocols

- 1. \mathcal{P} computes Z_f with $Z_f(\alpha) = 1$, $Z_f(\alpha^i) = \prod_{j < i} f(\alpha^j)$. Computes Z_g similarly.
- 2. Sends Z_f , Z_g to I.
- 3. \mathcal{V} checks following identities on \mathbf{H} .
 - 3.1 $L_1(X)(Z_f(X) Z_g(X)) = 0$
 - 3.2 $Z_f(X)f(X) = Z_f(\alpha \cdot X)$
 - 3.3 $Z_g(X)f(X) = Z_g(\alpha \cdot X)$
 - 3.4 $L_n(X)(Z_f(\alpha \cdot X) Z_g(\alpha \cdot X)) = 0$

The bottom line (on BLS-381 curve)

700 byte proofs with one trusted setup for all fan-in two circuits of ${\bf n}$ gates.

Prover does $12n G_1 \exp$.

For batch of proofs on same circuit only 3n G_1 exp and 240 bytes for each additional proof.

Bonus material: The KZG polynomial commitment scheme

SRS: $[1],[x],\ldots,[x^d]$, for random $x \in \mathbb{F}$.

$$\mathcal{L}_{\mathcal{L}}$$

 $\mathsf{cm}(\mathsf{f}) \coloneqq \textstyle \sum_{i=0}^d \alpha_i \left[x^i \right] = \left[\mathsf{f}(x) \right]$

 $f(X) = \sum_{i=0}^{d} \alpha_i X^i$

SRS: $[1],[x],...,[x^d],$ for random $x \in \mathbb{F}$.

for random
$$x \in \mathbb{F}$$
.

cm(f) := [f(x)]

open(f, i) := [h(x)], where $h(X) := \frac{f(X) - f(i)}{X - i}$

$$cm(f) := [f(x)]$$

$$\mathsf{open}(\mathsf{f},\mathfrak{i}) \coloneqq [\mathsf{h}(x)] \text{, where } \mathsf{h}(X) \coloneqq \tfrac{\mathsf{f}(X) - \mathsf{f}(\mathfrak{i})}{X - \mathfrak{i}}$$

verify(cm,
$$\pi$$
, z , i):

 $e(cm - [z], [1]) \stackrel{?}{=} e(\pi, [x - i])$

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verify(cm, π , z, i):

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Thm[KZG,MBKM]: This works in the Algebraic Group

Model.