

# **Digital Logic Design (CS302)**

Assignment # 01 Spring 2024

Total marks = 20 Deadline 16<sup>th</sup> of November 2024

Please carefully read the following instructions before attempting the assignment.

### **RULES FOR MARKING**

It should be clear that your assignment would not get any credit if:

- The assignment is submitted after the due date.
- The submitted assignment does not open, or the file is corrupted.
- Strict action will be taken if the submitted solution is copied from any other student or the internet.

You should consult the recommended books to clarify your concepts, as handouts are insufficient.

You are supposed to submit your assignment in <a href="Doc or Docx">Docx</a> format.

Any other formats like scanned images, PDF, ZIP, RAR, PPT, BMP, etc. will not be accepted.

### **Topic Covered:**

The objective of this assignment is to assess the understanding of students about:

- Boolean Algebra and Logic Simplification
- Karnaugh Map & Boolean Expression Simplification

### **Topic Covered**

Lecture 01 to Lecture 11

#### NOTE

No assignment will be accepted <u>via email after the due date</u> (whether it is due to load shedding, internet malfunctioning, etc.). Hence, refrain from uploading assignments within the last hour of the deadline. It is recommended that the solution be uploaded at least two days before its closing date.

If you find any mistakes or confusion in the assignment (Question statement), please consult your instructor before the deadline. After the deadline, no queries will be entertained in this regard.

For any query, feel free to email me at: CS302@vu.edu.pk

Question No 01 Marks (20)

Convert the following 32-Bit Binary Floating-Point Number into a Decimal Number.

## 110001001101101010110101111000111

Note: Please perform all steps required for conversion of a 32-Bit Binary Floating-Point Number. In case of missing steps, marks will be deducted.