

Kento Sato

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EDUCATION

- Mar., 2014 **Ph.D. in Science**, *Department of Mathematical and Computing*, Tokyo Institute of Technology, *Tokyo, Japan*.
- Mar., 2010 **Master of Science**, *Department of Mathematical and Computing*, Tokyo Institute of Technology, *Tokyo, Japan*.
- Mar., 2008 **Bachelor of Science**, *Department of Information Science*, Tokyo Institute of Technology, *Tokyo, Japan*.
 - Graduated top of the department

WORK EXPERIENCE

- Sep., 2014 - present **Postdoctoral Researcher**, *Center for Applied Scientific Computing*, Lawrence Livermore National Laboratory, *CA, USA*.
 - PRUNER project (Providing Reproducibility on Ubiquitously Non-deterministic Environments and Runs)
 - Extreme Scale Resilience project
- Apr., 2014 - Aug., 2014 **Postdoctoral Researcher**, *Global Scientific Information and Computing Center*, Tokyo Institute of Technology, *Tokyo, Japan*.
 - Extreme Scale Resilience project
- Apr., 2012 - Mar., 2014 **JSPS research fellow**, *DC2*, JSPS (Japan Society for the Promotion of Science), *Tokyo, Japan*.
 - I/O optimization in cluster, cluster and grid environments
- July, 2013 - Oct., 2013 **Research Intern**, *Center for Applied Scientific Computing*, Lawrence Livermore National Laboratory, *CA, USA*.
 - Developed a Fault tolerant Messaging Interface(FMI);
 - Developed an I/O interface(IBIO) and a resiliency model for burst buffer
- Aug., 2012 - Nov., 2012 **Research Intern**, *Center for Applied Scientific Computing*, Lawrence Livermore National Laboratory, *CA, USA*.
 - Developed a Fault tolerant Messaging Interface(FMI)
- Aug., 2011 - Nov., 2011 **Research Intern**, *Center for Applied Scientific Computing*, Lawrence Livermore National Laboratory, *CA, USA*.
 - Developed an asynchronous checkpointing system
- Oct., 2010 - Mar., 2011 **Research Staff**, *Global Scientific Information and Computing Center*, Tokyo Institute of Technology, *Tokyo, Japan*.
 - I/O optimization in grid environments

- Apr., 2010 - **Associate, Equity Operation**, J.P.Morgan Securities Japan Co., Ltd., *Tokyo, Japan*.
Aug., 2010
 - Trading system monitoring
 - FIX (Financial Information Exchange) system log analysis
 - Release of algorithm trading programs
 - In-house user service
- Oct., 2006 - **Business Intern, Equity Research**, Deutsche Securities Inc., *Tokyo, Japan*.
Jan., 2008
 - Developed a financial chart viewer library and a web interface to SQL databases

PROFESSIONAL ACTIVITIES

- 2018 International Conference on High Performance Computing in Asia-Pacific Region (HPC Asia), Technical Program Committee
- 2017 ACM/IEEE Supercomputing Conference (SC), Posters Committee
- 2017 IEEE ICPP, Technical Program Committee
- 2017 IEEE Cluster, Proceedings Chair
- 2017 IEEE Cluster, Technical Program Committee
- 2016 IEEE Cluster, Technical Program Committee
- 2016 IEEE/ACM CCGrid, Technical Program Committee
- 2016 International Workshop on Fault Tolerant Systems (FTS), Technical Program Committee
- 2015 IEEE Cluster, Technical Program Committee
- 2015 International Workshop on Fault Tolerant Systems (FTS), Technical Program Committee
- 2014 IEEE Transactions on Parallel and Distributed Systems (Journal), Reviewer
- 2014 Concurrency and Computation: Practice and Experience (Journal), Reviewer
- 2013 IEEE Transactions on Parallel and Distributed Systems (Journal), Reviewer
- 2013 Parallel Computing (Journal), Reviewer

AWARDS & SCHOLARSHIP

- Feb., 2015 Seiichi Tejima Doctoral Dissertation Award
- Mar., 2014 IEEE/ACM CCGrid2014 Best Paper Award
- Apr., 2012 Japanese Society for the Promotion of Science (JSPS) scholarship for two years, (Research fund: \$10k/year)
- Mar., 2008 Graduated top of the Department of Information Science, Tokyo Institute of Technology, Awarded

GRADUATE ADVISOR

Satoshi Matsuoka, Tokyo Institute of Technology