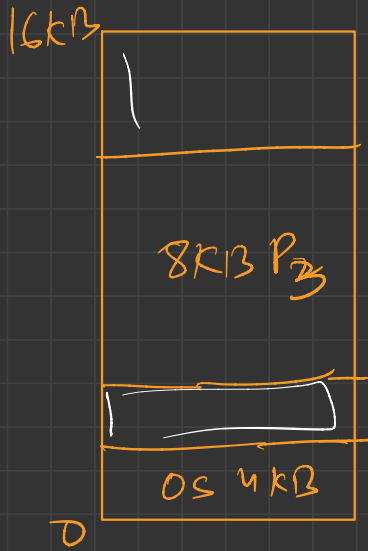


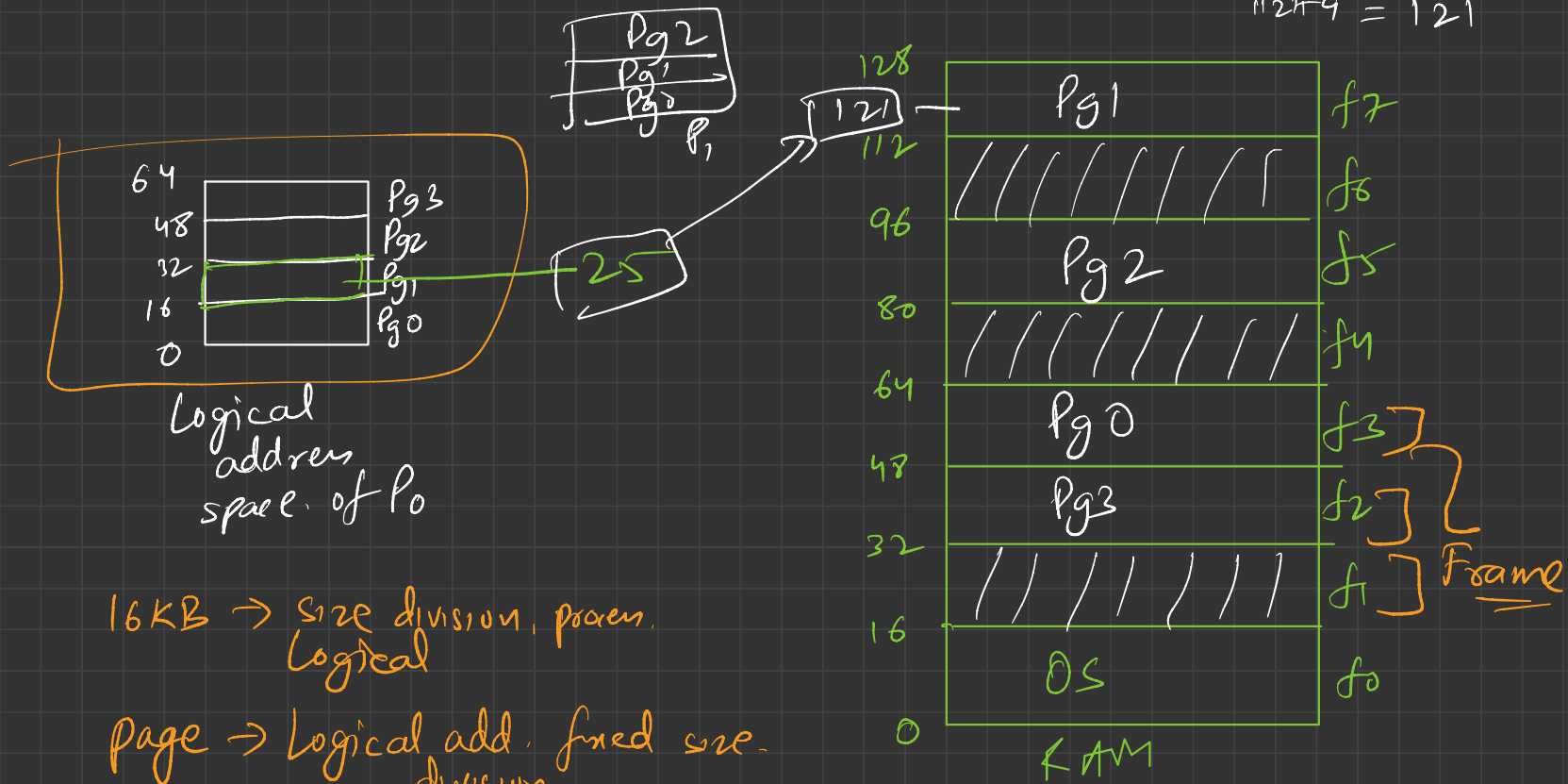

Lec-26



2KB → 12KB — mul.

$P_n \Rightarrow 3KB$

$$112 + 9 = 121$$



16KB \rightarrow size division, process.
Logical

Page \rightarrow Logical add. fixed size.
division

$$\text{Page size} = \text{frame size}$$

* Page table

logical page no.

00
01
10
11

pg no
1
2
3

physical frame no.

frame 3 011
7 → 111
5 101
2 010

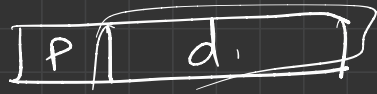
⇒ logical address → P₆ → 64 bytes,
 $2^6 = 64$

6 bits ⇒

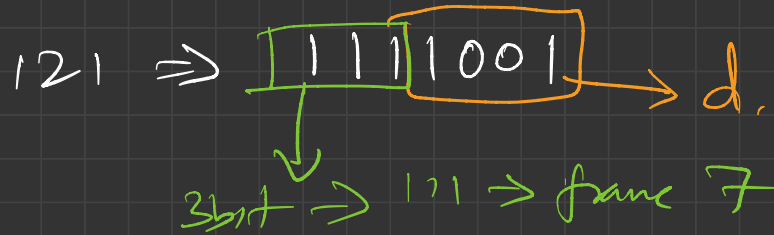
25 = 011001 → offset
↓
page no. 1 1001

Pg no 1 → Base → $16 + 9 \Rightarrow$ 25 9

Logical address space

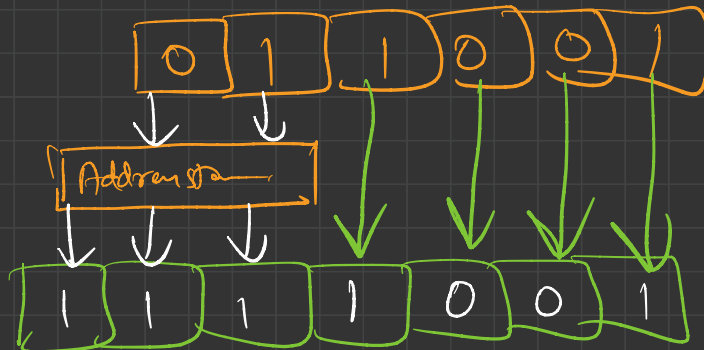


Physical address

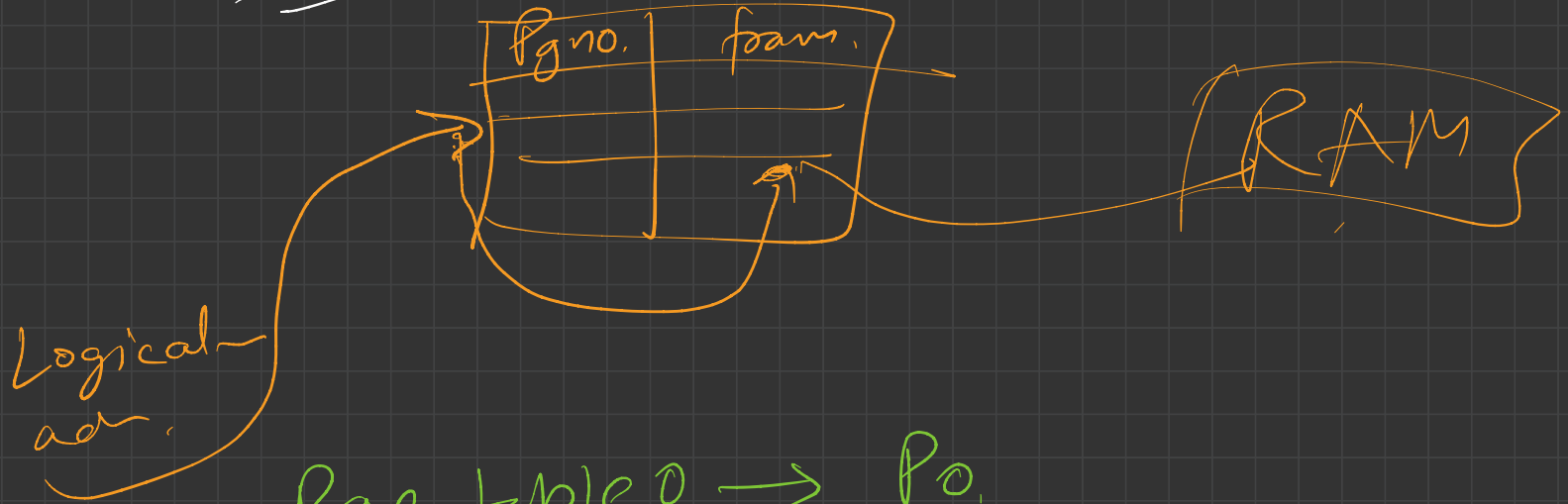


Logical address

Physical address ->



* TLB



Page table₀ → P₀

PT₁ → P₁

PT₂ → P₂

P₀ →

P₀ →

Pgn ₀	fno.
10	100

TCB

Content S
↓ TCB reset

P₁ →
↓
10 2

Prg	fn

ASID	Pgn ₀	fno.
0	10	100
1	10	45

① flush
the TCB
when context
switches
occur

② unique identifier
that will identify
unique process.