Aris Magklaras

SW Architect / Lead SW Engineer ASML B.V. Netherlands

PhD Candidate, Combinatorial Optimization and Optimal Experimental Design, University of Patras and ASML B.V. Netherlands

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Experience

SW Architect, *ASML B.V., Netherlands*
January 2023 – today

Owner at Technithon, Netherlands

May 2023 - today

SW Engineer, ASML *B.V., Netherlands*
January 2019 – January 2023

PhD Candidate, University of Patras, Greece, ECE Department

January 2019 – today

Education

2012 – 2018 BS & MSc Electrical and Computer Engineering

Skills

SW Architecture, SW Engineering, Systems Engineering, Programming, Problem Solving, Unix, Linux, Git, SVN
Combinatorial Optimization, Forecasting, Digital Twins, Smart Electricity Grids, Energy Markets, Machine learning, Deep Learning, Reinforcement Learning,
Optimal Experimental Design, Semiconductors Manufacturing, Photolithography

Programming Languages

(Professional Experience in) C++, C, Python, MATLAB

Useful links:

[Google Scholar Profile](), [LinkedIn Profile](), [Technithon Profile]()