

Project Report

Design a full-fledged network for an organization with multiple subnets.

Course Title: Computer Networks

Course Code: CSE405

Section: 04

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Title:

Designing a Full-fledged Network for an Organization with Multiple Subnets.

Preface:

University of Scholars, is an enterprise like East West University, owns many computers, with a complex network infrastructure. Apart from wired internet access to all the classrooms, labs, employee PCs, library and other administrative and academic wings, the university also provides wireless internet access for every campus. On top of that the university runs a complex networked systems to support several of its business process like admissions, advising, results, eTender, library management, accounts and so on. The task is to create a complete model of a complex network by discovering the interconnectivity of the systems and subnetworks, which will reflect the University of Scholars structure and facilities, features within the network.

Tools:

Components Used:

- 1. PT-Router
- 2. Wireless Routers
- 3. Straight Through Cable
- 4. Serial DCE cables
- PT- Switches
- 6. PC as end devices
- 7. DNS Server
- 8. Web Server
- 9. DHCP server
- 10.Laptop, Smart Phone

Software Used:

Cisco Packet Tracer version 8.2.1

Physical Diagram: ALL Campus.

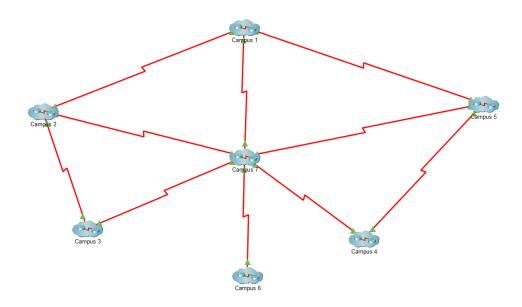


Figure 1: Network Model created in Cisco Packet Trace

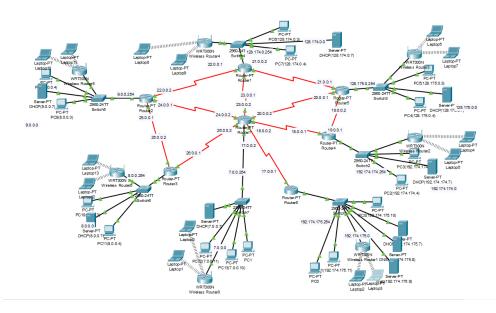


Figure 2: Network Model created in Cisco Packet Trace

Design Issues:

There is a problem of Cisco Packet tracer that we have to send a ICMP packet for 2/3 time or its failed but after that its works perfectly.

Number of Hosts:

Total number of hosts is: 30

Number of Networks:

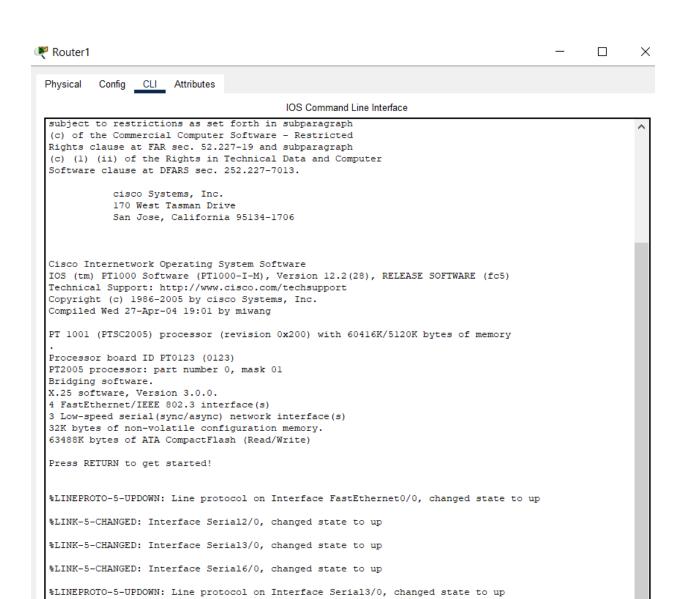
Total number of networks is: 20

Limitations:

The network is very complex. Maintaining this network can create problems. More campus networks cannot be added very easily. To add more networks, manual configuration is needed. The network can support a limited number of hosts.

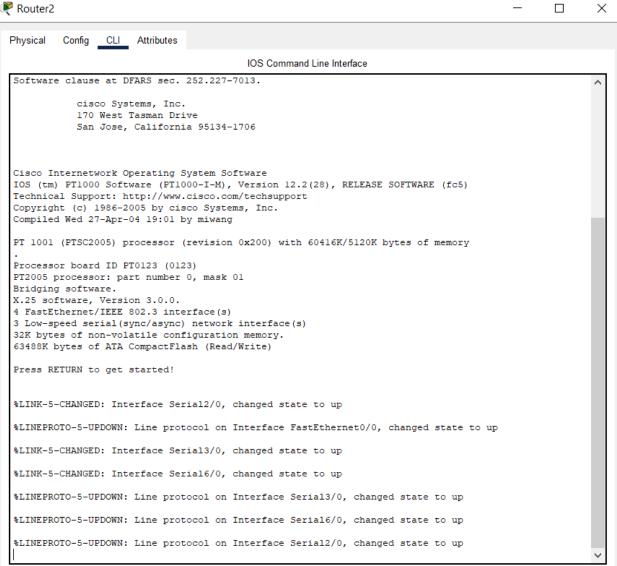
Lines of Code:

Router Configuration Code:



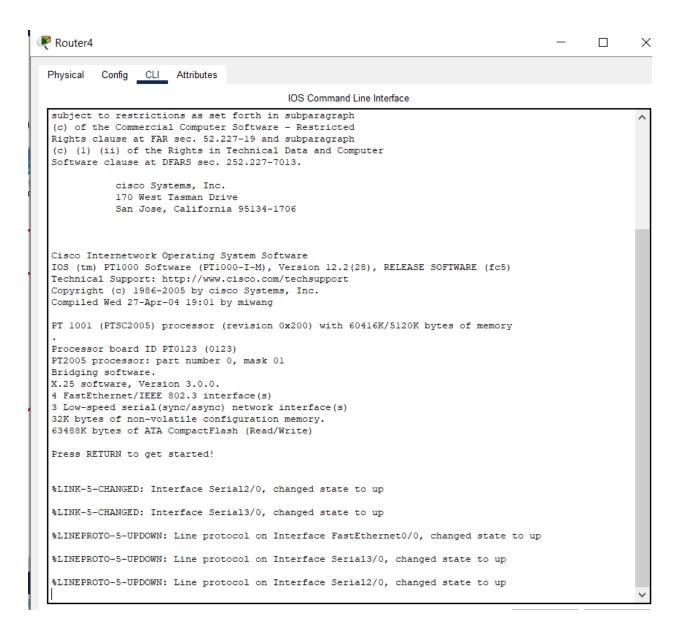
%LINEPROTO-5-UPDOWN: Line protocol on Interface Serial2/0, changed state to up
%LINEPROTO-5-UPDOWN: Line protocol on Interface Serial6/0, changed state to up

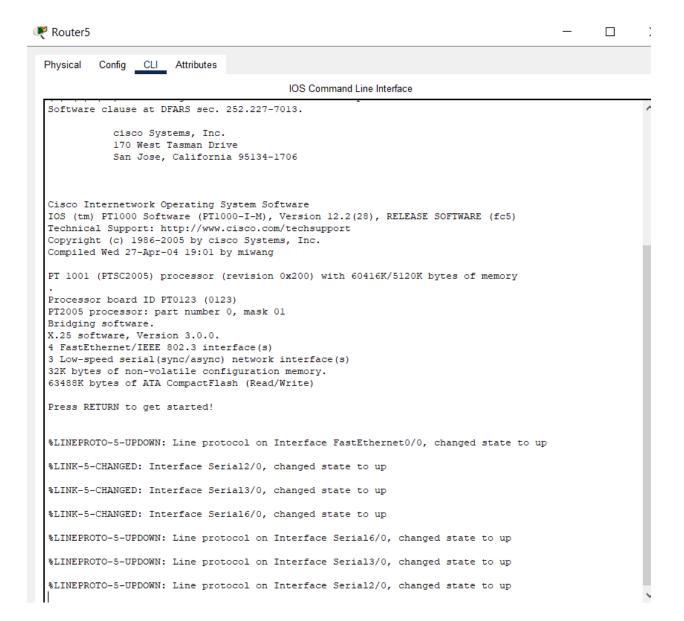


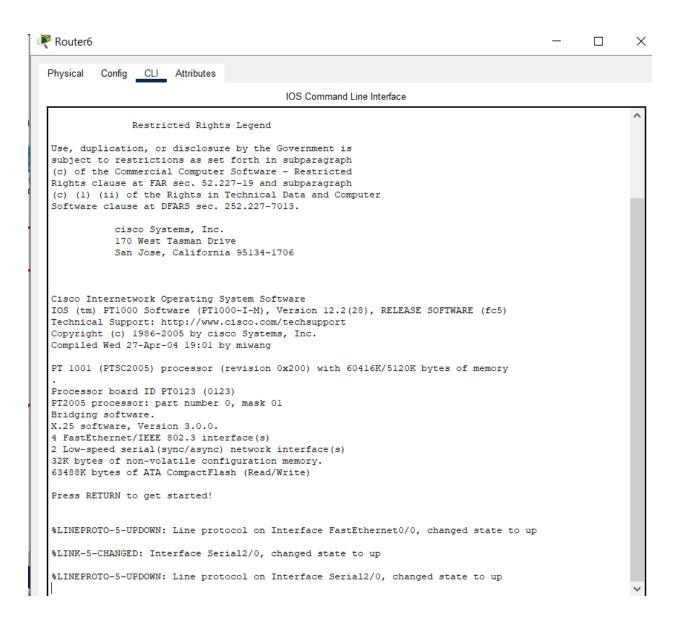




Config CLI Attributes Physical IOS Command Line Interface subject to restrictions as set forth in subparagraph (c) of the Commercial Computer Software - Restricted Rights clause at FAR sec. 52.227-19 and subparagraph (c) (1) (ii) of the Rights in Technical Data and Computer Software clause at DFARS sec. 252.227-7013. cisco Systems, Inc. 170 West Tasman Drive San Jose, California 95134-1706 Cisco Internetwork Operating System Software IOS (tm) PT1000 Software (PT1000-I-M), Version 12.2(28), RELEASE SOFTWARE (fc5) Technical Support: http://www.cisco.com/techsupport Copyright (c) 1986-2005 by cisco Systems, Inc. Compiled Wed 27-Apr-04 19:01 by miwang PT 1001 (PTSC2005) processor (revision 0x200) with 60416K/5120K bytes of memory Processor board ID PT0123 (0123) PT2005 processor: part number 0, mask 01 Bridging software. X.25 software, Version 3.0.0. 4 FastEthernet/IEEE 802.3 interface(s) 2 Low-speed serial(sync/async) network interface(s) 32K bytes of non-volatile configuration memory. 63488K bytes of ATA CompactFlash (Read/Write) Press RETURN to get started! %LINEPROTO-5-UPDOWN: Line protocol on Interface FastEthernet0/0, changed state to up %LINK-5-CHANGED: Interface Serial2/0, changed state to up %LINK-5-CHANGED: Interface Serial3/0, changed state to up %LINEPROTO-5-UPDOWN: Line protocol on Interface Serial3/0, changed state to up %LINEPROTO-5-UPDOWN: Line protocol on Interface Serial2/0, changed state to up



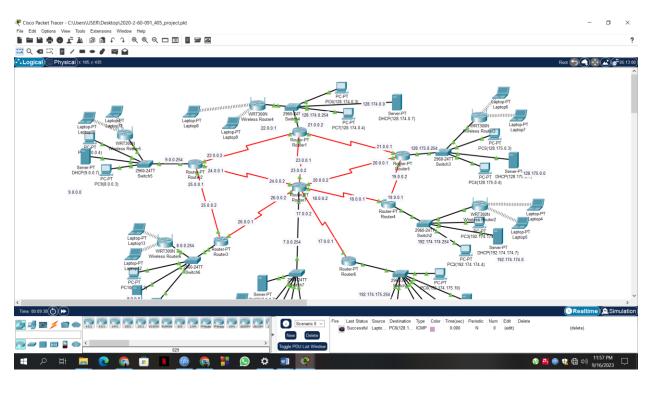


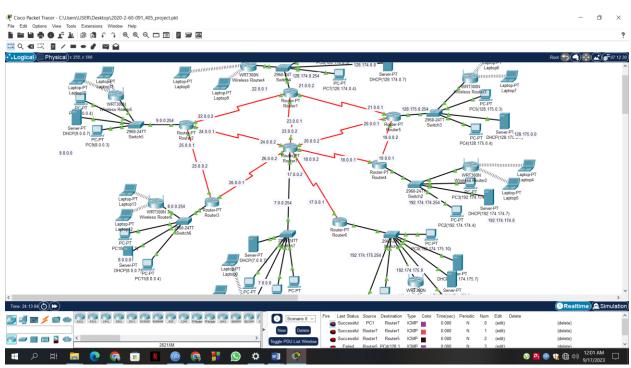




```
PT 1001 (PTSC2005) processor (revision 0x200) with 60416K/5120K bytes of memory
Processor board ID PT0123 (0123)
PT2005 processor: part number 0, mask 01
Bridging software.
X.25 software, Version 3.0.0.
4 FastEthernet/IEEE 802.3 interface(s)
6 Low-speed serial(sync/async) network interface(s)
32K bytes of non-volatile configuration memory.
63488K bytes of ATA CompactFlash (Read/Write)
Press RETURN to get started!
%LINK-5-CHANGED: Interface Serial2/0, changed state to up
%LINK-5-CHANGED: Interface Serial3/0, changed state to up
%LINK-5-CHANGED: Interface Serial6/0, changed state to up
%LINK-5-CHANGED: Interface Serial7/0, changed state to up
%LINEPROTO-5-UPDOWN: Line protocol on Interface FastEthernet0/0, changed state to up
%LINK-5-CHANGED: Interface Serial8/0, changed state to up
%LINK-5-CHANGED: Interface Serial9/0, changed state to up
%LINEPROTO-5-UPDOWN: Line protocol on Interface Serial9/0, changed state to up
%LINEPROTO-5-UPDOWN: Line protocol on Interface Serial2/0, changed state to up
%LINEPROTO-5-UPDOWN: Line protocol on Interface Serial6/0, changed state to up
%LINEPROTO-5-UPDOWN: Line protocol on Interface Serial7/0, changed state to up
%LINEPROTO-5-UPDOWN: Line protocol on Interface Serial8/0, changed state to up
%LINEPROTO-5-UPDOWN: Line protocol on Interface Serial3/0, changed state to up
```

Packet Transfers:





Conclusion:

Despite difficulties, I implemented my plan in accordance with the project description, and try my best to complete this project perfectly. In this project, a complete model of a complex network is designed. End devices, Routers, Switches, and wireless routers were used to create this network. Communication between all devices all over the network was established perfectly