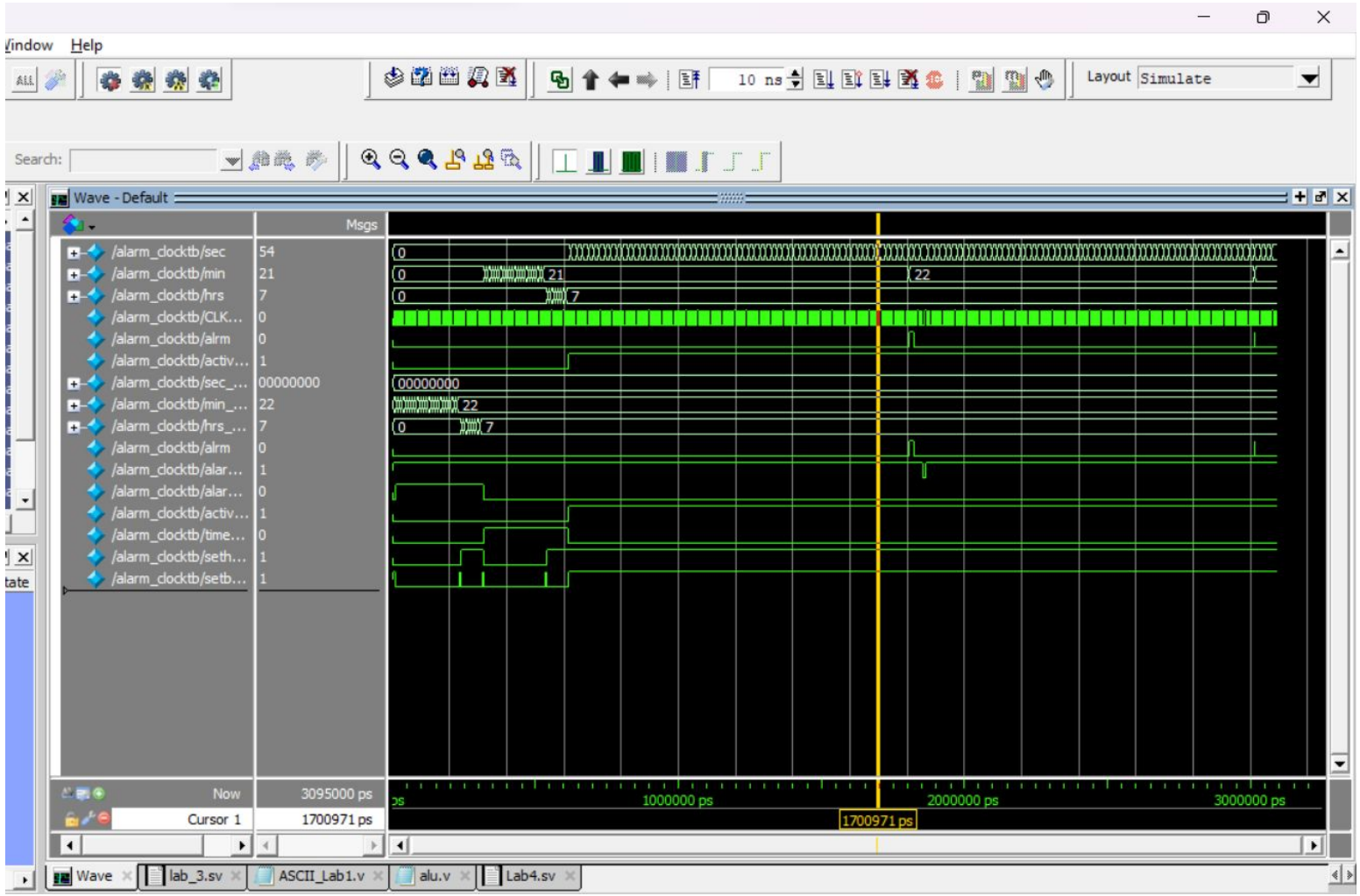
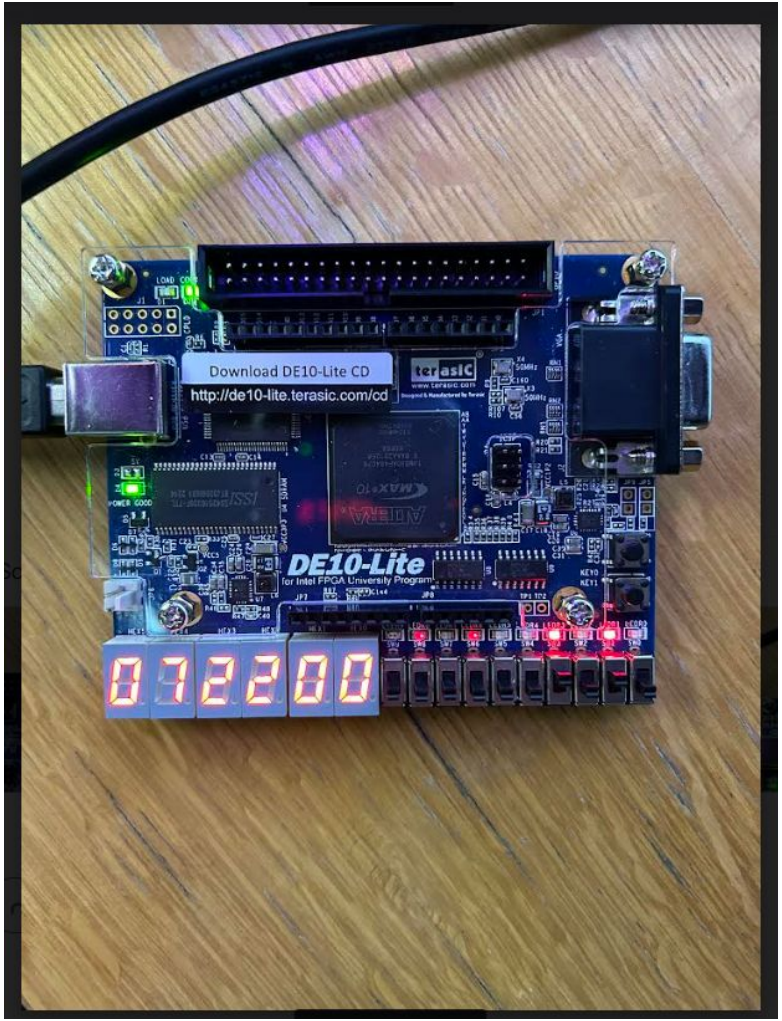
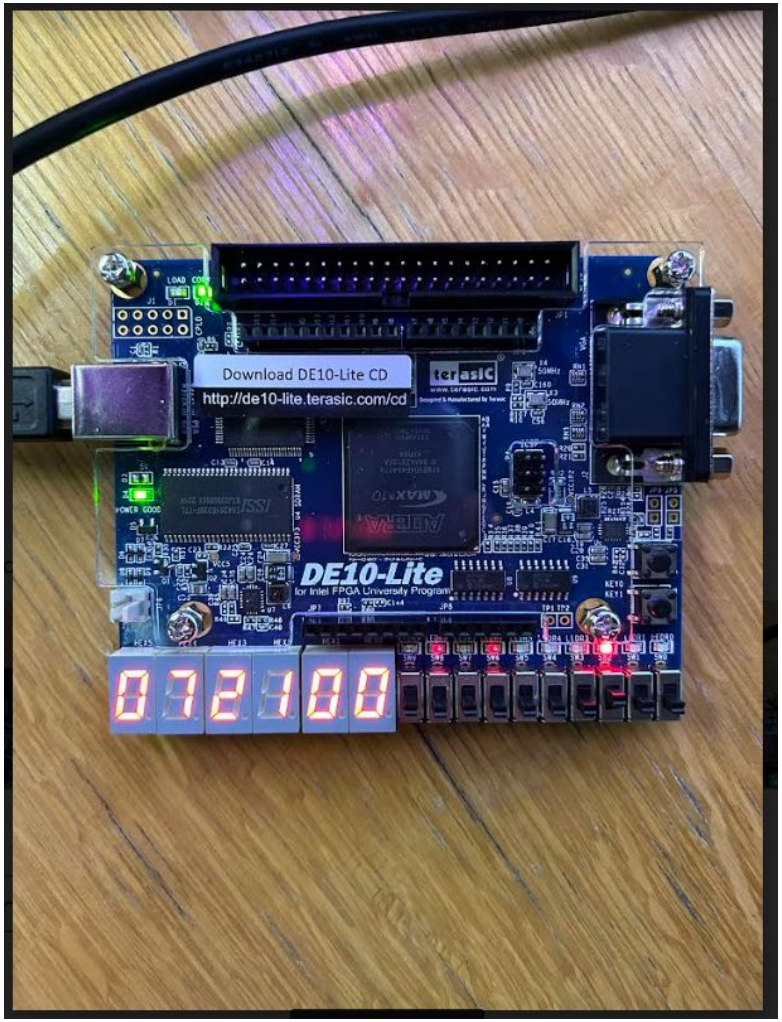
```
VSIM 139> run -all
# Display: 00000000 : 00000000 : 00000000
# Alarm Display:   0 :  22 :   0
# Alarm Display:   7 :  22 :   0
# Time Display:   0 :  21 :   0
# Time Display:   7 :  21 :   0
# Time Display:   7 :  21 :   1, Alarm = 0
# Time Display:   7 :  21 :   1, Alarm = 0
# Time Display:   7 :  21 :   2, Alarm = 0
# Time Display:   7 :  21 :   2, Alarm = 0
# Time Display:   7 :  21 :   3, Alarm = 0
# Time Display:   7 :  21 :   3, Alarm = 0
# Time Display:   7 :  21 :   4, Alarm = 0
```

```
# Time Display:   7 :  21 :  57, Alarm = 0
# Time Display:   7 :  21 :  58, Alarm = 0
# Time Display:   7 :  21 :  58, Alarm = 0
# Time Display:   7 :  21 :  59, Alarm = 0
# Time Display:   7 :  21 :  59, Alarm = 0
# Time Display:   7 :  22 :   0, Alarm = 1
# Time Display:   7 :  22 :   0, Alarm = 1
# Time Display:   7 :  22 :   1, Alarm = 0
# Time Display:   7 :  22 :   1, Alarm = 0
# Time Display:   7 :  22 :   2, Alarm = 0
# Time Display:   7 :  22 :   2, Alarm = 0
# Time Display:   7 :  22 :   2, Alarm = 0
```

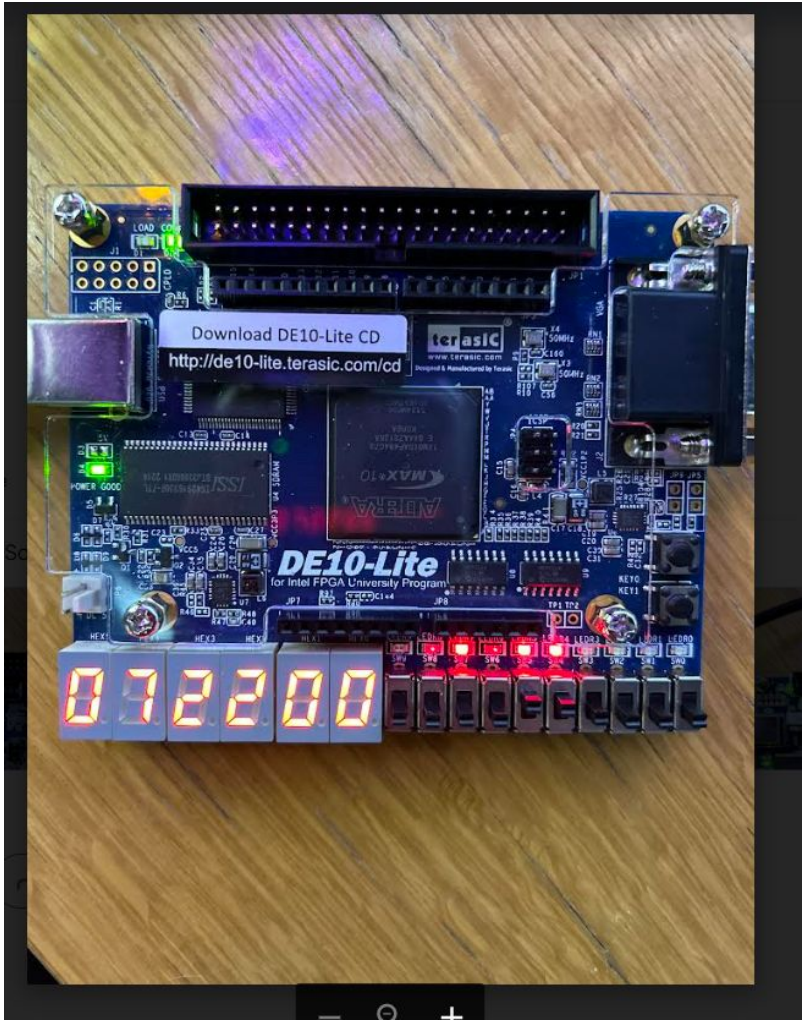

Window Help
Layout Simulate
Search:
Wave - Default
Msgs
/alarm_clocktb/sec
/alarm_clocktb/min
/alarm_clocktb/hrs
/alarm_clocktb/CLK...
/alarm_clocktb/alrm
/alarm_clocktb/activ...
/alarm_clocktb/sec_...
/alarm_clocktb/min_...
/alarm_clocktb/hrs_...
/alarm_clocktb/alrm
/alarm_clocktb/alar...
/alarm_clocktb/alar...
/alarm_clocktb/activ...
/alarm_clocktb/time...
/alarm_clocktb/seth...
/alarm_clocktb/setb...
54
21
7
0
0
1
00000000
22
7
0
1
0
1
0
0
1
1
Now
3095000 ps
Cursor 1
1700971 ps
1000000 ps
2000000 ps
3000000 ps
1700971 ps
Wave
lab_3.sv
ASCII_Lab1.v
alu.v
Lab4.sv


Download DE10-Lite CD
http://de10-lite.terasic.com/cd
terasIC
DE10-Lite
for Intel FPGA University Program
072200

Alarm set


Download DE10-Lite CD
http://de10-lite.terasic.com/cd
terasIC
DE10-Lite
for Intel FPGA University Program
072100

Time set



Alarm going off

Download DE10-Lite CD
http://de10-lite.terasic.com/cd
terasIC
DE10-Lite
for Intel FPGA University Program
07 22 19

Alarm reset