

LA from
CPU

Segment Table

Limit	Ba. Ad. of PMT for S

Main Memory

O/S

PMT for S

Frame# for p'

Paged-Segmented MMT

IITI-cs303-Autumn-2014

LA from
CPU



S	O
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Segment Table

Limit	Ba. Ad. of PMT for S

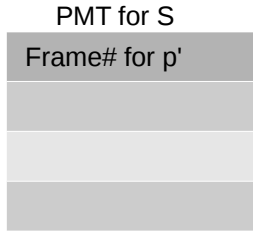
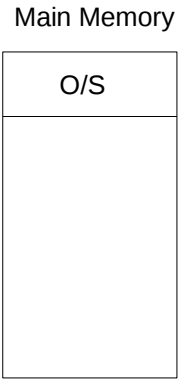
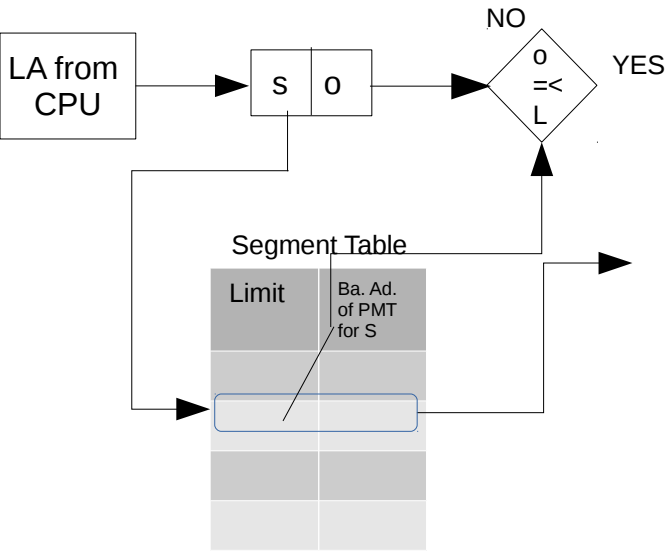
Main Memory

O/S

PMT for S

Frame# for p'

Paged-Segmented MMT



Trap and
Terminate

NO

YES

0
=<
L

Main Memory

O/S

Segment Table

Limit

Ba. Ad.
of PMT
for S

PMT for S

Frame# for p'

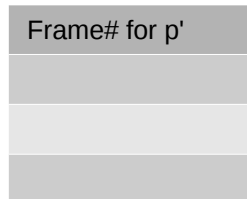
Paged-Segmented MMT

IITI-cs303-Autumn-2014

LA from
CPU

S O

Limit	Ba. Ad. of PMT for S



Trap and
Terminate

NO

YES

0
=<
L

0

Main Memory

O/S

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Limit

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LA from
CPU

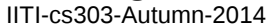
S

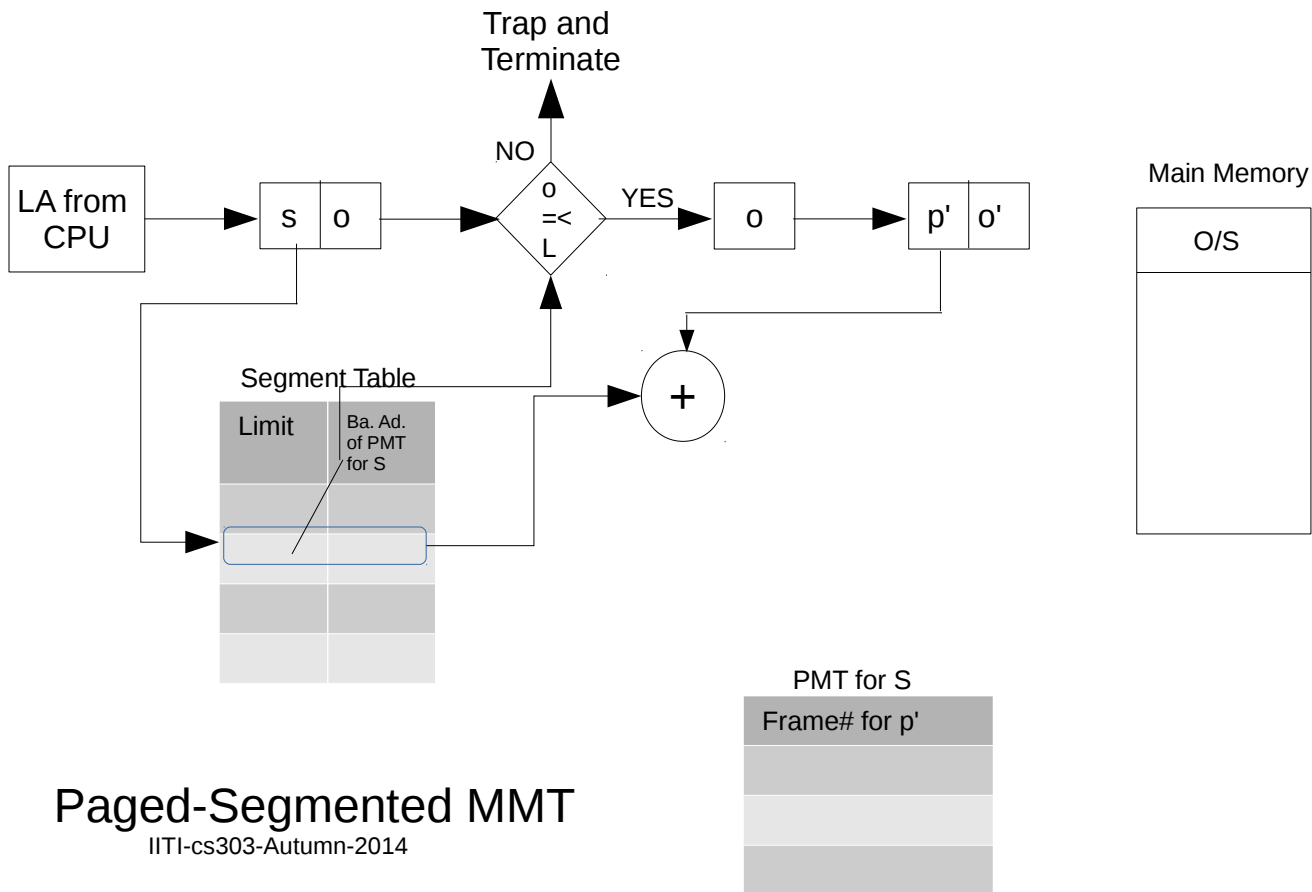
O

Limit	Ba. Ad. of PMT for S

O/S

Frame# for p'



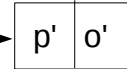


Trap and
Terminate

NO

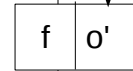
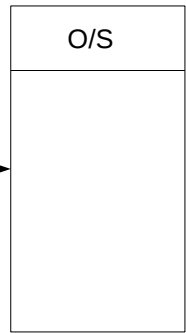
YES

$0 \leq L$



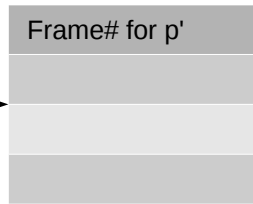
Main Memory

O/S

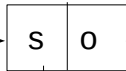
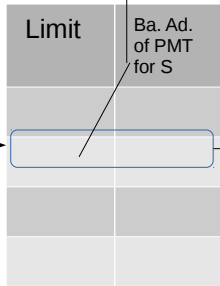


PMT for S

Frame# for p'



Segment Table



LA from
CPU

Paged-Segmented MMT

IITI-cs303-Autumn-2014

Memory Management: Demand Paging and Page Replacement Algorithms – FIFO and LRU

9Nov2018
Autmun 2018
CS303

Demand Paging Framework

