

Considerations:

- 2.Current Capacity: Verify that the GPIO pins can handle the current required by the motor driver's input. If higher currents are needed, consider using a transistor or a driver chip to interface.
- 3.Check SPI interface 35,36,37 specially SDO connection to V3V6
- 4.Pin Optimisation needed

Verify if V3v3 and V3v6, entry path is valid

Malevolent MOSFET

Sheet: /ESP32/

File: ESP32.kicad_sch

Title: ESP32 ESC Dev. Board

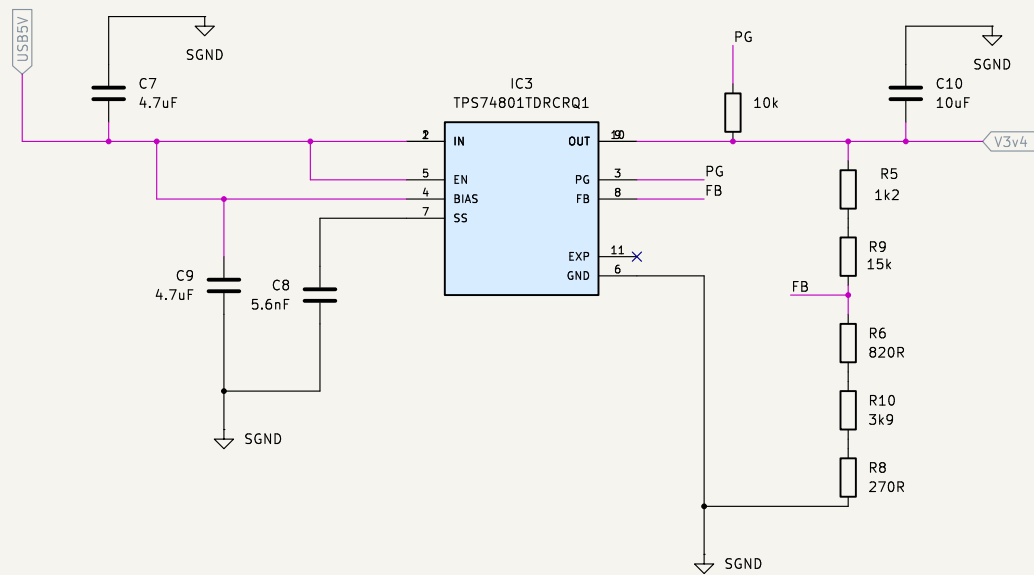
Size: A2

Date: 2024-06-23

Rev: Rel 0.91

KiCad E.D.A. B.0.3

Id: 2/7



Does this design really need PG?

C8=Css, 5.6nF is 10ms of Soft Start Delay Check if OK for downstream

30th March: Set Output Resistors to 3.3V based on DS

Check EXP Pin

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Sheet: /LDO/

File: LDO.kicad_sch

Title: ESP32 ESC Dev. Board

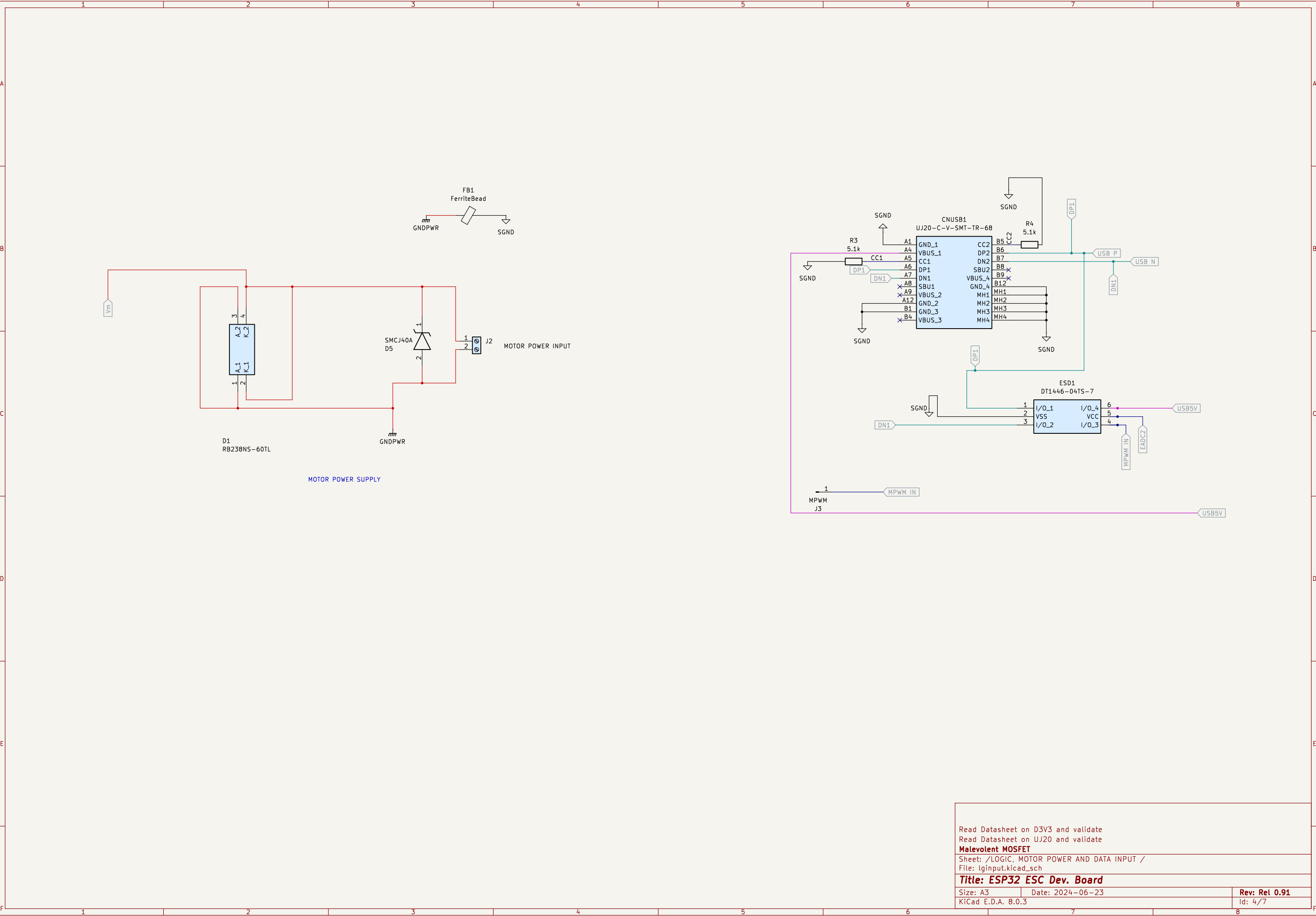
Size: A4

Date: 2024-06-23

Rev: Rel 0.91

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Id: 3/7



Read Datasheet on D3V3 and validate
Read Datasheet on UJ20 and validate

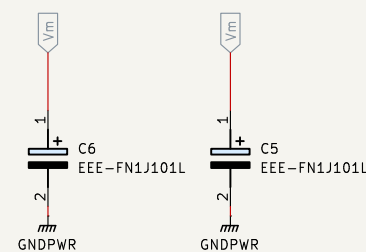
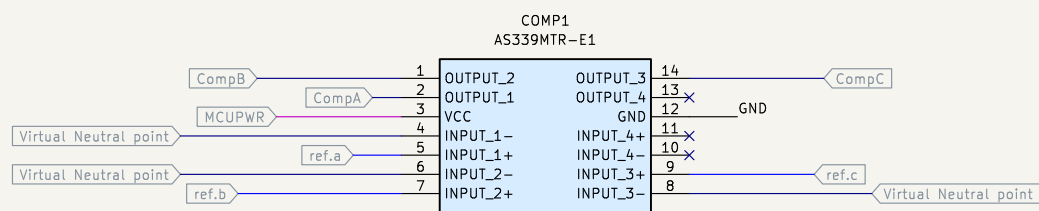
Malevolent MOSFET

Sheet: /LOGIC, MOTOR POWER AND DATA INPUT /
File: lgInput.kicad_sch

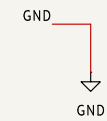
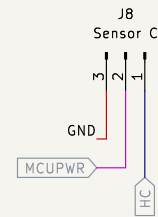
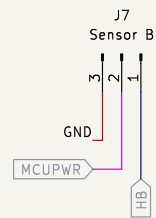
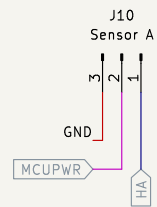
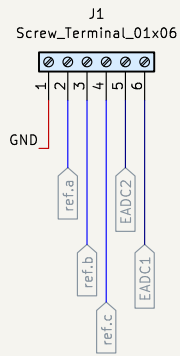
Title: ESP32 ESC Dev. Board

Size: A3
Date: 2024-06-23

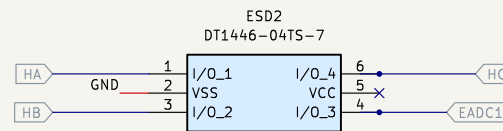
Rev: Rel 0.91
Id: 4/7



Id: 6/7



Check if power supply strat is OK



Malevolent MOSFET

Sheet: /Sensored Control & Peripheral/
File: hallio.kicad_sch

Title: ESP32 ESC Dev. Board

Size: A4

Date: 2024-06-23

Rev: Rel 0.91

KiCad E.D.A. 8.0.3

Id: 7/7