

1) Design the logical circuit for the equation below, using 8:1 MUX.

$$Y = \overline{A} \overline{B} \overline{C} \overline{D} \overline{E} + \overline{A} \overline{B} \overline{C} D \overline{E} + \overline{A} B \overline{C} \overline{D} E + \overline{A} \overline{B} C D \overline{E} + A \overline{B} \overline{C} \overline{D} \overline{E} + A \overline{B} C D E + \overline{A} B C \overline{D} E + A B \overline{C} E + A B C D$$

