

1) Design the logical circuit for the equation below, using 8:1 MUX.

$$Y = \overline{A}\overline{B}\overline{C}\overline{D}\overline{E} + \overline{A}\overline{B}\overline{C}D\overline{E} + \overline{A}\overline{B}\overline{C}DE + \overline{A}\overline{B}C\overline{D}\overline{E} + \overline{A}\overline{B}C\overline{D}E + \overline{A}\overline{B}CDE + \overline{A}BC\overline{D}\overline{E} + AB\overline{C}E + ABCD$$

A _____

B _____

C _____

D _____

E _____

