

Name:.....Student ID ..... Sent by 30/1/2025

If we have a plenty of 8bit shift registers with a clock of 1kHz, and the starting data is  $00000001_2$ .

Let  $d$  = last two numbers of your student i.d. Please design a circuit that can give a time delay in a range between  $d$  and  $10d$  seconds.

